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## COVER 1 <br> Have you decided that the only way to make the video movies you want is to buy a portable VCR and camera? Well, think again! Our wireless video link will let you <br> 

 transmit video signals from your camera to your VCR over one of the UHF channels! It just might be the ultimate in portability!Making moves isn't all that our video link will let you do. You can also use it to transmit signals fromyour VCR in one room to a TV in any other room in the house-all with wireless convenience.
Our thanks to Magnavox for providing the LoLite video camera that our model is using with the wireless video link.

## NExT Month

## THE MARCH ISSUE IS ON SALE FEBRUARY 4

## SPECIAL SECTION ON ROBOTICS

Two special articles will bring you up to date on the robotics industry. The first is a buyer's guide to the market. The second tells you what's involved in building your own robot!

## BUILD A DELUXE VIDEO TITLER

Due to space restrictions, we were unable to run the final installment this month. In March, we'll finish things up with a look at the software and how to interface the titler to a computer.

## BUILD AN AMPLIFIER FOR YOUR WALKMAN

Who says you have to listen to your personal stereo through headphones?

## HOW TO AVOID HOT PROJECTS

The second and final installment gives more hints and design considerations.

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Advertising Sales Offices listed on page 138.

# NEW！ uniden Bearcat <br> \author{  

}

## Scanners

Communications Electronics， the world＇s largest distributor of radio scanners，introduces new scanners and scanner accessories from J．I．L． Regency and also Uniden／Bearcat． Chances are the police，fire and weather emergencies you＇ll read about in tomorrow＇s paper are coming through on a scanner today．

## NEW！Regency？MX7000－K

List price $\$ 699.95 /$ CE price $\$ 429.00 /$ SPECIAL 10－Band， 20 Channel e Crystalless • AC／DC Frequency range： $25-550 \mathrm{MHz}$ continuous coverage and 800 MHz ．to 1.2 GHz ．continuous coverage In addition to normal scanner listening，the MX7000 offers CB，VHF，and UHF TV audio，FM Broadcast，all aircraft bands（civil and military）， 800 MHz communications，cellular telephone， and when connected to a printer or CRT，satellite weather pictures．

## NEW！Regency ${ }^{0}$ MX5000－K List price $\$ 599.95 /$ CE price $\$ 329.00$ SPECIAL

 Multr－Band， 20 Channel • No－crystal scanner Multi－Band， 20 Channel © No－crystal scSearch o Lockout o Priority © AC／DC Selectable AM－FM modes e LCD display World＇s first continuous coverage scanner Frequency range： $25-550 \mathrm{MHz}$ continuous coverage． Never before have so many features come in suich a small package．The Regency MX5000 mobile or home scanner has continuous cove age from 25 to 550 MHz ．That means you can hear CB，Television audio，FM broadcast sta－ tions，all aircraft bands including military and the normal scanner bands，all on your choice of 20 programmable channels．

## NEW！Regency ${ }^{\text {® }}$ MX4000－K

List price $\$ 629.95 /$ CE price $\$ 299.00 /$ SPECIAL search－Lockout © Priority 0 AC／DC Solectable AM－FM modios．LCD display Selectable AM－FM modes e LCD display
Bands： $30-50,118-136,144-174,440-512,800-950 \mathrm{MHz}$ The Regency MX4000 is gives coverage in the standard VHF and UHF ranges with the impor－ tant addition of the 800 MHz ．and aircraft bands． It features keyboard entry，multifunction liquid crystal display and variable search increments．

## NEW！Regency ${ }^{\circ}$ Z60－K

List price $\$ 379.95 /$ CE price $\$ 216.00 /$ SPECIAL 8－Band， 60 Channel－No－crystal scanner Bands： $30-50,88-108,118-136,144-174,440-512 \mathrm{MHz}$ Cover your choice of over 15,000 frequencies on 60 channels at the touch of your finger．


## NEW！JIL SX－400－K

List price $\$ 799.95 /$ CE price $\$ 469.00 /$ SPECIAL Multr－Band， 20 Channel e No－crystalScanner Search • Lockout • Priority • AC／DC With optionally equipped RF converiers $150 \mathrm{KHz}-3.7 \mathrm{GHz}$ ． The JIL SX－400 synthesized scanner is designed for The JIL SX－400 synthesized Scanner is designed for commercial and professional monitor users that de－ mand eatures notfound in ordinary scanners．The 400 will cover from 150 KHz to 3.7 GHz ，with R converters．Order the following RF converters for your SX－400 scanner．RF－1030－K at \＄259．00 each fo frequency range $150 \mathrm{KHz},-30 \mathrm{MHz}$ ．USB，LSB，CW and AM．（CW filter required for CW signal reception）；RF－
$5080-\mathrm{K}$ at $\$ 199.00$ each for $500-800 \mathrm{MHz}$ RF－8014 5080－K at \＄199．00 each for $500-800 \mathrm{MHz}$ ．；RF－8014－K at $\$ 199.00$ each for $800 \mathrm{MHz} .-1.4 \mathrm{GHz}$ ．Be sure to also order ACB－300－K at $\$ 99.00$ each which is an antenna control box for connection of the RF converters． The RC－4000－K data interface at $\$ 259.00$ each gives you control of the SX－400 scanner and RF converters through a computer．Add $\$ 3.00$ shipping for each RF converter，data interface or antenna control box．If you need further information on the JIL scanners，contact JIL directly at 213－926－6727 or write JIL at 17120 Edwards Road，Cerritos，California 90701 U．S．A．

## SPECIAL！JIL SX－200－K

## List price \＄499．95／CE special price \＄159．00

 Multi－Band－16 Channel © No－Crystal Scanner Frequency range 26－88， $108 \cdot 180$ ， $380-514 \mathrm{MHz}$The JIL SX－200 scanner tunes military，F．B．I．，Space Satellites，Police and Fire，Drug Enforcement Agencies， Defense Department，Aeronautical AM band，Aero Navigation Band，Fish \＆Game，Immigration，Paramedics， Amateur Radio，Justice Department，State Department， plus other thousands of radio frequencies most other scanners can＇t pick up．The SX－200 has selectable AM／FM receiver circuits，tri－switch squelch setting signal，audio and signal \＆audio，outboard AC power supply－DC at 12 volts built－in，quartz clock－bright vacuum fluorescent blue readouts and dimmer，dual level search speeds，tri－level scan delay switches， 16 level search speeds，tri－level scan delay switches， 16 tune（RIT）$\pm 2 \mathrm{KHz}$ ，dual level RF gain settings -20 db tune AGC test points for optional signal strength meters

## Regency ${ }^{\ominus}$ HX1000－K

## List price S329．95／CE price s209．00

6－Band， 30 Channel • No Crystal scanner Search e Lockout－Priority e Scan delay Sidelit liquid crystal display o Digital Clock Frequency range： $30-50,144-174,440-512 \mathrm{MHz}$ ． The new handheld Hegency HX1000 scanner is fully keyboard programmable for the ultimate in versatil－ ity．You can scan up to 30 channels at the same time． ity．You can scan up to 30 channels at the same time． ically override all other calls to listen to your favorite frequency．The LCD display is even sidelit for night use．Order MA－256－K rapid charge drop－in battery charger for $\$ 79.00$ plus $\$ 3.00$ shipping／handling． Includes wall charger，carrying case，belt clip． flexible antenna and nicad battery．Order now

## NEW！Bearcat ${ }^{\circ}$ 100XL－K

## List price $\$ 349.95 / \mathrm{CE}$ price s229．00

 9－Band， 16 Channel © Priority－Scan Delay Search • Limit e Hold e Lockout © AC／DC Frequency range： $30-50,118-174,406-512 \mathrm{MHz}$ ． The world＇s first no－crystal handheld scanner now has a LCD channel display with backlight for low light use and aircraft band coverage at the same low price．Size is $1.36^{\prime \prime} \times 71 / 2^{\prime \prime} \times 27 / 6^{\prime \prime}$ ．The Bearcat 100 XL has wide frequency coverage that includes all public service bands（Low， High，UHF and＂T＂bands），the AM aircraft band，the 2 － meter and 70 cm ．amateur bands，plus military and federal government frequencies．Wow．．．what a scanner！ Included in our low CE price is a sturdy carrying case， earphone，battery charger／AC adapter，six AA ni－cad batteries and flexible antenna．Order your scanner now．NEW！Regency ${ }^{\circ}$ HX2000－K The World＇s First 800 MHz．Handheld Scanner List price $\$ 569.95 /$ CE price $\$ 359.00$
7－Band， 20 Channel－No－crystal scanner Priority control－Search／Scan • AC／DC Sidelit liquid crystal clisplay e Memory backup Bands：118－136，144－174，440－512， $800-950 \mathrm{MHz}$ ． The HX2000 scanner operates on 120 V AC or 6 VDC ． Scans 15 channels per second．Size $3^{\prime \prime} \times 7^{\prime \prime} \times 11 /$ ．$^{\prime \prime}$ Includes wall charger，carrying case，belt clip，flexible antenna and nicad batteries．Selectable AM／FM modes．

NEW！Bearcat ${ }^{\ominus}$ 800XLT－K List price $\$ 499.95 /$ CE price $\$ 329.00$ 12－Band， 40 Channel－No－crystal scanner Priority control－Search／Scan e AC／DC Bands： $29-54,118-174,406-512,806-912 \mathrm{MHz}$ The Uniden $800 \times$ LT receives 40 channels in two banks Scans 15 channels per second．Size $914^{\prime \prime} \times 41 / 2^{\prime \prime} \times 121 / 2$ ．

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## Panasonic RF－B300－K Shortwave receiver

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PC22－K Uniden remote mount CB transceive PC5SK Uniden 10 cie mount CB tran Z10－K Regency 30 channel scanner Z45－K Regency 45 channel scanner R1060－K Regency 10 channel scanner MX3000－K Regency 30 channel scanner C403－K Regency 4 channel scanner R106－K Regency 10 channel scanner RH250B－KRegency 10 channel VHF transceiver RU150B－KRegency 10 channel UHF transceiver RPH410－K 10 ch ．handheld no－crystal transciever BC10－K Battery charger for Regency RPH410 MA257－K Cigarette lighter cord for HX1000 MA917－K Ni－Cad battery pack for HX1000 EC10－K Programming tool for Regency RPH4 10 SMRH250－K Service man．for Regency RH250 SMRU150－K Service man，for Regency RU150 SMRPH410－K Service man．for Regency RPH410 SMM X7000－K Svc．man．for MX7000\＆M X5000 SMM X3000－K Service man．for Regency MX3000 B－4－K 1.2 V AAA Ni－Cad batteries（set of foul） A－135C－K Crystal certificate
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CIE－K Covert Intelligenct，Elect．Eavesdropping A60－K Magnet mount mobile scanner antenna A70－K Base station scanner antenna USAMM－K Mag mount VHF／UHF ant．w／ 12 cable USAK－K $3 / 4$＂hole mount VHF／UHF ant．w／ 17 ＇cable． USATLM－K Trunk lip mount VHF／UHF antenna． Add $\$ 3.00$ shipping for all accessories ordered at Add $\$ 12.00$ shipping per shortwave receiver．

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# EDITORIAL 

## Radio-Electronics is on the move!

Radio-Electronics is moving! Yes, we're packing our bags and getting set to move out of New York City. We're not taking the move lightlywe've been at our present location for about seventeen years. And we have to admit, we like our Park Avenue South offices...but not as much as we're going to like our new place.

Being thirty miles east of Manhattan does have its advantages. Our new Long Island offices will give us room to think and room to grow.

Don't think that because we're leaving the Big Apple that we'll slow down to an easier pace. We'll be moving faster than ever to keep you up to date and informed. Any changes we make will be for the better. Next month, for example, you'll see a special section on robotics, and a new column devoted to new technology. We intend to stay on the cutting edge-and to keep you there, too.

We also plan to become more responsive to your needs. We started our new Ask $R$ - $E$ column to answer your questions. Of course we can't answer everybody, but we'll answer the questions that will be of interest to the largest number of readers. If we can't answer your question, we'll try to steer you to someone who will.

I see that I've strayed from the subject I intended to write about. But that's because I'm more excited about being the new Managing Editor than I am about moving. But I'd be even more excited to hear from you. I want to hear your honest opinion about Radio-Electronics. Just write a postcard or short letter and let me know what you think is the best article or column in this issue. Let me know what you think is the worst, too. While you're at it, tell me something about yourself.

I guess I better tell you our new address. It's:

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Model V-422 shown

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## MODEL V-422

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DC to 40 MHz , other features same as V -222 (w/two X1/X10 probe).

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# What's News 

## Hugo Gernsback honored by antique radio group

The Antique Wireless Association (AWA) declared 1985 "Gernsback Year," and devoted three categories of its annual Old Equipment contest to Electro Importing Co. items, and documentation of Gernsback publications.

The best exhibit, by AWA President Lauren Peckham, was a display of old Electro Importing Co. (EICo) equipment, which included some items rarely seen even by avid collectors. Rarest was a Gernsback Rotary Variable Condenser. Using a patented roller principle, with the foil and dielectric rolling off one roller and onto another, it had a maximum capacitance of $.01 \mu \mathrm{~F}$. Another exhibit, by Robert Trauterman of Ford City, PA, featured copies of a large number of early Gernsback magazines, plus a collection of his most famous editorials.

Feature of the AWA Annual Banquet was an interview by AWA expresident Charles Brelsford of Fred Shunaman, long-time managing editor of Radio-Craft and Ra-dio-Electronics, on Hugo Gernsback as an employer, as an individual, and as an influence on the radio art and the youth of his generation.
The conference was held at Canandaigua, NY, on September 25-29, 1985. Attendance was over 650. Sales at the AWA members' auctions totalled $\$ 18,300$, ten per cent of which goes to the Association. During the Conference eight papers were presented and there were three auctions and an amateur seminar.

## Discharge rate up 10 times in new solid-state battery

Eveready has just announced


THE EVEREADY 2-VOLT SOLID-STATE CELL
the development of a new battery with a discharge rate ten times greater than that of any earlier sol-id-state batteries. The new 2 -volt lithium cell also operates over a wider temperature range than conventional solid-state cells, and with no loss in shelf life.

The high discharge rate is the result of a patented Isostatic Compression Process. The electroactive materials are assembled in an argon atmosphere, then heatsealed in a plastic bag and compressed at 80,000 pounds to the square inch in a commercial isostatic press, which applies the compression equally in all directions, using water/water-soluble oil as the pressure transmitting fluid. "The resulting intimate parti-cle-to-particle contact thus achieved reduces the internal battery resistance to enable the higher rate of discharge," says a company spokesman.

The high-temperature performance is possible because the battery itself contains no liquid-it uses vitreous solid electrolytes.

It is expected that samples of the new battery will be available before the end of 1985.

## "Fuzzy logic" can make for better military decisions

A new approach being followed by General Electric scientists may enable military field commanders
to make fast, accurate decisions based on quantities of incomplete and/or conflicting reports.

The new approach includes "fuzzy logic." A conventional computer program must have complete and accurate information to make correct decisions. A "fuzzy logic" program resembles more closely the workings of an intelligent human mind-it can make sense of such relative terms as "almost" or "probably."

The new program belongs to the class of "expert systems" that have been developed to simulate a human expert. They are founded on a "knowledge base" which combines all the information obtainable on a given subject-not from one, but from many experts. When confronted with a problem, the computer searches the knowledge base, selects pertinent facts, and applies rules (conditional statements) to them to define the problem. The main difference between that expert system and earlier ones is the use of the fuzzy logic that makes it possible to make decisions even if not all the facts are in place.

Besides that "probabilistic reasoning," the new system has two other parallel approaches: a "dynamic truth-maintenance system" that enables the computer to "think" through various hypotheses and weed out the ones that are not applicable, and a method of reasoning by analogy-of weighing similarities and differences of a current situation compared with a previous one.

The development program is being funded by a two-year, $\$ 1$ million contract from the Defense Advanced Research Projects Agency (DARPA) of the U.S. Department of Defense.

R-E

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# Ask R-E 

## BUILDING A JOYSTICK

I'm building a robot, and I want to control it with a joystick or ball-type control like the ones used on many video games. I'd like to build my own. Can you help?-H. M., Palmetto, SC.

From your question, we'll assume that you are most interested in the analog types. Those offer a greater degree of control than the switch-closure type (the latter are used primarily by some types of video games), and are simple electrically, but mechanical construction requires a little skill and ingenuity. They are designed so that their motion increases or decreases the resistance of one or


FIG. 1
more potentiometers. Which potentiometers are affected is determined by the direction of the motion.

The mechanical design of a simple joystick is illustrated in Fig. 1. That design originally appeared in the British magazine Wireless World. The potentiometer used for side-to-side motion is fixed to


FIG. 2

the chassis or control box. The joystick is fastened to the body of the second potentiometer by its

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mounting nut. The shafts of the two potentiometers are drilled and fastened together by a small nut and bolt.

A more elaborate design, from the Argentinian magazine Revista Telegrafica Electronica, appears in Fig. 2. A total of four potentiometers are used to vary resistance in two circuits with side-to-side motion, and in two other circuits with fore-and-aft motion.
We can't help you with the construction of a trackball, but you might be able to substitute one of the thumb-operated designs shown in Fig. 3. Those designs are from Radio (Czechoslovakia).

Take care in selecting the potentiometers. Most have a rotation of about 270 degrees, but joysticks usually limit motion to about 45 degrees from the neutral or center position. Naturally, you'll want to use linear potentiometers so that equal deviations from the "neutral" position produce equal changes in resistance.

## FREQUENCY AND PERIOD

I know what a sinewave is, but I don't understand the difference between frequency and period. How are they related?-N. J. S., Greenboro, NC.

Frequency is the number of cycles that occur per unit of time. A complete cycle is measured between two successive points on the waveform that have the same amplitude and direction. For example: The time between two successive positive peaks, or between two successive positive-going zero crossings, constitutes one cycle. Until about 20 years ago, cycles per second was the unit in which frequency was specified.

But that term has been almost

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universally replaced by hertz-a unit equal to one cycle per second. We said almost universally because, while most of us use hertz, kilohertz, megahertz, etc., in connection with RF, some still refer to the $60-$ cycle power line, etc.

Whereas frequency refers to the number of events in a given period of time, the period of a signal is simply the amount of time required for one complete event to occur. The symbol for period is $T$, which represents time. Frequency
and period are reciprocals, so the higher the frequency, the shorter the period, and the longer the period, the lower the frequency. Frequency and period are related by these formulas:

$$
\begin{aligned}
& T(\mathrm{sec})=1 \div f(\mathrm{~Hz}) \\
& f(\mathrm{~Hz})=1 \div T(\mathrm{sec})
\end{aligned}
$$

For example, a signal with a frequency of 20 kHz has a period of $1 / 20,000$, or 0.00005 sec . A signal with a period of $12.5 \mu \mathrm{~s}$ has a frequency of $1 / 0.0000125$, or 80 kHz .

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WHAT ARE THOSE BANDS? A friend claims that his police-radar detector is superior to mine because his operates on the K-band, while mine operates on the X-band. What's the story on those bands? Where did they get their names, and what frequencies do they cover?-E. R., Bennetsville, SC.
During World War II, the Allies used various frequencies in the UHF and microwave bands for a variety of secret military applications. For secrecy, each band was identified by a letter. Some of those bands, and the frequencies and wavelengths associated with them, are shown in Table 1.
For years, police radar operated on the S-band. Equipment was expensive, bulky, and heavy (about 250 pounds). Usually, it was installed in a van, a truck, or a specially equipped automobile.
Later, radar was moved to the X-
TABLE 1
VHF AND MICROWAVE BANDS

| Band | Frequency | Wavelength |
| :--- | :--- | :--- |
| P | $225-390 \mathrm{MHz}$ | $133.3-76.9 \mathrm{~cm}$ |
| L | $390-1500 \mathrm{MHz}$ | $76.9-19.3 \mathrm{~cm}$ |
| S | $1.5-5.2 \mathrm{GHz}$ | $19.3-5.7 \mathrm{~cm}$ |
| C | $3.9-6.2 \mathrm{GHz}$ | $7.69-4.84 \mathrm{~cm}$ |
| X | $5.2-10.9 \mathrm{GHz}$ | $5.77-2.75 \mathrm{~cm}$ |
| K | $10.9-36 \mathrm{GHz}$ | $2.75-0.834 \mathrm{~cm}$ |
| Q | $36-46 \mathrm{GHz}$ | $0.834-0.652 \mathrm{~cm}$ |
| V | $46-56 \mathrm{GHz}$ | $0.652-0.365 \mathrm{~cm}$ |

band. The higher frequency made less expensive, more compact equipment possible. Radar units could be mounted on a tripod, or clamped to the window or rain gutter of a patrol car parked beside the road. Later X-band devices were hand-held. In either case, Xband radar had to be operated from a stationary position; it cannot be used if the operator is in motion.
The next advancement was the development of K-band equipment, which allows the operator to make speed measurements while he is either stationary or in motion. Also, it can be used to measure the speed of both oncoming vehicles and vehicles traveling in the same direction as the operator. Thus, K-band equipment has the advantages of low cost, light weight, and maximum versatility. Because of those reasons, it is used by many city, county, and state police departments. R-E

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# LETTERS 

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## PLYWOOD SATELLITE-TV DISH

The article, "A Plywood SatelliteTV Dish," by David J. Sweetnam, which appeared in the October 1985 issue of Radio-Electronics, contains several serious errors that affect the design and gain computation of the lens.
The equation for the $n$th zone radius that is presented as $r_{n}^{2}=$ $n d \lambda$ should be $r_{n}{ }^{2}=n d \lambda+$ $0.25 n^{2} \lambda^{2}$. In both equations, d is the focal length and $\lambda$ is the wavelength. See p. 337 of Introduction to Electricity and Optics, by N. H. Frank (published by McGraw Hill),
for the derivation of the equation.
Rearranging things we see that d $=\left(r_{n}{ }^{2} / \mathrm{n} \lambda\right)-0.25 \mathrm{n} \lambda$, rather than what was stated in the article, $\mathrm{d}=$ $\mathrm{r}_{\mathrm{n}}{ }^{2 /(n \lambda)}$. The only explanation I can think of for the difference is that an approximation was used. That approximation is valid for lightwhere $\lambda$ is much smaller than dbut not for microwaves.
The amplitude of the signal from each zone is proportional to the zone area (which is not the same for each zone if the radii are computed properly) and inversely proportional to the square of the
distance from the zone to the focal point, or:

$$
\begin{gathered}
\frac{A_{n}}{A_{1}}=\left(\frac{d}{d+\left(\frac{2 n-1}{4}\right) \lambda}\right)^{2}\left(\frac{1+\left(\frac{2 n-1}{4}\right)\left(\frac{\lambda}{d}\right)}{1+(\lambda / d)}\right) \\
=\frac{1}{\left(1+\left(\frac{2 n-1}{4}\right) \frac{\lambda}{d}\right)\left(1+\frac{\lambda}{d}\right)}
\end{gathered}
$$

It is not true that "gain varies without regard to the diameter of the lens." Given a wavelength and a lens diameter, there is an optimum number of zones. That number may be determined by computing the gain for an increas-
continued on page 22

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## LETTERS

continued from page 16
ing number (by two) of zones until a maximum gain is achieved. The proper technique is, given the diameter, $D$, and the wavelength, $\lambda$, assume $n$ zones, and then compute the focal length:

$$
d=\left((D / 2)^{2} / n \lambda\right)-0.25 n \lambda .
$$

Note that $r_{n}=D / 2$ in the same units. Then $r_{i}$ may be computed as $r_{i}=i d \lambda+0.25 i^{2} \lambda^{2}$ for $i=1$ to $n-1$. Gain may then be calculated as $G$ $=20 \log _{10} \Sigma A_{i}$ for $i=1$ to $n$.

Gain calculated thus is considerably less than that claimed by the article, as is the focal length. For an 8 -foot lens with $\lambda=8.108 \mathrm{~cm}$ and $\mathrm{n}=20, \mathrm{~d}$ should be 51.12 cm and the gain should be 21.9 dB , rather than 91.83 cm and 26.0 dB .

An 8 -foot lens for $\lambda=8.108 \mathrm{~cm}$ and $d=91.83 \mathrm{~cm}$, with zones arranged as suggested in the article, will result in a gain of only 9.1 dB . The reason for that low gain is that the zones do not properly match the phase distribution of the incoming signal at the lens, relative to the focal point. The feedhorn will actually see portions of 15 zones walking progressively in and out of phase with the 20 -zone lens. If zones 7, 9, and 15 of the 20-zone lens are blocked, then the gain should actually increase to 14.6 dB . The gain for an 8 -foot parabolic dish would be about 36.9 dB .
SAM M. STRICKLAND
Bellevue, WA

## BEEFED-UP BENCH SUPPLY

I read with interest Mr. Vaughn Martin's bench power-supply article in the October 1985 issue Ra-dio-Electronics. I've been a technician for about seven years, and I would like to build such a supply for my home bench, but with the high-current option Mr. Martin mentions.

I think that, in addition to Mr. Martin's instructions for increasing power output-using a highercurrent transformer and highercurrent output transistors (Q1, Q3, Q5, Q7) - it might also be necessary to use lower-value, higherpower resistors for R37, R27, R9, R18.
continued on page 104


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clamped/unclamped video switch. The model ESR 424 is priced at \$699.00.

The model ESR 424B adds multichannel capability to the model ESR 424 package. Using a 950-1450 MHz IF output frquency, the block-conversion model features dual input switching to eliminate the need for external relays or switching splitters. It is priced at \$759.00.-R. L. Drake Company, PO Box 112, Miamisburg, OH 45342.


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vidual testing. In addition, staggered contact rows on . $100^{\prime \prime}$
centers permit easy probe attachment and help prevent accidental shorting of adjacent probes.

The test clip is available in al-loy-part number 923670-20, with a suggested retail price of $\$ 19.95$, and in gold-part number 923675-20, with a suggested retail price of \$25.90.-AP Products, Incoporated, 9325 Progress Parkway, PO Box 540, Mentor, OH 44060.

POWER SUPPLIES, the $X T$ Series, are 60 -watt linear power supplies, offering six voltage and current ranges in single, dual, triple, and quad configurations. The DC supply series is designed for laboratory, university, service, and other benchtop operations; they may also be used in control systems and ATE applications.

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SCANNER PLUS, the model Z60, is not only a sophisticated scanner radio, but a digital alarm clock as well. The scanner monitors 60 channels and 7 public-service bands (including VHF-Aircraft), plus the standard FM broadcast band. When the alarm is activated, listeners can wake up to their favorite FM music program, and then switch to police or fire calls, aircraft communications, amateur radio transmissions, and up-to-the-minute broadcasts from the National Weather Service.


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The model $Z 60$ has a suggested retail price of $\$ 379.95$.-Regency Electronics, Inc., 7707 Records Street, Indianapolis, IN 46226.

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## Overall specifications

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for most of our tests. All operations are initiated through a 31-key keypad; all data is displayed on a small CRT that measures about \(21 / 4^{\prime \prime}\) by \(27 / 8^{\prime \prime}\). The logic analyzer weighs about 11 pounds and measures about \(4 \times 7.5 \times 15\) inches. A small leatherette pouch attached to the lid of the machine stores the AC power cord, probes, etc. A snapon plastic cover protects the unit when it is in transit; the carrying handle rotates smoothly throughout 360 degrees to prop the 318 to a comfortable viewing angle.
On the left side of the instrument (as you face it) are jacks for serial input: a 25-pin "D" connector (DB-25), and a BNC jack to which a low-capacitance os-cilloscope-type probe may be connected. A BNC-type composite video jack is also located on the left side. On the right side is space for up to four parallel-input connectors (sub-miniature "D" type), a BNC jack for an external-clock input, and four pin jacks for external trigger input and output, a start output, and ground. The right side of the case also provides access to several important points in the 318's circuit that are used for diagnosing problems with the unit, should they arise.

On power-up, the 318 goes through a series of self tests. The power-up sequence takes only about five seconds, after which the machine is ready ready for use. All operations are accessible through a set of menus (Setup, Threshold, and Trigger); four cur-sor-control keys, a select key, and an execute key help you choose the desired item.

\section*{The menu displays}

Pressing the setup key allows you to set the overall operating mode of the 318: serial vs. parallel, local (keyboard) vs. remote operation, etc. If you select the parallel mode, a new menu appears that allows you to set the acquisition mode (see below), and to assign specific data bits to specific display channels. That allows you, for example, to hook your connectors up in just about any convenient manner. You can then let the logic analyzer group data lines, address lines, and control lines together in

a comprehensible display. In the parallel mode, the 318 has a data memory of 256 bits for each of the 16 input channels; in the serial mode, up to 256 bytes can be stored.

If you select the serial mode from the setup menu, a new menu appears that allows you to select the communications parameters (baud rate, sync/async, parity, etc.) and the acquisition mode. In addition, you can set the 318 up to acquire data until a portion of the most recently received data matches-or doesn't match-a designated portion of the 318 's optional battery-powered reference memory.

The Threshold menu allows you set the voltages the 318 uses to distinguish between different logic states. You can pick standard TTL levels, or any values from -10 to +10 volts, in 0.1 -volt increments, to represent the "high" and "low" values used by your system.

The Trigger menu allows you to set the event around which the data display centers. In the serial mode, data acquisition can be initiated by an external trigger signal, by receipt of a one- or two-byte data sequence, or either. The trigger byte(s) may be specified in binary, octal, decimal, or hexadecimal. Any bit or digit may be a "don't-care" value. For example, in hex mode, a trigger-byte specification of "FX" would cause any received byte that has a leading digit of " \(F\) " to generate a trigger.

The trigger position field of this menu allows you to choose seeing the data that precedes the trigger byte, data that follows it, both, or data that follows the trigger, but that is delayed by any number of bytes you specify (up to 65,000 ).

In the parallel mode, three trigger words are available that may be


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combined in various ways to trigger data capture. For example, you can specify that a first trigger word must be followed by either a second or a third trigger word. The 318 allows other triggering options in the parallel mode, but we haven't the space to discuss them.

\section*{Data display}

After operating parameters have been set using the three menus, it's time to press the data key. That brings up one of several display screens, depending on the mode. In the serial mode, all data in the 256-byte buffer can be displayed at one time in the character menu display as ASCII (or EBCDIC) characters, or, in the state table display, each location in memory is displayed on a separate line. Each line contains the memory location, the hex (or octal or decimal) value, the binary value, and the ASCII (or EBCDIC) value of that location.
The cursor-control keys allow you to scroll through memory; alternatively, you can enter the location you'd like to view via the dataentry keys in the keypad. You can change some of the communication parameters at the display screens without returning to the setup menu. You can flip between the two display screens at will; indeed you can go to most menus at any time to change operating parameters.
To initiate data-capture, the start button is pressed, or an external trigger is applied. The message "WAITING TRIG" appears in the lower right corner of the screen; when the trigger-word condition is satisfied, or when you press the stop key, an appropriate message is displayed, and you are free to examine the contents of memory. The 318 provides compare and search functions to aid in that process. The compare function compares the current data to a reference memory. A separate function is provided from the setup menu that allows you to transfer the contents of the data memory to the reference memory.

You could use the search function to search for the "FX" byte (or any other, of course) mentioned above. The 318 locates all matches, highlights them on the
screen, and displays the total number of matches. You can move through memory, using the cursor keys, or you can jump from match to match.

In the parallel mode, a state table that lists received data in the desired number base \((2,8,10\), or 16 ), and a timing diagram are available. The state table is similar to the serial mode state table, except that no ASCII values are displayed. The timing diagram can display glitches (which are captured whether or not they are displayed), and a portion of the display can be magnified for greater viewing accuracy. Search and compare functions are also available in the parallel mode.

\section*{Conclusions}

After you become familiar with the 318 , operation is fairly intuitive. The problem is in getting familiar. The operator's manual that is supplied appears to be little more than a polished-up version of the engineering specifications. It contains most of the information you need to know, but the manual's organization is atrocious. Tektronix should have supplied a tutorial introduction for such a complicated machine. However, people who have worked with other brands of logic analyzers shouldn't have much trouble applying their prior experience.

The only other thing we would criticize is the small size of the display screen. Even those with good eyesight reported difficulty reading the screen from a distance greater than about one foot. Evidently, Tektronix was aware of that problem, as they included a com-posite-video output jack. But it's inconvenient to use a separate monitor; we think the 318 would benefit from a \(5^{\prime \prime}\) CRT.

As you can see, the 318 logic analyzer has quite a few powerful fea-tures-and we've only scratched the surface. Other than the few inconveniences we've mentioned, the 318 should make a useful addition to the professional test bench. The 318 lists for \(\$ 5300\). The model 338 is similar to the 318 , but it has a 32 -bit data path, and a 20 MHz maximum clock rate. It lists for \(\$ 5800\).
continued on page 40

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\section*{Rhoades TE-600} Teledapter Stereo Synthesizer

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THE HOTTEST THING IN VIDEO THESE days is audio. Or, to be more precise, stereo audio. Everywhere you turn, you see the new stereo TV's and VCR's.

But many of us still own mono equipment, and the idea of scrapping an otherwise quite serviceable set-up in favor of a new one just for the sake of stereo often is not appealing. And it is certainly not economical, especially when one considers the high-end price tag of much of that new equipment.

There is an alternative. It is the Rhoades (Highway 99 East, Columbia, TN 48401) TE-600 Teledapter. The primary use of that device is as a stereo synthesizer, but it has other features that help make sure that the unit will not someday join the rest of your system on the scrap heap.

\section*{A stereo synthesizer}

The primary application of the unit is as a stereo synthesizer. It can accept a mono-audio input from a TV, VCR, etc., and produce a stereo-audio effect. A low-impedance input is provided to accommodate those systems where no audio output is provided. In those cases, the audio can be tapped directly from the TV's speaker terminals and fed to the low-impedance input. An inputlevel control is provided to set the low-impedance input to the proper level.

The unit uses Rhoades' StereoPlex circuit to perform the stereo synthesis. According to the manufacturer, that circuit works by creating a time delay between frequency elements of the original signal. The manufacturer claims that that technique allows the frequency response of the derived stereo channels to be essentially flat from 20 - to \(20,000-\mathrm{Hz}\).

In use, the stereo effect that re-
sults is dramatic. A/B testing is facilitated by an in/Out switch on the front panel. When stereo synthesis was selected, the sound spread out and filled our listening room. Listening to the same source material in both "natural" stereo and synthesized stereo, it was often difficult to discern between the two.

\section*{And a bit more}

If the \(T E-600\) stopped there, it would merit a spot at the top of the list for anyone interested in such a product. But there's quite a bit more to that unit.

Rhoades has provided for the day when your video equipment will be upgraded. An ambience circuit that's designed to work with stereo sources has been included in the unit. That circuit detects and reduces common components of the audio from the two stereo channels. The result is widened stereo separation and a concerthall effect. For instance, when the ambience circuit is used, vocals tend to reverberate and seem to "bounce" off walls, as they would in a live performance.

Finally, the manufacturer has included a National Semiconductor \(D N R\) noise-reduction system. That noise-reduction system can be used with either mono or stereo sources. In addition, it is singleended. That means that, unlike the more familar Dolby systems, it does not require encoded source material. It will remove high-frequency hiss from all sources, including TV, video and audio tapes, AM and FM broadcast radio, and satellite signals. When properly set, the DNR circuit did an excellent job of removing that high-frequency hiss, without appreciably affecting the frequency response of the input material.

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\section*{New Ideas}

\section*{An inexpensive crystal timebase}


FIG. 1

STOPWATCHES, DIGITAL EVENT COUNters, and frequency counters need an accurate and stable source of 1 Hz pulses. Figure 1 shows a way of generating 1 -second and 0.1 -second pulses, as well as a manual RESET pulse, using an inexpensive TV colorburst crystal, a pushbutton switch and a few common IC's. Since all IC's are CMOS types, the circuit may run from any five- to fifteen-volt power source. The whole circuit, including IC sockets, can be put together for under ten dollars.

\section*{Circuit operation}

An on-board oscillator and a 17stage divider compose IC1. By connecting a standard \(3.58-\mathrm{MHz}\) television color-burst crystal as shown, an accurate source of \(60-\) Hz squarewaves is generated at the IC's output, pin1. Those pulses are then fed to IC2, a 4024 sevenstage ripple counter. Its outputs are connected to different gates in IC3, which is a dual four-input NAND gate. Depending on which position pulse-select switch S2 is in, one of those gates will provide

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an output/reset pulse of the selected width.
First assume that S2 is in the 0.1 SECOND position. The Q2 and the Q3 outputs of IC2 go high after six input pulses have been received. Pin 13 of IC3-a then goes low, so pin 11 of IC4-a goes high, and that resets IC2, so counting starts over. If S2 is in the 1SECOND position, the reset/output pulse is generated when IC2's Q3-Q6 outputs (pins \(2-5\) ) all go high; that is, after 60 input pulses have been received.
The pulse from IC3-a or IC3-b is continued on page 111

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\section*{Practical Performance}

If you don't need the 800 MHz range coverage, Regency offers two exciting new units. The MX5000 is a 20 channel, no-crystal scanner that receives continuously from 25 to 550 MHz with all the same features as the MX7000. Then there's the 30 channel MX3000. It's digitally synthesized so no crystals are necessary, and the pressure sensitive keyboard makes programming simple. What's

more, it has a full function digital readout, priority, search and scan delay, dual scan speed, and a brightness switch for day or night operation.

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\section*{Transmitter circuit overview}

The block diagram of the wireless video link is shown in Fig. 1. The RF chain is fairly conventional. Its first stage is a crystal-controlled oscillator (Q1) with a frequency of 60 to 65 MHz , which is one-eighth the final output frequency. For example, our prototype used a crystal frequency of 60.40625 MHz , which gives a final output frequency of 483.25 MHz the video-carrier frequency of UHF channel sixteen.

The oscillator produces a signal of about \(+6 \mathrm{dBm}(4\) milliwatts) that drives three stages of frequency doublers. The combined action of those doublers multiplies the input frequency by eight for a final output frequency of (nominally) 500 MHz . Double-tuned circuits are used between each stage to help reduce spurious outputs that might cause unwanted interference.
The video input signal (from your VCR, video camera, etc.) drives a video modulator (Q6 and Q7) that adds the video signal to the +12 -volt line supplying power to the final


FIG. 1-BLOCK DIAGRAM OF THE TV TRANSMITTER reveals the overall simplicity of the circuit, which is composed of an oscillator, three frequency doublers, and video and audio modulators.
doubler (Q4) and the output amplifier (Q5). That method of modulation is similar to the way a conventional AM-radio transmitter is modulated. The video modulator has a nominal bandwidth of five MHz .

Audio input is applied to Q8, which operates as a VCO (Voltage Controlled Oscillator) running at a nominal frequency of 4.5 MHz to produce the modulated sound carrier. For simplicity, Q8 is a freerunning oscillator, since the \(\pm 25-\mathrm{kHz}\) frequency deviation that is required would be very difficult to produce at that frequency with a crystal-controlled oscillator. Besides, most TV sound systems will accept a \(\pm 10-\mathrm{kHz}\) error in the soundcarrier frequency without producing undue distortion, and that greatly simplifies the circuitry required.

\section*{Calculating maximum range}

The following equations may be used to calculate the maximum range you can expect from the wireless video-cameratink. All logarithms in this equation (and in those following) are calculated in base 10; frequencies ( \(f\) ) are specified in MHz; and distances ( D ) are specified in miles.

The average TV receiver has a bandwidth (BW) of 4.0 MHz and a noise factor \((\mathrm{NF})\) of 6 dB . For a snow-free picture, the carrier-to-noise ratio \((\mathrm{C} / \mathrm{N})\) should be 40 dB or better. Receiver sensitivity can then be calculated as the Minimum Desired Signal (MDS):
\[
M D S=N F+10 \log (B W)-174+C / N
\]

Plugging values into that equation, we find that:
\[
\text { MDS }=6+10 \log \left(4 \times 10^{6}\right)-174+40
\]

Therefore, \(\mathrm{MDS}=62 \mathrm{dBm}\), or \(0.794 \times 10^{-3} \times 273=216\) millivolts. So for a transmitter power of +15 dBm and 2 isotropic antennas, that allows a \(P\) ath \(L\) oss (PL) of 87 dB .
\[
\mathrm{PL}=37+20 \log (\mathrm{D})+20 \log (f)
\]

Isolating the distance factor we see that:
\[
\begin{aligned}
20 \log (D) & =P L-37-20 \log (f) \\
20 \log (D) & =87-37-(20 \times 2.7) \\
\log (D) & =-4 / 20=-0.2
\end{aligned}
\]

So the maximum range obtainable should be:
\[
\begin{gathered}
D=10^{-0.2} \\
D=0.6 \text { mile }=3100 \text { feet }
\end{gathered}
\]

However, you wouldn't really be able to get a range of 3100 feet because of reflections, loss from "dead spots," terrain loss, and obstacle shielding. However, a distance of several hundred feet is easily possible using a +12 V supply.

\section*{Detailed circuit description}

The complete schematic of the wireless video link is shown in Fig. 2; we'll discuss each stage in detail. Transistor Q1 is a common-base (or Colpitts) oscillator biased by resistors R1, R2, and R3. Inductor L4 and capacitors C3, C4, C5, and C8 form a circuit that is tuned to the frequency of the crystal.

The crystal is series-resonant at some frequency between 60 and 65 MHz , so it appears as a low impedance ( 50 ohms or less) at that frequency. Therefore Q1 will have sufficient gain as a common-base amplifier only at the resonant frequency of the crystal. Hence the signal developed at the junction of C 4 and C 5 will be amplified by Q1 only if that signal is at the same frequency as the crystal. At that frequency, Q1, has sufficient gain to oscillate because the ratio of the voltage initially developed between C4 and C5 to that at Q1's collector is greater than unity.

Capacitors C3 and C8 complete the tuned circuit; they also form a voltage divider that feeds the base of Q2 about one volt of the signal from Q1. Transistor Q2 functions as an overdriven amplifier that distorts its input signal and thereby produces harmonics of the input frequency.

The second harmonic ( 120 MHz ) is the

PARTS LIST
All resistors \(1 / 4\)-watt, \(5 \%\) unless otherwise noted.
R1, R5, R11-22,000 ohms
R2, R6, R12, R16- \(4,700 \mathrm{ohms}\)
R3, R7, R13-220 ohms
R4, R9, R10, R18, R19, R21, R25-100 ohms
R8, R14, R33- 10 ohms
R15, R26, R35- 100,000 ohms
R17, R28, R30-2200 ohms
R20, R22-470 ohms
R23, R27, R34- 10,000 ohms
R24-3300 ohms
R29- 82 ohms
R31-1000 ohms, linear taper potentiometer
R32- 10,000 ohms, audio taper potentiometer
Capacitors
C1, C6, C15, C32, C38-0.01 \(\mu\) F 50 volts, cermaic disc
C2, C7, C9, C14, C16, C23, C26, C29, C30-470 pF cermaic disc
C3- \(33 \mathrm{pF}, 5 \%\), mica
C4, C19- \(15 \mathrm{pF}, 5 \%\), mica
C5- \(56 \mathrm{pF}, 5 \%\), mica
C8- \(82 \mathrm{pF}, 5 \%\), mica
C10-18 pF, \(5 \%\), mica
C11-2 pF , \(\pm 1 \mathrm{pF}\), mica
C12-24 pF, 5\%, mica
C13-39 pF, \(5 \%\), mica
C17-8 pF, \(5 \%\), mica
\(\mathrm{C} 18, \mathrm{C} 24-1 \mathrm{pF}, \pm 1 \mathrm{pF}\), mica
C20-12 pF, 5\%, mica
C21, C27-47 pF, 5\%, mica
\(\mathrm{C} 22, \mathrm{C} 25, \mathrm{C} 28-1-8 \mathrm{pF}\), polystyrene trimmer
C31, C33, C39- \(8.2 \mu \mathrm{~F}, 20\) volts, tantalum
C34-470 pF,5\%, mica
C35-220 pF, 5\%, mica
C36-5-60 pF, polystyrene trimmer
C37-100 pF, 5\%, mica

\section*{Semiconductors}

Q1-Q3, Q6-2N3563
Q4-2N3564
Q5, Q7-2N3866
Q8-MPF102
Q9-2N3565
LED1-standard red LED
D1-1N7579V Zener
D2-MV2117 varactor
D3-1N4002
Other components
J1-Video camera jack
J2-BNC jack
J3-Coaxial power jack
L1- \(6.2 \mu \mathrm{H}\) (see text)
L2, L3- \(0.074 \mu \mathrm{H}\) (see text)
L4- \(0.15 \mu \mathrm{H}\) (see text)
L5, L6- \(0.035 \mu \mathrm{H}\) (see text)
L7, L8, L9- \(0.018 \mu \mathrm{H}\) (see text)
L10, L11-5. \(6 \mu \mathrm{H}\)
S1-SPST miniature toggle
XTAL-Crystal (see text)
Miscellaneous: Aluminum project case AA-penlight cell battery holders
Note: The following are available from
North Country Radio, P. O. Box 53, Wykagl Station, New York 10804: Etched and drilled PC board, ferrite cores for coils L1 and L4, and crystal for Channel 14 or 15, \(\$ 32.50\); Complete set of all parts that mount on PC board, \(\$ 69.95\). In either case, be sure to specify channel.


FIG．2－COMPLETE SCHEMATIC OF THE TV TRANSMITTER is shown here．The circuit is straightfor－ ward，but see the text for information on winding coils L1－L9．

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frequency we're interested in; L2 and C10 are tuned to that harmonic, and C 8 is also series-resonant at that frequency. The additional base current supplied by C8 also improves Q2's efficiency of oscillation. A double-tuned circuit is provided by \(\mathrm{Cl1}\), \(\mathrm{C} 12, \mathrm{C} 13\), and L3; those components filter harmonics higher than the second, as well as the \(60-\mathrm{MHz}\) fundamental.

Another stage of frequency doubling is provided by Q3, which operates very much like Q2, except that the tuned circuits at its input resonate at approximately 125 MHz , and its output circuits resonate at about 250 MHz . Again, Q4 operates like Q3, taking account of the values of the components in the tuned circuits. However, note that no emitter-bypass capacitor or resistor is used. It is difficult to get good bypassing in the \(430-500-\mathrm{MHz}\) range with ordinary components, and it takes only a very small impedance in the emitter to kill the power gain of that stage. Therefore, the emitter is directly grounded.
Power amplification is provided by Q5; it receives its drive from Q4 and the dou-ble-tuned VHF circuits composed of L7, L8, C22, C24, and C25.

Both Q4 and Q5 receive their supply voltage from the emitter of Q7, which supplies +4.5 volts with no input signal to Q4 and Q5. That voltage has positive-sync-tip video superimposed on it. The video gain provided by Q6 and Q7 is about eight, and the bandwidth is greater than 10 MHz . Those transistors are capable of driving a 75 -ohm load to a level of 10 volts peak-to-peak.

Negative-sync input video from a camera, VCR, etc., is DC-coupled to the junction of R21 and R22. Video bypass is provided by C31. Gain and Q-point are set by R24; potentiometer R31 acts as a video gain control; and R29 keeps the input impedance around 75 ohms.

FET Q8 functions as a Hartley-type VCO with a free-running frequency of 4.5 MHz ; C36 is used to fine-tune that frequency. Feedback is provided by C35 and C34; D2 is a varactor (variable-capacitance) diode. It is biased at about nine volts by R25, R26, and Zener diode D1, which also biases Q8.

The varactor (D2) changes capacitance at the audio rate, and that causes the oscillator's frequency to vary. In other words, the varactor provides frequency modulation (FM). Audio is fed to D2 via R27 and C38, and C37 provides RF bypassing at 4.5 MHz . The small value of \(\mathrm{C} 38(0.01 \mu \mathrm{~F})\) provides pre-emphasis to the audio. Audio pre-amplification is provided by Q9; R32 is the audio gain control. The \(4.5-\mathrm{MHz} \mathrm{FM}\) signal from Q8 is summed with the video signal through R23. The sound-level carrier may be varied by changing R23 as necessary.

The power supply is a 12 -volt battery pack composed of eight AA alkaline


FIG. 3-COMPONENTS ARE MOUNTED ON THE PC BOARD as shown here. Note how R31 dangles from the PC board. We suggest you use a more secure method of mounting that potentiometer than we did.
cells; 10 AA Ni-Cd cells could be used instead. Alternatively, external power may be coupled in through jack J3. The power-on condition is indicated by LED1, which is current-limited by R28.

If, for any reason, video bandwidth must be reduced, simply add a small capacitor across R24. It should have an impedance equal in magnitude to R24's impedance at the highest intended video frequency.

\section*{Construction}

Our transmitter is built on a singlesided PC board; the foil pattern is shown in "PC Service" elsewhere in this magazine. You may use either 0.8 - or \(1.6-\mathrm{mm}\) G-10 glass-epoxy PC-board material. For most low-power, solid-state applications, G-10 epoxy is suitable for frequencies at least as high as 1000 MHz . That material has high electrical resistance, low electrical loss, mechanical rigidity, dimensional stability and low water-absorption characteristics.

The majority of construction projects published the past few years have been digital-low frequency-projects. Since many hobbyists have never attempted to build a high-frequency project like our transmitter, a few words about UHF construction techniques are in order. So bear with us for a few moments if what we say below is old hat to you.

First, be careful to select the proper components. For example, at low frequencies (under about 10 MHz ), most ce-
ramic bypass capacitors work fairly well. Typical 0.01 and \(0.1 \mu \mathrm{~F}\) bypass capacitors in TTL and CMOS circuits fail to do their job as operating frequency is increased to about 25 or 30 MHz . That may be corrected by reducing the value of those capacitors to \(0.001 \mu \mathrm{~F}\), and by keeping the lead lengths short-zero, if possible.

As the operating frequency approaches \(100-150 \mathrm{MHz}\), bypassing again becomes a problem. At that point even smaller values ( 47 to 470 pf ) are required, and leads must be very short. At even higher frequencies, up to about 500 MHz , chip capacitors, which have no leads, are soldered directly to the PC board. The moral is: Keep leads very short-do not use \(1 / 4\)-inch leads on bypass capacitors.

Common electrolytic capacitors generally work well up to 10 MHz , depending on value and physical size. Mica capacitors (DM-15 type) are pretty good up to the VHF range, depending on value, but they can have from 5 to 15 nanohenries of series inductance. That inductance can increase apparent capacitance. For example, a capacitor with a value of 47 pF at 30 MHz may measure 82 pF at 150 MHz . That must be taken into account when designing tuned circuits at the frequencies we are dealing with.

Typical \(1 / 4\)-watt resistors of 15 to 1000 ohms behave pretty well up to about 250 MHz or so. Values below 15 ohms tend to appear inductive, and values above 1000 ohms tend to appear capacitive. That can cause a shift in impedance, and, there-


FIG. 4-MOST COMPONENTS ARE STANDARD, but note how C26 and C29 connect to L8 and L9, respectively. Also note the unequal turn-spacing of L2, L3, L5, and L6, which was brought on by tuning those coils. Finally, note the small slug L4 is wound around, and L1's toroidal core.
fore, the resonant frequency, of a tuned circuit. Stray inductive and capacitive reactances cause that shift.

Also, remember that every PC trace has inductance and capacitance. A PC pad may have, depending on size, several pF of capacitance to ground or to another PC pad. A long PC rail may act as an RF transmission line or even as a resonant circuit. It may also appear as an additional turn on a coupling transformer, by which unwanted signals might be radiated to other components or PC traces.

We hope you're not scared by all those cautions. We just want you to be aware of some of the problems involved in RF design and construction. But if you take care to duplicate our prototype exactly, the chances are that you'll have no problem getting your transmitter working. There is no "black magic" involved-only common sense and careful construction. Just be sure to use the same or equivalent parts-be careful!
After you have your components together, check the PC board over for shorts and opens, and make sure that the copper is clean and shiny. Then, referring to the parts-placement diagram in Fig. 3, insert and solder the components, starting with those that have the lowest profile (the resistors and diodes), and working up to the electrolytic and trimmer capacitors. Remember to cut all leads-especially the capacitors' leads-as short as possible. Don't overheat the semiconductors when soldering them to the PC board.

Now you're ready to wind and install the RF coils. Spread the turns of each coil evenly, but don't worry about spacing those turns perfectly, since the coils will
be compressed and expanded later when you tune up your transmitter. You can see in the photo (Fig. 4) how our prototype's coils turned out.
- L1 is eight turns of 22-gauge wire wound on a Ferroxcube 768 T 188 toroid core made from 4C4 ferrite.
- L2 and L3 are seven turns of 22-gauge wire wound on a \# 26 drill bit. Of course, remove the drill bit after the coil is completed.
- L4 is wound around a standard 10-32 screw thread. The screw should be removed after the coil is completed. Then L4 should be fitted with a ferrite slug. You may be able to find an appropriate slug in an old TV, radio, or CB radio. Best results will be obtained when the slug is taken from a circuit that operates in the 25-100MHz range, such as a TV IF circuit, or the front end of an FM radio. But it's not really critical, and almost anything should work (provided it's made of ferrite) as a last resort. If necessary, L4's diameter, and number of turns, could be changed to fit the slug you have.
- L5 and L6 are three turns of 22-gauge wire wound on a \#26 drill bit.
- L7, L8, and L9 are merely 1.5 cm loops of wire wound on a \(3 / 8\)-inch form and sol-


FIG. 5-RF-PROBE may be used with any VOM or DVM that has sensitivity greater than 20 K ohms/volt.
dered to the PC board. One end of capacitor C26 is mounted in the normal fashion, and the other end hangs from the approximate mid-point of L8's loop. Similarly, C29 is mounted from the board to the midpoint of L9; the lead then continues to the pad near the collector of Q5.
- L10 and L11 are standard \(5.6 \mu \mathrm{H}, 10 \%\) tolerance chokes obtainable from the J.W. Miller Corporation, etc. They could also be wound from 36 -gauge wire on \(1 / 2\)-watt, 1 -megohm carbon resistors if desired.

Last, install the transistors, making sure that they are oriented correctly and that their lead length is minimized.

We use a 10 -pin camera jack for J1, but feel free to substitute whatever connectors you need. If no sound carrier is needed, R23, R25, CR2 and all other components associated with Q8 and Q9 can be omitted. Doing that will not affect the operation of the video portion of the transmitter. They can be added at a later date should audio transmission become necessary.

We chose not to leave space on the PC board for the audio- (R32) and videoinput (R29, R31) components, since those components were unnecessary in our application. We used fixed-value resistors for R31 and R32, but small potentiometers could be mounted to the case and wires run to the PC board. If no gain control is necessary, R32 should be replaced by a fixed 10 K resistor, and C38 should be connected directly to the collector of Q8.

Solder the coils to the PC board now, and solder short interconnecting wires from the board to the chassis components. Before applying power, check over your work: Make sure no solder bridges exist and make sure that all polarized components are correctly oriented.

\section*{Testing and alignment}

The following equipment is necessary to align the transmitter:
- VOM or DVM having sensitivity of at least 20,000 ohms/volt
- RF probe
- Video source (VCR or camera)
- TV set or monitor
- 50 -ohm dummy load

If an RF probe is not available, you can use the circuit shown in Fig. 5 with your voltmeter. If you have no 50 -ohm dummy load, you can use a 51 -ohm, \(1 / 4\)-watt resistor. One handy gadget to have is a tuning wand: a plastic rod with a ferrite slug at one end and a brass slug at the other. The inductance of a coil can be increased or decreased by placing the ferrite or the brass slug, respectively, near the coil.

Apply power and check for +12 V at R4, R9, R10 and the collector of Q7. Check for +9 V at the drain of Q . Then check for 1.0 to 1.5 volts at the emitters of Q1, Q2, and Q3. Check for +4.5 to continued on page 114

\title{
HIGHLIGHTS of the NPE CONVENTION
}

\section*{The unification of NESDA and NATESA was just one of the highlights of the 1985 National Professional Electronics Convention.}

WHILE EACH YEAR'S NATIONAL PROFESsional electronics convention (NPEC) is a worthwhile and memorable experience, 1985's session, held in August in Hartford CT, was particularly significant for the electronics industry. At that meeting a consolidation agreement between the Na tional Electronics Sales and Service Dealers Association (NESDA) and the National Association of Television and Electronic Servicers of America (NATESA) was unanimously approved by NESDA. Later in the month, NATESA, at its convention held in St. Charles, IL, also unanimously approved it.

\section*{Joining together}

Under the agreement, the Chicagobased NATESA organization will become the NESDA state affiliate in Illinois as of January 1, 1986. At the same time, NESDA will accept and perpetuate the heritage of NATESA as its own. For two years, current NATESA members who reside outside of Illinois may choose to either remain members of the Illinois association or to join the NESDA affiliate in their home state. Current NESDA members residing in Illinois have two years to join the new Illinois affiliate.


GETTING INVOLVED. Murray Bariowe, CET, and Ed Kimmel, CET, participate in the Electronics Instructors Conference, one of the many seminars at the Hartford NPEC.


MAN OF THE YEAR. Larry Steckler, publisher of Radio-Electronics, Chairman of the Electronics Industry Hall of Fame, and NESDA's Man of the Year speaks at the Hall of Fame Banquet. Roger Companion, NATESA Vice President, is seated at the right.

Former officers of NATESA will be recognized equally with those of NESDA. The founder of NATESA, the late Frank J. Moch, who, at the convention, was inducted into the Electronics Industry Hall of Fame, will be perpetually honored as the father of both organizations.

The current presidents of both organizations highly praised the new agreement. Said continuing NATESA president Tom Leeney, "We're proud to contribute our part to a vibrant NESDA organization, and this agreement shows that the people in our industry have the ability to work together for the good of all." Dorothy Cicchetti, the newly-elected president of NESDA, added, "With the addition of the diligent and loyal NATESA members, NESDA will become an even more dominant force for good in our profession. '

\section*{Elections and awards}

A spirited contest led to the election of Dorothy Cicchetti, of Flushing, NY, as NESDA president.

The other NESDA officers elected included: Robert Harrell, Johnson City. TN, 1st National Vice President; Clifford A. Shaw, Columbia, VA. Secretary; and Dick Scott, CET, Olympia, WA, Treasurer. Regional Vice Presidents are Art Van Sicklin, Hartford, CT; Faust Guarino, CET, Oceanside, NY; Jim Teeters, CET, Norfolk, VA; Gene Dillingham, CET, Louisville, KY; Bob Mesa, CSM, Parma, OH; Floyd Hack, CET, Taft, TX; Gennie Randel, Greensburg, KS; Vince Hostetler, CSM, Grand Junction, CO; Ken Duncan, CSM, Antioch, CA; and Bob Villont, CET/CSM, Tacoma, WA.

The new ISCET (International Society of Certified Electronics Technicians) officers are: Jim Parks, CET, Orlando, FL, Chairman: Don Winchel, CET, Smart-


OUTGOING NESDA PRESIDENT George Bluze (standing left) presents certificates recognizing the industry contributions of Paul Kelley (standing center), Robert Haskins, and Robert Read. Tom Plant CET (standing right) accepted the certificate for Haskins and Read.
ville, CA, Vice Chairman; Hal Robbins, CET, Van Nuys, CA, Secretary; and Earl Tickler, CET, Baltimore, MD, Treasurer. Bob Villont will continue as the NESDA representative to the ISCET board of governors.

Turning to the awards, as previously mentioned, the late Frank J. Moch was inducted into the Electronics Industry Hall of Fame. In addition, Larry Steckler CET/EHF, Publisher, Gernsback Publications (including, of course, Radio-Electronics) and Chairman of the Electronics Industry Hall of Fame was named NESDA's Man of the Year; he was also presented with ISCET's Continuous Service Award.
Other major awards included: Ken Duncan, CSM, Officer of the Year; Cliff Shaw and Chic Young, P.E./CSM (tie), Outstanding Committee Chairman; Don Erwin, CSA, Outstanding State President; Ed Erich, CSM, Outstanding Local President: Martin Fleming, CET. Technician of the Year: and Lester Dodd, CET, 1985 Friend of ISCET.

Rounding out the event was an outstanding slate of professional seminars, a trade show, and an unusually rich assortment of memorable social functions. R-E


CELEBRATING THE SIGNING of the momentous agreement consolidating NESDA and NATESA are NESDA President Dorothy Cicchetti (left) and NATESA President Tom Leeney.


JEROME MOCH, (standing left) son of the late Frank J. Moch, accepts the plaque designating the induction of his father into the Electronics Industry Hall of Fame. Making the presentation is Wallace Harrison, Director of Communications of NESDAISCET.

\section*{BUTMT TRHIS}


\title{
Satellite-TV Receiver
}


RICHARD MADDOX
OK, TVRO fans, here's your chance to build a high-performance satellite receiver-for peanuts!

Part 2Last time, we showed you how our budget satellite receiver works. This month, we give you step-by-step instructions detailing how to build the unit.

\section*{Construction}

Use of a PC board is strongly recommended; a foil pattern is presented in the "PC Service" section of this magazine.

Use the component-placement diagram in Fig. 3 and the photograph in Fig. 4 to assemble the board. When inserting parts in the PC board, work in numeric sequence, and check off each part as it is installed.

1-Beginning with resistor R1, insert all resistors in numerical order. R96 and R100 are not installed on the PC board.

2-Install the wire jumpers. Insulation should cover the entire exposed length of each jumper.

3-Install the trimmer potentiometers on the board, followed by the trimmer capacitors, and then the other capacitors. Carefully check the value of every part before mounting it. Be sure to orient all polarized capacitors correctly.

4 -Install all diodes. Be careful to install them with the correct polarity, and use extra care in bending the leads on glass diodes.

5-Insert all IC sockets, making sure that pin one of every socket is oriented correctly. IC1 and FL1 should both be mounted flush to the board and soldered in. The leads of transistors Q1 and Q2 should be trimmed to a length of 2 mm , and those transistors should be mounted and soldered on the solder side of the board. The other transistors should be inserted from the top and soldered now. Leave \(1 / 4\)-inch of lead protruding above the board.

6-Voltage regulators IC10 and IC11 need heat sinks. Use heat-sink grease and mount them loosely to the heat sinks before inserting the assembly in the PC board. Align the legs of the regulators and the heat-sink tabs with the corresponding holes in the PC board, solder the regulators and heat sinks to the board, and tighten the screws. Install the 7805 regulator, IC9.

7-Mount power switch S3 and AFC switch S2 on the board and solder them.

8-Solder the two type-F connectors, J 1 and J 5 , and the three RCA-type phono jacks, J2, J3 and J4, to the PC board.

9-Mount the RF Modulator on the board. Be sure that all four mounting tabs are flush, and then solder them to the board.

10-Mount and solder the inductors. Note that L4 is mounted vertically on the board. Install FL2 now.

11-Mount green POWER LED3 \(1 / 2\)-inch above the board. It will be bent to fit into the front panel later.
(Editor's note: We recently heard from an experimenter who reported that the following modifications will improve performance: Remove R69 from the board. Replace R64 with a 220 -ohm, \(1 / 4\)-watt unit. Add a 330 -ohm, \(1 / 4\)-watt unit at the base of Q7.)

\section*{Front-panel assembly}

At this point, all components should be installed except the IC's and the front- and rear-panel components. The front-panel layout is shown in Fig. 5. The wire lengths given below assume that you are using the PC board and case specified in the parts


FIG. 3-ON-BOARD COMPONENTS are shown here. Note that transistor Q1 and Q2 are mounted on the underside of the board.


FIG. 4-THE COMPLETED PC BOARD. Be sure to route the interconnecting wires away from the voltage regulators.
list. If you're not, use wires of the appropriate length.

1-Mount potentiometers R103, R108, and R110 to the front panel. Orient all three so that their terminals point down.

2-Viewing the panel from the rear, connect the center and right terminals of R108 together. Solder a seven-inch wire to all terminals except the right terminal of R108.
3-Mount switches S2 and S4. Solder six-inch wires to the three terminals on S4. Solder a seven-inch wire to each center terminal of S2; connect a short jumper between the upper-left and the lower-center terminals
4 -Insert LED1 and LED2 into the appropriate holes in the front panel. Orient them so that their cathodes (the flat sides) are toward polarity-switch S2. Solder each cathode to the nearest lower-end terminal on that switch. Solder a seven-inch wire to each anode.
5-Solder ten inches of wire to each meter terminal. Mount the meter in the front panel.

\section*{Rear-panel assembly}

The rear-panel layout diagram is shown in Fig. 6.

1-Mount the terminal strip and ACinput jack J6 to the back panel.

\section*{PARTS LIST}

All resistors \(1 / 4\)-watt, \(5 \%\) unless otherwise specified.
R1, R3-270 ohms
R2- 510 ohms
R4, R5, R32, R33, R77- 680 ohms
R6- 150 ohms, \(1 / 2\)-watt
R7- 1200 ohms
R8, R30, R44, R46- 10,000 ohms
R9, R10, R26, R53- 56 ohms
R11, R16, R17, R24, R25, R28, R60, R65, R87-75 ohms
R12, R15, R29, R34, R42, R52, R72, R79, R82, R86, R95- 1000 ohms
R13, R47, R70, R93, R96-100 ohms
R14- 3.9 ohms
R18-R21, R54, R73-470 ohms
R22, R23-120 ohms
R27-820 ohms
R31, R43, R69-220 ohms
R35, R37-22,000 ohms
R36- 18,000 ohms
R38, R74, R94- 100,000 ohms
R39, R40, R45, R92-4700 ohms
R41-unused
R48, R64- 330 ohms
R49, R51, R88-2200 ohms
R50- 120,000 ohms
R55, R56, R58, R62, R63- 150 ohms, \(1 / 12\) watt
R57, R75- 3300 ohms
R59, R61, R66, R68, R76- 560 ohms
R67-43 ohms
R71-12,000 ohms
R78, R83, R84- 0 ohms (jumper)
R80-47,000 ohms
R81- 1500 ohms
R85-8200 ohms
R89, R90-2700 ohms
R91- 68,000 ohms
R96-180 ohms, 1 watt
R97-R99, R101-unused
R100- 5.6 ohms, 5 watts
R102, R104, R105, R107, R111, R112-
5000 ohm trimmer potentiometer
R103- 5000 ohm linear potentiometer
R106, R113-10,000 ohm trimmer potentiometer
R108- 10,000 ohm linear potentiometer
R109-2000 ohm trimmer potentiometer
R110-5000 ohm linear potentiometer

R114- 100,000 ohm trimmer potentiometer

\section*{Capacitors}
\(\mathrm{C} 1-\mathrm{C} 5, \mathrm{C} 7, \mathrm{C} 8, \mathrm{C} 13-\mathrm{C} 18, \mathrm{C} 20, \mathrm{C} 21, \mathrm{C} 23\), C24, C27, C42, C44, C50-C52, C61, C64, C65, C67-0.01 \(\mu \mathrm{F}\), ceramic disk
C6, C37, C41, C43, C54, C59, C66-unused
C9, C10, C63-33 pF, ceramic disk
C11, C12, C46, C53- \(0.001 \mu \mathrm{~F}\), ceramic disk
C19-47 pF, silver mica
C22, C45, C49, C58, C60-10 \(\mu \mathrm{F}, 25\) volts, tantalum
C25, C56- \(47 \mu \mathrm{~F}, 16\) volts, tantalum
\(\mathrm{C} 26, \mathrm{C} 48, \mathrm{C} 57, \mathrm{C} 59-0.22 \mu \mathrm{~F}, 30\) volts, tantalum
C28-100 pF, ceramic disk
C29-56 pF, ceramic disk
C30-68 pF, ceramic disk
C31- 300 pF, ceramic disk
C32-220 pF, ceramic disk
C33, C38, C62-100 \(\mu \mathrm{F}, 16\) volts, electrolytic
C34, C68-0.1 \(\mu \mathrm{F}\), ceramic disk
C35- \(-470 \mu \mathrm{~F}, 16\) volts, electrolytic
C36-10 pF, ceramic disk
C39- \(0.0047 \mu \mathrm{~F}\), ceramic disk
C40- \(0.047 \mu \mathrm{~F}\), ceramic disk
C47- \(5600 \mu \mathrm{~F}, 40\) volts, electrolytic
C55- \(0.0022 \mu \mathrm{~F}\), ceramic disk
C70- \(220 \mu \mathrm{~F}, 25\) volts, electrolytic
C71-5-70 pF, variable
C72-2-20 pF, variable

\section*{Semiconductors}

IC1-MWA120, hybrid small-signal amplifier
IC2-MC10116, triple differential line receiver
IC3, IC4-LM358, dual op-amp
IC5-MC1496, video detector
IC6-NE555, timer
1C7-NE592, video amplifier
IC8-NE564, phase-lock loop
1C9-7805, 5 -volt regulator
IC10-7812, 12 -volt regulator
IC11-7818, 18 -volt regulator
Q1, Q2-BFR91
Q3, Q4, Q6-2N2222
Q5, Q7-BC328 or 2 N 3683


ALL DIMENSIONS IN INCHES
FIG. 5-FRONT-PANEL LAYOUT, with dimensions to fit the controls mounted to the PC board.

2-Solder a jumper wire between the two ground terminals of TS1

3-Solder R100 and the positive lead of C70 to the +5 V terminal. Solder the minus lead of C70 to ground.

4 -Solder one four-inch wire to the pULSE output terminal, another to one ground terminal, and another to the other end of R100. Solder a three-inch wire to the two bottom terminals of J6.

Q8-BC548 or ECG548
D1-D4-1N60
D5-1N752, 5.6 -volt zener diode
D6-HP 5082-2800 or 1N6263 Schottky diode
D7-1S2075
D8-D11-1N4002
D12-BB119 tuning diode
LED1-standard green LED
LED2, LED3-standard red LED's
Other components
J1, J5-"F" connector
J2, J3, J4-RCA phono jack
J6-coaxial power input jack
TS1-4-position screw-terminal strip
L1, L5- \(10 \mu \mathrm{H}\)
L2- \(0.33 \mu \mathrm{H}\), six turns on a \(1 / 4\)-inch form.
L3- \(100 \mu \mathrm{H}\)
L4-2.7 \(\mu \mathrm{H}\)
S1, S4-SPDT, toggle switch
S2, S3-DPDT, toggle switch
FL1-BO124 SAW filter
FL2-5-8 MHz block filter (Dick Smith L-1600)
M1-200 \(\mu \mathrm{A}\) edge-reading meter
RF modulator
T1-18-volt AC power transformer

Note: the following are available from Dick Smith Electronics, Inc., P.O. Box 8021, Redwood City, CA, 94063: Complete kit of parts including case but no power transformer, \#K-6316, \$99.95 plus \(\$ 4\) shipping; SAW filter, \#L-1620, \$29.95; Case, \#H-2507, \$12.95; PC board, \#H-7000, \$29.95; 18-volt transformer, \#M6672, \$7.95;BFR91 transistor, \#Z-1691, \$1.19; BB119 diode, \#Z-3070, \$0.20; MWA120 RF amplifier, \#Z-6095, \$12.50; MC10116 ECL IC, \#Z-6000, \$0.79; HP5082-2800 Schottky Diode, \#Z-3230, \$2.00; 5-8-MHz filter, \#L-1600, \$3.95. Other individual parts, and complete satellite systems, are also available from Dick Smith. California residents please add \(6.5 \%\) sales tax. Orders outside U.S. must include U.S. funds and add \(15 \%\) of merchandise total for shipping.


\section*{Final assembly}

The component-placement diagram in Fig. 7 should be consulted while connecting the front- and rear-panel components to the PC board.

1-Insert and solder PC terminal pins (or short pieces of stiff wire) in all holes to which off-board components will be connected. Place the PC board in the cabinet, but do not screw it down yet.

2-Slide the front panel over the two PC-mounted switches, and then insert the front panel into the first slot of the cabinet. Carefully insert the green power LED into its hole.

3-Slide the rear panel over the connectors mounted at that end of the board, and then insert the rear panel into the last slot of the cabinet.

4 -Insert and tighten the four mounting screws.


\section*{ALL DIMENSIONS IN INCHES}

FIG. 6-REAR-PANEL LAYOUT, with dimensions.


FIG. 7-FRONT- AND REAR-PANEL COMPONENTS are connected to the PC board as shown here.

5-Connect all wires from the offboard components to the appropriate pins on the PC board. Doublecheck all connections.

6-Plug the transformer's output into J 6 , and then plug it into the wall.

7-Check the output of each regulator (IC9-IC11) for the correct voltage. Also, check the voltages at J 5 (the +18 V connector), at the +5 V terminal strip, and at
pin 8 of IC4 \((+12 \mathrm{~V})\). The pOWER LED and meter lamp should light up, and so should either LED1 or LED2, depending on the position of S2. Flip that switch to verify that the opposite LED lights up.
8-Turn off the receiver and unplug the transformer.

9-Insert all the ICs into their sockets. Be sure to orient them correctly, and make sure all IC's are seated properly.

10-Route all wires down the center of the board. Be sure to leave clearance around the trimmer potentiometers, and route wires away from the regulators' heat sinks. Use wire ties to secure the wires.

11-Set each trimmer to the center of its range using a small screwdriver. Set the two trimmer capacitors so that the adjustment slot is parallel to the side walls of the cabinet.

12-Turn on the receiver and measure the total current drain. It should be about 400 ma .

13-Measure the voltage at J1. The voltage should vary as R103, TRANSPONDER TUNING, is varied.

\section*{Aligning the polarizer}

A friend or spouse may be helpful in completing the following alignment.

1-Connect the polarizer to the appropriate terminals on TS1. The red wire usually connects to +5 V , black connects to ground, and white connects to the pulse output-but make sure your unit follows that convention. Center the SKEw control.

2-Turn on the receiver. Verify that the probe moves as polarity switch S2 is toggled. Mark the vertical probe position on the side of the polarizer. Move S2 to horizontal. Adjust R113 so that the probe is 90 degrees from its former position.

\section*{Aligning the tuning control}

The adjustments made now will be fine-tuned a little later.

1-Measure the voltage at pin 1 of IC3. Turn AFC on. Adjust R105 for exactly 3.0 volts DC. Turn AFC off. Adjust R106 for the same voltage.

2-Measure the voltage at JI. Set the transponder tuning control to line up with the "Channel 1" label on the front panel. The voltage should be set to the lowest voltage specified by the downconverter manufacturer. That is done by adjusting R104.

3-Set the transponder tuning control to Channel 24. Adjust R102 for the highest voltage needed by the downconverter.

\section*{Final check-out}

The easiest way to check out and adjust the receiver is to use it on a system that is already working. Ideally, a friend or neighbor will allow you to hook up the receiver and downconverter for final testing. If that is not possible, then first you will have to get your dish aimed precisely at Galaxy 1. Instructions for doing that are usually given in the assembly instructions packed with the dish.

Next, the feedhorn, LNA and downconverter should be installed, and then cable should be run to the receiver. Do not use more than about 250 feet of cable between the downconverter and the receiver.
continued on page 114

\section*{BUTMUD TITIS \\ HUMIDITY MONITOR}


Don't let static zap your costly electronic gear! This electronic humidity monitor lets you spot potentially dangerous conditions.

\section*{MARK C. WORLEY}

IF YOU'RE LIKE MANY PEOPLE, YOU TEND to downplay-or simply ignore-the importance of humidity. But you shouldn't! The reason is that, after temperature, humidity is the most important environmental condition that affects our comfort level. We're comfortable when the humidity is low in the summer, but that same low humidity can make us feel uncomfortably cool in the winter.

Perhaps more important to the readers of Radio-Electronics is the fact that hu-midity-or the lack of it-can drastically affect the operation of the electronic devices we love so well: computers, TV's, VCR's, stereos, etc. Proper humidity control in the winter months can reduce the static buildup that is so often detrimental to the operation of electronic equipment. For example, a rule of thumb states that you should be careful when the humidity drops below about \(50 \%\) as the temperature drops below about \(70^{\circ}\). You certainly wouldn't want to handle any CMOS IC's in those conditions!

In order to help you bring your humidity problems under control, we will discuss what humidity is, some of its effects, and several historical means of measuring it. Then we'll show you how to build a modern, electronic humidity monitor that features 5\% accuracy for about \(\$ 50\).

\section*{What is humidity?}

Humidity is usually specified as percent Relative Humidity, or RH, for short. Relative humidity is not a measurement of the amount of water vapor in the air. Rather, RH is the ratio of the amount of water vapor in the air to the maximum amount of vapor that air can hold. That maximum varies primarily with temperature, although barometric pressure affects it to a lesser degree.

For example, let's assume that a given volume of air at a given temperature can hold one ounce of water vapor. If that air contains half an ounce of water, its relative humidity is \(50 \%\). If that same volume of air were cooled, it might be able to hold a maximum of only \(6 / 10\) an ounce of water. So if that air still contained half an ounce of water, the relative humidity would now be \(0.5 \div 0.6\), or \(83.3 \%\).

On a hot day the relative humidity governs our comfort primarily because it affects the efficiency of our natural cooling system-our sweat glands. If the humidity is high, sweat can't evaporate as readily. That's why a hot, dry day is more comfortable than a warm, humid day. Various "comfort zone" charts have been developed that show which combinations of temperature and humidity are the most comfortable.

The effects of humidity are evident all around us. For example, dew is caused by cooling of the air during the night until it saturates (reaches \(100 \% \mathrm{RH}\) ), and it then releases excess moisture onto any cool surface. The temperature at which that saturation occurs is called the dew point.

Here's another common effect of humidity: iced drinks that "sweat" on a hot day. That "sweating" is really caused as follows. The outer surface of the glass is cooled by the icy contents of the glass. That surface in turn cools the surrounding air.

When that air reaches the dew point temperature, it releases some of its excess moisture onto the surface of the glass. So in reality that "sweating" is not perspiration from the glass, but condensation from the atmosphere. Hence the reason cold drinks don't "sweat" as much in dry climates as they do in humid ones is that there's very little water in the air to condense on the glass.

\section*{Measuring humidity}

Temperature is easy to measure using a simple thermometer, or any of a number of solid-state devices. Humidity, on the other hand, is probably the most difficult environmental condition to measure. The search for an accurate, dependable means
of measuring humidity has occupied scientists for centuries. For example, Leonardo Da Vinci noticed in 1550 that a ball of wool weighed more on a rainy day than on a dry day. Ever since then scientists have been refining ways of measuring RH precisely. For example, methods using various organic substances, electrooptical sensors, resistive sensors, and variable-capacitance sensors have been developed. Each method has unique advantages and disadvantages.
Organic sensors like human hair, animal hair, and animal membranes have been in use the longest, and are still in use today. An organic tissue absorbs moisture readily, and, as it does, it will stretch more easily. That stretching can be measured, and that provides an indirect indication of RH. As you might suspect, the primary disadvantage of organic sensors is their tendency to age rapidly, and that requires frequent re-calibration.

Relative humidity can also be calculated by measuring the dew point. The dew-point method is highly accurate, but cumbersome, because of the cleanliness, and the complex, precise circuitry that are required. A mirrored surface is monitored as it cools until moisture begins to form on it. The temperature at which moisture is detected is the dew point, and that is dependent upon relative humidity. The dewpoint method is most suitable for laboratory work.

Resistive sensors have their problems, too. The resistance of that sort of sensor usually ranges from the hundred of thousands of ohms to the tens of megohms. That high resistance, plus the non-linear response curves of those sensors, makes them difficult to work with. In addition, they can be damaged by direct contact with moisture, by common airborne contaminants, or by simple DC voltages. Most sensors require an AC excitation voltage, because even a small DC voltage can cause chemical migration within the sensor, and that usually ruins it.

Another humidity sensor is based on variations in capacitance. Sensors of that type weren't commonly used in the past because of their high cost-typically \(\$ 100\) or more apiece-and because they can be difficult to use due to their small variation in capacitance. However, the sensor shown in Fig. 1, developed by the N. V. Philips Company, and sold in this country by Mepco/Electra (Columbia Road, Morristown, NJ 07869), is inexpensive and easy to work with.

\section*{The sensor}

Philips' humidity sensor is a capacitor formed from a dime-sized piece of plastic film that is coated on both sides with a very thin layer of gold. Because the dielectric constant of that film varies with changes in RH, so does the sensor's capacitance. On each side of the film the


FIG. 1-PHILIPS' HUMIDITY SENSOR provides a 45-pF change in capacitance over the 0-100\% humidity range.


FIG. 2-THE SENSOR'S CAPACITANCE and the relative humidity are related exponentially.
gold functions as one plate of a capacitor; it also provides the electrical contact for the sensor-housing's spring-contact leads. The sensor measures about \(0.6^{\prime \prime}\) in diameter, and \(0.9^{\prime \prime}\) high. We list some of the specifications of Philips' sensor in Table 1. For more information, see Mepco's Technical Information Brochure 063, their Technical Note 134, and the data sheet that comes with the sensor.

The curve in Fig. 2 shows how the sensor's capacitance varies with humidity. By
extrapolating a little we see that capacitance varies from about 115 pF at \(0.0 \%\) RH to about 160 pF at \(100 \%\) RH. In other words, there is a change in capacitance of 45 pF over the entire RH range. So, in order to measure RH, all we need is a circuit that translates that 45 pF variation in capacitance to a properly scaled variation in voltage.

But before we discuss the details of circuit operation, there are a few other things you should know that affect the accuracy obtainable from our humidity monitor. First, the sensor has a drift of \(0.1 \%\) per degree Celsius, which translates to an inaccuracy of \(1 \%\) for a \(10^{\circ} \mathrm{C}\) change in temperature. So, over a range of \(40^{\circ} \mathrm{C}\), accuracy will drop to about \(4 \%\). Given proper calibration, our humidity monitor should be accurate, therefore, to better than \(5 \% \mathrm{RH}\) over a wide temperature range. Just compare that to the typical \(25 \%\) accuracy - or worse-of the dialtype humidity indicator included with many wall- and desk-top thermometer-ba-rometer-humidity monitors! You should also be aware that the capacitance of the sensor is somewhat dependent upon the frequency applied to it, but to obtain the accuracy we're interested in, we can ignore that variation.

\section*{Circuit operation}

If we were simply to build an oscillator whose frequency varied in response to changes in the sensor's capacitance, we could measure relative humidity, but we'd have an offset problem, because \(0.0 \% \mathrm{RH}\) corresponds to 115 pF , not 0.0 pF . In other words, we'd have some output even at \(0.0 \%\) relative humidity. So we use two oscillators in our circuit, and measure the difference between their outputs. That allows us to obtain an output of 1.00 volt for \(100 \%\) RH.

Our circuit is a modified version of one supplied by Philips. In their circuit, one 4001 CMOS quad-NOR package was used to build the two oscillators, and the gates in a second 4001 were connected in parallel to provide extra drive for the rectifier/ filter circuit. We decided to use 7555 's for the oscillators because they're only slightly more expensive, but much more

\section*{TABLE 1-SENSOR SPECIFICATIONS}
\begin{tabular}{ll} 
Parameter & Value \\
Humidity range & \(10-90 \% \mathrm{RH}\) \\
Temperature range & \(0-85^{\circ} \mathrm{C}\) \\
Capacitance \(\left(25^{\circ} \mathrm{C}, 43 \% \mathrm{RH}, 100 \mathrm{kHz}\right)\) & \(122 \mathrm{pF} \pm 15 \%\) \\
Frequency range & \(1-1000 \mathrm{kHz}\) \\
Temperature dependence & \(0.1 \% \mathrm{RH}{ }^{\circ} \mathrm{C}\) \\
Response time (max) & \\
\(10-43 \%\) RH & 3 minutes \\
\(43-90 \%\) RH & 5 minutes \\
Typical hysteresis & \(3 \%\) \\
Maximum voltage & 15 volts \\
& \\
To \(90 \%\) of final value, @ \(25^{\circ} \mathrm{C}\), in circulating air &
\end{tabular}


FIG. 3-THE HUMIDITY MONITOR CIRCUIT uses CMOS 555's to keep current drain low.


FIG. 4-TIMING RELATIONSHIPS of various points in the humidity monitor's circuit are shown here.
predictable (and repeatable) than oscillators made from CMOS gates. Also, we found that it was unnecessary to use paralleled gates to drive the metering circuit.
As you can see from the schematic in Fig. 3, our humidity monitor is composed
of three CMOS IC's and a low-power voltage regulator. Total current drain is only about 5 mA , so battery operation is entirely feasible. The circuit consists of two oscillators, a few nOR gates, and a detector/filter circuit that helps linearize the output.

A CMOS 7555 is used for ICl, a \(7-\mathrm{kHz}\) astable oscillator. A CMOS 7555 is also used for IC2, a one-shot whose pulse width is determined by R3 and the capacitance of the sensor. The master oscillator (IC1) drives IC3-a, which provides the trigger pulse that drives IC2. The relationship of those two signals, and the others to be discussed below, is shown in the timing diagram in Fig. 4.

The output of IC2 is inverted by IC3-b and combined with the master-oscillator signal by IC3-c, which passes only the difference between the two signals to the filter/detector circuit that follows. Trimmer potentiometer R2 is provided to null the circuit out at the low end of the RH scale. On a dual-trace scope the null condition appears as equal-phase, equal-width pulses at pins 5 and 6 of IC3-c. With inputs of that sort, IC3-c gives no output.

The detector circuit is composed of di-
ode D1, resistors R5-R9, and capacitor C3. The pulses from IC3-c are rectified and filtered into a DC voltage that is proportional to relative humidity. Full-scale meter adjustment is provided by R6, and R8 and R9 function as a voltage divider that scales the output to exactly one volt at \(100 \%\) RH.

Since the sensor's capacitance is exponentially related to RH, something is needed to increase the linearity of the circuit. That something is provided by R7, which supplies extra charging current to C3, which would normally be fed only by the detector. Also, R8 and R9 discharge C3 to further increase linearity. The only drawback to our scheme is that, due to the voltage-divider effect of R7-R9, the out-

\section*{PARTS LIST}

All resistors \(1 / 4\)-watt, \(5 \%\), metal film (not carbon composition!) unless otherwise noted.
R1- 6800 ohms
R2-5000 ohms, linear potentiometer, PC mount
R3- 464,000 ohms, \(1 \%, 1 / 4\)-watt
R4-2200 ohms
R5- 3300 ohms
R6-2000 ohms, linear potentiometer, PC mount
R7-806,000 ohms, \(1 \%, 1 / 4\)-watt
R8- 7500 ohms, \(1 \%, 1 / 4\)-watt
R9- 10,000 ohms, \(1 \%, 1 / 4\)-watt

\section*{Capacitors}

C1- \(0.01 \mu \mathrm{~F}, 10 \%\), mylar or polycarbonate
C2- \(0.001 \mu \mathrm{~F}, 10 \%\), mylar or polycarbonate
C3- \(0.22 \mu \mathrm{~F}, 10 \%\), mylar or polycarbonate
C4, C5, C7, C8- \(0.1 \mu \mathrm{~F}\), ceramic, monolithic, or disc
C6- \(10 \mu \mathrm{~F}, 16\) volts, electrolytic or tantalum, radial leads
Semiconductors
IC1, IC2-7555 CMOS 555 timer
IC3-4001B, CMOS quad dual-input NOR gate
IC4-78L05, 5 -volt, \(100-\mathrm{mA}\) voltage regulator
D1-1N914, 1N4148, or equivalent
Other components
M1-50 \(\mu\) A, Radio Shack \#270-1751
S1-SPST toggle or momentary switch
Sensor-Mepco \#2322-691-90003
Sensor socket-Molex part \#10-18-2031
Portable case-Radio Shack \#270-1751
Outdoor case-Keystone part \#677 (set of \#666 \& \#685)
Note: The following are available from Mark Worley, 909-B Country Aire, Round Rock, TX 78664: Screened, drilled, and plated PC board, \(\$ 7.00\); Sensor \(\$ 15.00\) for 1 or \(\$ 25.00\) for 2; Calibration capacitors ( 115 pF and 160 pF , \(1 \%\) mica), \(\$ 2.00\) per set; Kit of IC's, sensor, sensor socket, PC board, resistors, and capacitors, \(\$ 40.00\) (calibration capacitors, meter, enclosure, \& hardware not included). Add \(10 \%\) for shipping, \(\$ 3.00\) maximum. Most orders shipped within 1 week, but allow up to 30 days for delivery. Send cash or check only.


\section*{THE MARK III}

\section*{HV CIRCUIT SCANNER}
* Checks the horiz output circuit for open / shorts,
\(\star\) Checks the flyback, yoke, PC, and HV mult,
» Checks all scan derived B+ sources,
\(\star\) Checks all circuits that rely on scan derived \(\mathrm{B}+\) voltage,
* Checks for open safety capacitor,
\(\star\) Checks the emitter circuit of the horiz output,

\section*{THEN,}
« Provided the green normal light is lit, the Mark lil will safely power up the TV set so that you can "look" for open circuits by examining the picture on the CRT.
\(\star\) Circumvents all start up and horiz drive related shut down circuits.

APPLICATIONS: The Mark III will analyze the horiz, flyback, hivoltage, scan derived B + sources, yoke, pin cushion, HV multiplier circuits in any TV set that employs either an NPN transistor or a single SCR for its horiz output device. This applies to any age, any model, any chassis, any brand - - including Sony.

In brief, the "test" function scans for shorts, the "run" function permits you to observe any "open" circuits via the symptoms that appear in the CRT screen.

HOOK.UP: Simply remove the set's horiz output device and replace it with the scanner's interface plug. No wires to disconnect, no other connections required (not even a ground connection).

MISTAKE PROOF: No damage will result if an error is made during hook up. The scanner simply won't turn on until the error is corrected.

RED OPEN LIGHT means the emitter circuit of the horiz output stage is open (no ground path).

YELLOW SHORT LIGHT means the flyback primary, HV multiplier, vertical output, horiz driver, and R-B-G color output stages are not shorted. Instead, a circuit that normally draws a small amount of current is shorted (i.e. the tuner, IF, AGC, video chroma, matrix, vertical or horiz oscillator).
RED SHORT LIGHT means either the flyback, the HV multiplier, the vertical output, horiz driver or one of the R-B-G output transistors is shorted.
GREEN NORMAL LIGHT means the TV set's entire flyback circuit is totally free of shorts. It also means that it is safe to power up the TV set with the "run" button so that you can look for open circuits by observing the symptoms on the CRT screen.

FEATURES: All start up circuits and all horiz drive related shut down circuits are automatically circumvented by the Mark III during all test and run functions. During the test function all flyback secondary output is limited to approx \(80 \%\) of normal. 2nd anode voltage is limited to approx 5 KV .

This means all circuits that are not shorted will have some \(80 \%\) of their normal \(\mathrm{B}+\) voltage during the "test" phase. It also means that any shorted circuit will have zero DC volts on it. This feature makes any short easy to isolate.

The MARK III sells for only \(\$ 595^{\circ 0}\)
The money you are now spending for unnecessary flybacks alone will easily pay for your Mark III. Why not order yours today!

\section*{Visa and Mastercharge Welcome!}

Diehl Engineering • 6661 Canyon Drive " \(F\) " • Amarillo, TX 79110
Phone: (806) 359-0329 or (806) 359-1824

\(\star\) Checks the horiz output stage for opens / shorts
ڤ Checks flyback, yoke, PC, and HV mult,
* Checks all scan derived \(B+\) sources,
* Checks for open safety capacitors
\(\star\) Checks for open ground path for horiz output stage
\(\star\) Checks for open primary LV supply,
\(\star\) Checks for error in interface connections,
« Checks for proper LV regulation,
\(\star\) Checks for proper start up circuit operation,
* Checks for shorted horiz driver transistor,
« Checks the operation of the horiz osc / driver circuits,
\(\star\) Checks B + "run" supply for the horiz osc / driver circuits,
» Checks all circuits in the TV set that rely on scan derived B +.
\(\star\) Automatically circumvents all start up circuits and horiz drive related shut down circuits.

HOOK UP: (Identical to Mark III)
OPERATION: Turn the Mark V on, turn the TV set on, then, simply look at the lights.
RED "HOOK UP" LIGHT means that you have made an error in hook up. No damage has been done, correct the problem then continue.
RED "EMITTER" LIGHT means that the ground path for horiz output stage is open. Correct the problem then continue.
RED "B + OPEN" LIGHT means that the primary LV supply in the TV set is open. Correct the problem then continue.
No "top row lights" equals normal.

\section*{Look at the middle row of lights}

RED "START UP" LIGHT means that the start up circuit in the TV set is not working (no start up pulse).
GREEN "START UP" LIGHT means the start up circuit in the TV set is working normally. Yes, it is \(100 \%\) accurate. Even on Zenith's single pulse start up circuit !
RED "HORIZ DRIVE" LIGHT with a green start up light means that the horiz driver transistor in the TV is shorted ( E to C ).
GREEN HORIZ DRIVE LIGHT means that the horiz oscillator and driver circuits are operational.

\section*{READ THE DC VOLTAGE METER THEN,} PUSH THE TEST BUTTON
If the meter comes up to, or, falls back to, factory specified DC collector voltage, the LV regulator circuit is working. If it fails to do so, it is not working!

RED "B + RUN" LIGHT means that the \(\mathrm{B}+\) source that normally keeps the horiz osc / driver circuits running after the start up B + pulse has been consumed has become open.

GREEN "B + RUN" LIGHT means that the B + resupply voltage (scan derived) is being provided. All is normal if all three lights are now green.

The scan circuit short detector in the Mark V is identical in all ways to that which is used in the Mark III. Operation is also identical. Both units are virtually indestructable when simple directions are followed. Both units carry a full year's warranty against defects in materials and workmanship (parts and labor). Either unit can be easily repaired by almost any technician in his own shop.

\section*{If the green "circuits clear" light is now lit}

It is now safe to push the "run" button and examine the symptoms that appear on the CRT screen, for the purpose of isolating any "open" circuits.

Except for hook up and CRT filament warm up time, this test can easily be completed in two to five seconds!

The Mark V sells for only \(\$ 995^{\circ 0}\)
Stop losing money on start up/shut down scan derived B + problems; order your Mark V today!

\section*{Visa and Mastercharge Welcome !}

\footnotetext{
Diehl Engineering • 6661 Canyon Drive " \(F\) " • Amarillo, TX 79110
Phone (806) 359-0329, or (806) 359-1824
}


FIG. 5-AN OP-AMP VOLTAGE FOLLOWER may be used to buffer the output of the humidity monitor.


FIG. 6-METERING CIRCUITS for the humidity monitor. Use the circuit shown at a with a \(100-\mu \mathrm{A}\) meter, and the one at \(b\) with a \(50-\mu \mathrm{A}\) meter.
put cannot fall below 60 mV , which corresponds to \(6 \%\) RH. So, our monitor can read no lower than \(6 \% \mathrm{RH}\).

For best accuracy, the output should be monitored with a meter having an input impedance of at least one megohm. Alternatively, the output could be buffered to drive an analog meter or other low-impedance load. The simple op-amp voltagefollower circuit shown in Fig. 5 could be powered by the same (battery) supply that powers the remainder of the circuit. Or you could, if desired, substitute a preassembled digital LED- or LCD-meter module.

Another alternative would be to use a \(100-\mu \mathrm{A}\) moving-coil meter as an indicator. Since a \(100-\mu \mathrm{A}\) meter with a fullscale reading of one volt has a resistance of 10 K , it could replace R9 in the circuit, as shown in Fig. 6-a. But since a \(100-\mu \mathrm{A}\) meter is slightly difficult to obtain, we used a \(50-\mu \mathrm{A}\) meter with the voltage divider shown in Fig. 6-b for our prototype.

\section*{Current drain}

The 78L05 low-power voltage regulator
adds about 4 mA of current drain to the circuit, which would otherwise consume only about 1.5 mA . That 4 mA is the regulator's required operating current, so it can't be eliminated (unless you can obtain one of National's new micro-power voltage regulators). A Zener diode would not alleviate the current-drain problem, since it would require even more operating current. A Zener diode would also have poorer regulation, which could affect accuracy. With no voltage regulator at all, the pulse height from IC3-c would vary with battery voltage, so accuracy would be affected.

For portable or occasional use, a 9-volt battery is the ideal power source, since current drain is low. Alternatively, power could be supplied by an inexpensive wallmount transformer with an output of 7.5to 12 -volts DC. For permanent outdoor installation, mount the power supply inside the house, not out in the weather.

\section*{Construction}

Our humidity monitor can be used in a portable mode both indoors and outdoors. However, for permanent outdoor installation more rugged construction techniques will have to be used. We'll present plans for both portable and permanent units, although we'll stress construction of the portable unit.

The circuit should be built on a PC board to minimize stray capacitance that could affect ICl's output frequency and IC2's pulse width. You can purchase a PC board from the source listed in the Parts List, or you can etch and drill your own using the foil patterns shown in the "PC Service" section of this magazine. It's not recommended, but if you assemble the circuit on perf board, use the kind that has a solder pad around every hole (such as OK Industry's \#A-PC-02 prototyping board). You may have trouble experimenting with our circuit on a solderless breadboard because that type of breadboard has a large amount of distributed capacitance and a ground plane that can also affect circuit operation.

Use only high-quality, low-tem-perature-coefficient components to limit the effects of temperature on the circuit's accuracy. Capacitors C 1 and C 2 should be polystyrene or polycarbonate types. It might be worthwhile experimenting with a positive-temperature-coefficient capacitor for Cl to offset some of the sensor's


FIG. 8-YOUR COMPLETED PC BOARD for the humidity monitor should appear as shown here. Interconnecting wires attach to the bottom of the board.
temperature drift, especially if the monitor will be used outdoors. The resistors should be metal- or carbon-film types; carbon composition resistors should not be used in our circuit because their values vary widely with temperature and humidity.

When you have a suitable board, use the parts-placement diagram in Fig. 7 and the photo in Fig. 8 to mount and solder all components. Don't use IC sockets since they can contribute to stray capacitance. Solder the 3-pin Molex socket to the board vertically (as shown in the photo) if you want to mount your sensor as it is in our prototype. Also, for proper vertical clearance you may find it necessary to use a small tantalum capacitor for C6, or to mount that capacitor to the foil side of the PC board.

After mounting all components, check your work carefully, looking for solder bridges between adjacent pads, pins and traces. If the board is OK, remove all flux from it. Be careful to keep any solventparticularly acetone-away from the sensor. Use isopropyl alcohol to remove finger oils, and avoid touching cleaned surfaces.

Use a pair of clipped-off resistor leads to extend the lengths of the sensor's leads. Carefully solder the wires to the sensor, and make sure you don't damage the sensor from too much heat! Then clip the leads to an overall length of \(1 / 4\) inch. They will project through the case and into the Molex socket after final assembly.

Now let's turn to mechanical construction. First we'll discuss the portable unit. Drill a mounting hole in each corner


FIG. 7-PARTS-PLACEMENT DIAGRAM reveals the compact size of the humidity monitor.
of the board, and use those holes to mark the locations of the screw holes in the case. Also, drill four holes for the sensor: two for the leads, and two for the mounting tabs. Use an eighth-inch bit (or larger) for the sensor leads to minimize capacitive coupling to the case, as well as to eliminate the possibility of a short. Allow for a little offset in the position of the sensor due to the construction of the Molex socket.

The meter used in our prototype has a resistance of about 2 K , so we added an 18 K resistor in series with the meter to give it a total resistance of 20 K . Then R9 was changed to a 20 K resistor, so that the combination of R9, the meter, and R10 would be 10 K . That combination gives the meter a full-scale range of one volt.

If you use a \(100 \mu \mathrm{~A}\) meter, pad its resistance to 10 K , if necessary. Then remove R9, and use the meter in place of that resistor. If you decide to use a digital meter, you can leave R9 at 10 K , since most digital meters have at least 10 megohms input impedance, and the parallel combination of R9 and the DVM will not affect accuracy.

Mounting the sensor at the "rear" of the case might reduce the sensor's ability to respond to current humidity conditions, especially if the monitor is pushed back on a shelf. However, mounting the sensor in that way reduces the possibility that it will be damaged. But feel free to experiment with your own case and mounting methods. We bent a \(1 / 2\)-inch piece of thin steel strapping into a "U" shape with legs. Holes were drilled through the legs and through the case on either side of the sensor. Then the " \(U\) " was bolted to those holes to prevent mechanical damage to the sensor.

Finish assembling the case by installing the power switch, battery holder, and four \(1 / 4\)-inch standoffs for the PC board. If necessary, carefully remove the face plate of the meter and use rub-on letters to re-label the meter's scale. Assuming you're using a \(50-\mu \mathrm{A}\) meter, wire the case-mounted components as shown in Fig. 9. If you're using a different metering circuit, substitute the appropriate resistive network. Loose wires and components can move around and cause the outputs of ICl and IC2 to vary, so mount all components securely, and keep all leads short.


FIG. 9-IF YOU USE A \(50-\mu \mathrm{A}\) METER with the humidity monitor, it should be wired as shown here.

To complete assembly of the portable unit, mount the board to the case. Then insert the sensor's leads through the case and mount the sensor to the case with two sheet metal screws. Be careful not to crack the sensor by over-tightening those screws.

\section*{Building the outdoor monitor}

For permanent outdoor use, the sensor must be covered to protect it from direct rain and sunlight. A small louvered or screened box about ten inches on a side should work. Just make sure that air can circulate freely through the enclosure to reach the sensor. Also, avoid placing the assembly in vegetation, near a sprinkler, or in any other location that might tend to exaggerate the actual humidity. A hundred feet of 22 -gauge wire between a re-motely-mounted sensor and an indoor power supply and display meter should not affect accuracy.

To install the monitor outdoors permanently, the circuit will have to be mounted in a watertight enclosure. We'll discuss how to do that using the Keystone enclosure specified in the Parts List.

First, trim the PC board to fit the case. Then cut four two-inch lengths of 18 gauge wire and insert them into the holes at the end of the board. Bend the leads so that at least \(1 / 4\)-inch of the wire lies flat against the copper foil, and then solder the leads. The \(1 / 4\)-inch of contact helps strengthen the support for the board. Slide the free ends of the wires into the octal base of the case. Carefully bend the leads so that the end of the PC board rests against the base. Then clip the leads flush with the end of the plug's pins, and solder the wires inside the hollow pins. The octal


FIG. 10-OUTDOOR INSTALLATION of the humidity monitor requires a watertight case; drill holes for the sensor as shown here.
plug won't fit into its socket if solder leaks through to the outside of the pins, so be careful.

Refer to Fig. 10 to drill the mounting holes for the sensor in the top of the case, and to Fig. 11 to drill the holes in the side of the case that allow access to trimmer


FIG. 11-TWO HOLES MUST BE DRILLED in the side of the waterproof case to allow final calibration.
potentiometers R2 and R6. Those holes must be located precisely as shown to allow proper assembly and adjustment. If you use a small metal punch for the access holes in the side, you may be able to reinsert the punched-out pieces in the holes after calibration; otherwise fill the holes with epoxy, or cover them with electrical tape.

Bend the leads of the Molex connector 90 degrees before mounting it flush against the PC board. Then solder a scrap of wire across the socket to hold it in place firmly; two holes have been provided in the PC board for that support wire. The sensor's leads will have to be extended to a length of \(5 / 8\) inch.

To complete assembly, attach the sensor to the case with two sheet metal screws. A thin rubber gasket placed between the sensor and the case will provide additional weatherproofing. Carefully insert the board and the base assembly into the case so that the Molex connector mates with the sensor's extensions. Use four screws to hold the case and the octal base together. The two holes in the side of the case should line up with R2 and R6 so that those potentiometers can be adjusted easily with a small screwdriver.

\section*{Final check-out}

The check-out is the same for both the portable and the permanent versions of our humidity monitor. Before powering up, carefully check the board once more to make sure that all components have been installed correctly, and that there are no solder bridges between traces, etc. Then plug the sensor into its socket and apply power. You should be able to mea-
sure some voltage across R9 (in other words, M1, if installed, should deflect); that voltage should rise if you breathe on the sensor.
If you get no output, re-check your work, and verify that supply voltage (five volts) appears at pin 8 of IC1, pin 8 of IC2, and at pin 14 of IC3. If that voltage is present, use an oscilloscope to verify the presence of the waveforms shown in Fig. 4. After the board is debugged, allow it to cool down from the heat of soldering before doing the final calibration. Also, isolate the sensor from hand and breath moisture until it stabilizes to ambient hu-midity-about 5 minutes should do it.

\section*{Calibration}

For the first step of calibration we'll assume that the sensor's output exactly matches the curve shown in Fig. 2. Doing that allows us to substitute \(1 \%\) silver-mica capacitors for the sensor. Insert a \(115-\mathrm{pF}\) capacitor into the sensor's socket and adjust R2 for a reading of \(6 \% \mathrm{RH}(60 \mathrm{mV})\). Then replace that capacitor with a \(160-\mathrm{pF}\) unit and adjust R6 for a reading of \(100 \%\) RH ( 1.00 volt).
After assembling the case, you will need to re-adjust R2 so that the output agrees with a secondary humidity standard. That adjustment alters ICl's pulsewidth to correspond to the sensor you're using. Remember, the Philips sensors have a tolerance of \(\pm 15 \%\) at \(43 \%\) RH. This means that, although there will still be a \(45-\mathrm{pF}\) change in capacitance over the entire \(0-100 \%\) range of RH, the high and and low values may be shifted above or below the nominal values. It is R2 that provides compensation for that shift.

\section*{Absolute calibration standards}

Finding a humidity standard can be difficult, but here are a few ideas that may be useful. The most common method of measuring humidity accurately is with a sling psychrometer. You may be able to borrow one from a science or chemistry lab at a local high-school or college. The sling psychrometer has both dry- and wetbulb thermometers. The wet-bulb unit has a wick on its bulb that is moistened with distilled water. When the psychrometer is whirled in a circle, the evaporation of the wick cools the thermometer's bulb. The amount it cools depends on the amount of water that evaporates, and that is governed by the amount of moisture in the air-the relative humidity.

The dry-bulb thermometer is unaffected by that procedure since it's not moistened; it simply indicates the temperature of the ambient air. With every psychrometer comes a chart that allows you to determine RH from the readings on the two thermometers.

The accuracy of the sling psychrometer method depends on the accuracy of the two thermometers, the accuracy with

TABLE 2-SATURATED SALT SOLUTIONS
\begin{tabular}{lcc} 
Salt & \% RH @ \(68^{\circ} \mathrm{F}\) & \% RH @ \(77^{\circ} \mathrm{F}\) \\
Lithium Chloride Monohydrate & 11.3 & 11.2 \\
Magnesium Chloride Hexahydrate & 33.1 & 32.8 \\
Magnesium Nitrate & 54.4 & 52.9 \\
Sodium Chloride & 75.5 & 75.3 \\
Potassium Chloride & 85.1 & 84.3 \\
Potassium Sulphate & 97.6 & 97.3
\end{tabular}
which they're read, the cleanliness of the wick and the water, and also upon a sufficient quantity of air blowing across the wick. Small sling psychrometers with one degree increments and short thermometers have an accuracy of only \(10 \%\), or worse.

Saturated salt solutions offer better accuracy, but they are more difficult to use because the sensor has to be placed as close as possible to the solution without touching it, and the calibration process must occur inside an airtight container. That can make circuit adjustment awkward. Anyway, the solution maintains an equilibrium of humidity within the sealed container as long as that solution remains saturated. Both salt and water must be pure for best accuracy. We list some com-monly-used solutions, and the humidities you can obtain with them, in Table 2.

Caution: Those solutions are poisonous, so handle and store them with care, and keep them out of the reach of children and pets. If you use the lithium chloride solution, don't allow it to fall below a temperature of \(18^{\circ} \mathrm{C}\left(64^{\circ} \mathrm{F}\right)\), since the humidity reference of the solution will be permanently altered. Whichever salt you use, stir in crystals a little bit at a time until precipitates begin collecting on the bottom of your container. When you're sure no more salt will dissolve, put the solution and your circuit board in an airtight container, and adjust R2 so that the meter agrees with the value in Table 2.

One problem with the above calibration procedure should be obvious-how does one make the adjustments while the board is within the air-tight container? There are two possibilities. One is to make the adjustment outside the container, then place the board inside to see the result. Repeat as needed until the meter reading agrees with the value in Table 2. A more sensible solution would be to mount only the sensor in the container so that R2 can be adjusted from outside.

If those methods of calibration are impractical for you, you might try tuning in a local weather broadcast on radio or TV, or you could call the National Weather Service in your area. To do the final calibration, whatever standard you have chosen, apply power and then adjust R2 so that the value indicated by the meter agrees with your standard. Construction is now complete.

\section*{Final thoughts}

Whether or not you actually built our humidity monitor, we hope you learned something about what humidity is and about ways of measuring it. Like many subjects, there is a great deal more that could be said about humidity; we encourage you to do some investigating on your own. In a similar vein, our circuit was not intended to be used as the basis of a precision instrument, but we hope you'll find it fun to build as well as useful.
As you know, indoor humidity drops drastically in the winter. The reason is that cold, dry outside air is further dried by most indoor heating systems. (To be more accurate, hot air can hold more moisture than cool air, so the RH drops as the temperature rises. You may find it interesting to know that office buildings are often built these days with humidity controllers in addition to the systems that control their heating, ventilation, and air-conditioning (HVAC) systems.
You can use our humidity monitor to help maintain humidity levels at your home or office at a comfortable (and safe!) level. Humidity control can help prevent respiratory problems, and it can prolong the life of valuable paintings, books, and electronic devices.
For indoor use, avoid placing the humidity monitor near air-conditioning or heating vents; also, keep the monitor far away from any large potted plants since they can affect accuracy. For desktop use, keep the sensor away from a hot worklight, as heat can also affect the humidity reading.

An RH of less than \(20 \%\) can easily occur during the winter. And with such low humidity, a large quantity of static electricity can build up, since the conductive moisture usually present in the air isn't available to provide a discharge path for that energy.

Making sparks fly by touching your spouse's nose or a metal surface may be fun, but doing that to your computer (or just about any electronic device) could prove fatal for the machine. Similarly, you're much more likely to damage CMOS and other components while building projects like our humidity monitor. So, an RH of about \(50 \%\) is low enough for you to be comfortable, and high enough for your electronics to be safe.


MICHAEL F. VIOLETTE

\section*{This month, we show you how to head off interference before it enters your electronic equipment. We look at how to deal with power-line transients, too!}

Part 3electromagnetic interference (EMI) can couple into your sensitive equipment in two ways. One of those is via radiation. Last time, we saw some of the techniques that can be used to eliminate that coupling path.

The second way is through conduction. This month, we'll turn our attention to conducted interference and see the various ways that we can combat its harmful effects.

As we saw in Part 1 of this article (see the November 1985 issue of Radio-Electronics), there are two forms of conducted interference. In differential-mode interference, the interfering signal appears across two conductors, such as a pair of single lines or the hot and neutral sides of a power line. In common-mode interference, the interfering signal appears on both conductors, with the reference being a third point (such as a chassis ground). In common-mode interference, the interfering signals on the two conductors may not
be of equal amplitude, but they are in phase.
There are a couple of ways to deal with conducted EMI. One way is to carefully plan out and design any interconnecting cabling. Often, however, you have no control over such cabling (such as in the case of power or telephone lines). In those cases, the only alternative is to install a filter between the line and the equipment.

\section*{Cable design}

Cables can act like pickup and receiving antennas. When a cable is acting like a pickup antenna, a radiated FMI signal is converted into a conducted EMI signal, which travels on the wires in the cable, through shields, and right into your circuitry. Or, conducted noise (from the commutator in the blower motor of a hair dryer, for instance) finds its way onto the power cord, travels over the AC wiring, and then into the "victim's" circuitry through its power cord. Either way, the result is interference.

Your projects can either radiate or pickup EMI, or they can do both. In this i stallment, we will see how to deal w radiation of EMI, but the techniques discussed are also effective against EMI pickup in the vast majority of instances.

Last time, we saw that antennas can be classified by their impedance; of interest here are the low-impedance types. Let's see why.

In many circuits, the operating voltages are relatively low, often ranging between 5 and 24 volts; the impedance of the source of that voltage, the power supply, is generally low. To get an efficient radiator when the EMI antenna is being driven by a low impedance voltage source, the "antenna" must have a relatively low impedance. One type of low-impedance antenna is a simple wire loop. In a circuit or a project, that loop can take many forms. For instance, any loop of wire, such as the power feed and return to a disk drive, or the power supply and ground traces on a PC board can become a surprisingly effi-
cient radiator. Loops can also be formed by any signal-carrying conductors such as the data or clock lines.

Just how much energy is put out by those unintentional antennas? The radiated field strength of a loop is proportional to the product of the current in the loop, the frequency of the signal carried by the loop, and the area of the loop. All three variables play a role in determining how much energy is output. The current in the loop determines how much energy is available to drive the radiator. The area of the loop, and the frequency of the signal that is driving it, determine the efficiency of the radiator. Generally speaking, the greater the area of the loop and/or the frequency of the driving signal, the greater the efficiency of the radiator. The same is true of a "receiving antenna"-the larger the area of the loop and the greater the frequency of the received signal, the more efficient the pickup.

Obviously, the current and frequency are determined by the parameters of the circuit; that means that, in the majority of the cases, their values can not be reduced (or changed in any manner) without affecting the correct operation of the circuit. The area of the loop, however, is an entirely different matter.

That leads us to one of the most important rules for reducing EMI: When designing or laying out cable runs and printed-circuit board traces, keep the area of any potential loops small. Consider the simple circuit shown in Fig. 1. It consists of a signal source, a load, and a ground. The area of the loop is formed by the product of the physical distance separating the source and the load (L), and the physical distance separating the "signal" and "return" (grounded) leads (H).


FIG. 1-THE AMOUNT OF INTERFERENCE that a circuit can pick up or cause is largely determined by the physical size of the loop that it forms.

The circuit shown can be realized in a number of ways. For instance, the source could be the output of one gate while the load could be the input of a second gate. The return line could be a common bus or the chassis ground of the project. But no matter what form the circuit takes, under the right circumstances, the whole thing can become a large radiating loop.

The way to minimizing the radiation output by such a loop is to reduce the distance between the source and the load,
run the signal and return leads as close together as possible, or both. For instance, by running a dedicated return line alongside the the signal line, rather than having the return current flow through the chassis ground, the loop area is reduced significantly. With dedicated signal/return cabling, H is effectively reduced to the thickness of the insulation surrounding the two wires. That solution works, and it is much cheaper than using a shielded cable.

When dealing with differential-mode currents, using twisted-wire pairs for the signal line and the return can further help reduce interference. Twisting the wire pair significantly reduces the overall area of the loop. Each twist of the wires does form a small radiating loop, but the twisting causes the loops to be polarized in opposite directions. The result is that the radiation from one loop cancels out the radiation from the adjacent loop. The tighter the twisting the better the cancellation.

The foregoing assumes that the currents in the two lines of a twisted pair are bal-anced-that is, that they are equal in amplitude. The closer the lines are to being balanced, the better the reduction of dif-ferential-mode radiation and pickup.

Balancing also helps the circuit to reject the effects of common-mode currents. Let's assume that we are dealing with two lines feeding two inputs on an unbalanced receiver (a logic gate, etc.). Further, let's assume that the commonmode currents traveling in those lines are equal. Here, unbalanced means that the impedances, "looking" into the receiver, from all lines to ground are unequal. The result is that when the equal commonmode currents encounter the unequal impedances, unequal voltages are generated at the inputs. Those unequal voltages stimulate the inputs to the receiver, just as a normal signal is supposed to, and an undesired output signal can result.
That is a common problem. In fact IC op-amps have a parameter called the Common Mode Rejection Ratio (CMRR). That parameter is a measure of how well the op-amp can reject the effects of a common-mode input. The interference caused by common-mode currents is the reason that balanced line drivers/receivers are used in many cases for long signal-cable runs.

\section*{Crosstalk}

Crosstalk is caused by the mutual inductance and parasitic capacitance that exists between any two conductors (see Fig. 2). One common example of crosstalk is the phantom conversations that are often overheard when you speak on the telephone.

Though the parasitic capacitance and mutual inductance are shown lumped in Fig. 2, those quantities are distributed


FIG. 2-TWO PARALLEL LINES are electromagnetically coupled because of the mutual inductance and parasitic capacitance that exists between them. That coupling is what causes the phenomenon known as crosstalk.
along the length of the lines. Although there are no physical connections between the lines, the electrical coupling provided by the parasitic capacitance and parasitic inductance provides a path for the current in one wire to "enter" the other. At low frequencies, mutual inductance is the primary coupling mechanism; as the frequency gets higher, parasitic capacitance becomes the primary mechanism.

The closer the cables are together and the longer the parallel runs between the two cables, the greater the coupling. Because of that, the obvious and most inexpensive way to reduce crosstalk is to physically separate conductors that are likely to interfere with each other. Move conductors carrying high voltages (such as AC power cords) away from ones carrying low voltages (such as signal lines). In addition, keep input and output cables isolated from the wiring inside the equipment box. In addition, keep any parallel runs between conductors as short as possible. One way to do that is to make extensive use of twisted-wire pairs. Note that lines that cross at \(90^{\circ}\) angles have almost no mutual inductance and negligible parasitic capacitance (a couple of picofarads).

\section*{Shielded cable design}

Using twisted-wire pairs is an effective method of reducing differential-mode interference. To combat common-mode interference, shielded cable is more effective. To combat both types of interference, shielded cables are often made of twisted-wire pairs. In very noisy environments, "double-shielded" cables, in which the individual twisted pairs are also shielded, are used.

The effectiveness of the shielding depends on the nature of its ground. Or, more specifically, whether one end or both ends should be grounded. When dealing with low frequencies, it is usually sufficient to ground the shield at one end only (usually the receiving end). In addition, grounding the shield at both ends can cause a ground loop to be formed. By low frequency we mean that the shield is electrically short when compared with the wavelength of the EMI signal (i.e., the shield is shorter than approximately \(1 / 20\) of a wavelength). Under that condition, every point on the shield can be considered to be at ground potential. At higher frequencies, however, both ends must be
grounded. That grounding should be made directly to the chassis ground, and at the point where the cable enters the circuit enclosure.

The wide usage of flat ribbon cables in many types of circuits merits special consideration. When using ribbon cables, and when the circuit is especially sensitive to pickup of radiation (or when the circuit could potentially radiate), a return or ground conductor should be placed be-


FIG. 3-TO ELIMINATE CROSSTALK problems in ribbon cable, alternate conductors should be grounded.


FIG. 4-A SINGLE CAPACITOR, installed as shown, can be used to filter out differentialmode interference. To handle common-mode interference, the capacitor should be installed between each line and ground.
tween each signal line (see Fig. 3).
Often, that is not done. It is common for a flat ribbon cable to have 25 or more conductors, only one of which is a ground. That can lead to high capacitance between signal conductors, resulting in crosstalk problems. The presence of a ground or return line between each signal line will "shield" the signal lines from each other.

Finally, if you want to be totally free from EMI radiation or pickup from cabling, use fiber optics. Made of glass or plastic fibers, such cables neither radiate nor pick up electromagnetic energy. They are coming into wide use in applications where long runs of data cables operate in noisy environments. Telephone companies, for instance, are now making extensive use of fiber-optic cables.

\section*{Filters}

Even with good cable design and layout, sometimes a filter is needed to reduce conducted EMI. A filter is a circuit that allows certain frequencies to pass unimpeded, but blocks others. Depending on its design, a filter can allow only signals below a cut-off frequency to pass (lowpass filter), only signals above a cutoff frequency to pass (highpass filter), or pass or block signals within a given range or band (bandpass or notch filters, respectively).

A simple filter, consisting of but a sin-


FIG. 5-THIS FILTER NETWORK is effective against both differential-mode and commonmode interference.
gle capacitor, is shown in Fig. 4. At high frequencies, the capacitor looks like a very low impedance to ground. As a result, any high-frequency signals are shorted to ground. At low frequencies, the capacitor is a high impedance, and the signals are unaffected. Thus, the capacitor is acting like a low pass filter. Capacitors are used in that way in a number of applications, such as across the terminals of automotive alternators, electric motors, and on printed-circuit boards (decoupling capacitors).
To use any filter effectively, including the simple capacitor filter, you need to know the nature of the EMI you are dealing with. That's because a filter designed to eliminate common-mode EMI will be largely ineffective against differentialmode interference, and vice-versa.

Let's look at a familiar and common source of EMI and see what kind of interference it creates. An electric razor is one such source. Because the electric motor in the razor is connected between the hot and neutral sides of the power line, the conducted interference it generates is differential mode. That is, the EMI voltage appears between the hot and neutral lines. On the other hand, when an overhead power line acts as a receiving antenna, coupling a broadcast radio signal to the AC power line, for instance, the EMI signal that results is common mode.

To filter differential-mode interference, the capacitor must be installed between the hot and neutral lines. For commonmode interference, capacitors must be installed between each line and ground.

If the interference is both common- and differential-mode, and/or is made up of a variety of frequencies, a more complex filter network is required. Such filter networks are made up of capacitors, inductors, or both. One popular design is shown in Fig. 5. That filter is effective against both common-mode and differen-tial-mode interference. It is installed at the point where the power cord enters the equipment's enclosure.

The transformer in that circuit, T1, is a common-mode choke. As its name implies, its purpose is to block commonmode signals. The transformer is built in such a way that the flux generated by the common-mode signals in one winding opposes the flux generated by the com-mon-mode signals in the other winding. That results in a high impedance to common mode signals, but differential-mode
signals are not affected. That deficiency is taken care of by the series inductors, \(\mathrm{L1}\) and L2; those coils present a high impedance to the high-frequency differentialmode signals. The function of the capacitors in the circuit is to shunt differentialand common-mode signals to ground, as previously discussed.

If your interference problem is not severe, or if the victim circuit is not overly sensitive to EMI, you can build a circuit like the one shown in Fig. 6. In that circuit, or in any filter circuit, keep the capacitor leads as short as possible. A capacitor with long leads starts to behave like an inductor as the frequency goes up. Also, use a low-impedance ground connection (the "ground" connection of a


FIG. 6-IF THE EQUIPMENT is not overly sensitive to EMI, or the EMI situation is not severe, here's a practical circuit that will cure the problem.
three-wire power cord is suitable).
One common problem is the unwanted reception of AM radio stations or CB radios by tape decks, audio amplifiers, etc. That type of interference problem occurs when a radio signal is picked up by power or signal cables and causes a commonmode current to flow into the electronics. That type of problem can often be cured by placing a piece of ferrite around the cable as shown in Fig. 7. The effect caused by doing that is similar to the one caused by installing a common-mode choke. That is a quick and easy solution as it entails no soldering-just unplug the


FIG. 7-PLACING A PIECE OF FERRITE around a cable is one simple way to eliminate some types of common-mode EMI.
cable and install the ferrite. (It must be mentioned that that solution will do nothing if the interference is differential mode.)
Interference is filtered from signal lines in much the same way, assuming that the interference signal differs in frequency range from the intended signal. But if the EMI is in the same frequency range as the desired signal, there is little that you can do to reduce the conducted interference with filters. In those cases, the interference must be prevented from getting into the cables by using proper cable design and shielding.

\section*{Filter installation}

The important thing to remember when installing either a commercial filter or a home-made one is to isolate the unfiltered lines from the filtered ones. In carrying out such installation, keep the following pointers in mind:

Install the filter at the entrance of the power cord to the equipment box; do not allow the AC line cord to penetrate the equipment enclosure. Keep the ground leads from the filter as short as possible. For best results, mount the filter on a metal surface (aluminum foil will do) so that the input connection is made through that metal surface; the surface is then connected to the the ground wire of the equipment's AC power cord.

\section*{Transient suppression}

Anyone who has ever lost a batch of data or has seen his program "crash" when the lights dimmed knows the frustration that power-line transients can cause. Severe transients are capable of causing damage to electronic devices, especially members of the very sensitive MOS logic families. The most common types of transients on the power lines are as follows: sags (the power line voltage drops below the normal value for a short period of time); surges (the line voltage rises above the normal value for a short period of time); dropouts (the line voltage drops to zero for a short period of time); impulses (fast-acting conducted spikes of positive or negative polarity that are superimposed on the normal line voltage), and frequency changes (when the frequency of the line voltage deviates from 60 Hz ).

The duration of those types of disturbances, especially the first three, is usually very short, typically lasting for only a few cycles (at 60 Hz , the period of a cycle is approximately 16.7 milliseconds). They have a variety of causes. Some of those include lighting, starting and stopping of heavy machinery, and momentary line faults (short circuits to ground) in the power distribution system. Long term disturbances usually take the form of blackouts or brownouts.

For protection against the long-term
disturbances, the only recourse is some type of uninterruptible power supply. Those devices contain storage batteries and sensing circuits that switch the load to the batteries when the line voltage falls below a certain value.
There are, however, many ways to protect your equipment from the effects of short-term transients. The first step is to recognize that transients can have two very different sets of characteristics. That is, transients can be unidirectional or oscillatory. An oscillatory transient has a fast rise-time and then becomes a decaying sinusoid, oscillating at some frequency until it damps out. The unidirectional wave has a very short rise-time and a comparatively long fall-time. Once the transient drops to zero, it shows no oscillation. Of the two, oscillatory transients are more commonly encountered.
Transients are capable of doing quite a bit of damage; impulse and surge voltages as high as 6000 , and currents as high as 200 amperes have been observed. To prevent damage from occurring, the energy must be diverted before it enters sensitive electronics equipment. Power-line EMI filters will help to reduce some of the transient energy, but the amplitude of some transients can overwhelm the filters and cause damage. A device especially designed for transient suppression is needed.

There are a variety of devices that are useful in protecting your equipment from high-energy transients. Many of those are available commercially; you simply buy them and install them between the AC outlet and your equipment. They are generally satisfactory for most applications, if they are installed properly and if a good ground is used.

It is also possible to design and build an effective "home-brew" suppressor. The key components for such a device-gas tubes, varistors, and Silicon Avalanche Suppressors (SAS) -are readily available.

Basically, gas tubes consist of a pair of electrodes that are encased in a non-con-


FIG. 8-A CROSS-SECTION of a gas tube is shown in \(a\); its schematic symbol is shown in \(b\).
ductive (usually glass) envelope that contains an inert gas. See Fig. 8-a; the schematic symbol for the gas tube is shown in Fig. 8-b. One of the electrodes is connected to the hot line of the AC power cord, and the other is either grounded or connected to the neutral line. Normally, the presence of a gas tube in the line has no effect, but when a high-energy transient occurs, there is arcing between the two electrodes. That arcing is the dissipation of the transient energy. Lightning arrestors, installed on the utility companies lines, are usually made up of very large gas tubes.

Varistor is short for VARIable resiSTOR, which essentially describes the action of that device. Varistors have a high resistance at low voltages, but as the voltage increases, the resistance greatly decreases. In the low resistance state, the varistor is capable of handling a large increase of current without a large increase in voltage. Thus, the voltage on the line is "clamped." Varistors are inherently bipolar devices.

The level of the clamping voltage depends on the type of varistor and the energy rating of the device. The varistor, if not properly chosen, can be over-stressed, resulting in damage to the varistor and a lack of protection for your equipment. However, varistors are available in various ratings that can be matched to the job at hand. In addition, varistors react very quickly to transients.

SAS devices are most suitable for use on signal lines, low voltage lines, telephone lines, and at the circuit-board level. SAS's, which are very fast-acting devices, are essentially large area p-n junctions or "beefy" diodes. The device's characteristic curve resembles that of a Zener


FIG. 9-A HYBRID transient-suppressor circuit allows you to combine the advantages of two or more types of suppressor devices.
diode, but they can handle much more energy. Both bipolar and unipolar devices are available.

It is possible to combine the above devices into a hybrid suppression network. Such hybrid networks are useful because the designer can combine the advantages of two or more suppressor components, such as the energy-handling capability of a gas tube with the speed of an SAS. Such a hybrid network is shown in Figure 9. The isolating impedance is included in the network to limit the transient current into the SAS; it also causes the voltage to build up high enough to trigger the gas tube.
continued on page 114


THIS MINI-MUSIC SYNTHESIZER IS NOT TOO complex, but it is a load of fun to build and use. The ingenious little unit samples and holds the frequency of a signal from a microphone or other sound source and outputs that signal as a single note. The input "signal" can be a whistle, hum, or some other sound. The duration and amplitude of the note can be controlled. The frequency of the note can be halved or doubled with a flick of a switch. A vibrato effect can also be applied via a variable tremolo control. To add to all of that, two channels are available to give chorus or stereo effects. Each of those channels has a separate volume control.

The signal source is derived from a lowto medium-impedance microphone. That microphone can be almost any dynamic or electret type. The types used with lowcost cassette recorders will work well.

The synthesizer's output is connected to an external amplifier of some type. A home-stereo system would be suitable; feed the signal to the auxiliary or tuner input. A PA amplifier could also be used.

\section*{How it works}

The schematic of the synthesizer is shown in Fig. 1. As you can see there, its input section is basically a microphone amplifier and signal shaper with three stages. Twin Voltage Controlled Oscillators (VCO's) each with a Voltage

\footnotetext{
Adapted from a project that originally appeared in Dick Smith's Funways into Electronics, volume three.
}

Controlled Amplifier (VCA) output stage make up the main section. The remaining circuitry is a simple, adjustable low-frequency oscillator that's used as a tremoloeffects generator.

The first stage of the input section is made up of IC1-a and its associated circuitry. That stage has a gain of about 100 at 1 kHz . The op-amp circuit used in that stage is very basic. Note, however the presence of the capacitor in the feedback loop. That capacitor causes a roll-off in gain as the input frequency increases. The output of the amplifier is AC coupled to the next stage by C 7 .

The second stage consists of IC1-b and its associated circuitry. That stage has a gain of about 20. Like the previous stage, the high frequency response is limited by a capacitor in the feedback loop. That response tailoring has been done to reduce the normally rich harmonic content of the human voice; the only signal we want to process is the fundamental note. The harmonics are low in amplitude and can be partially rejected by the simple approach used in that circuit.

The last stage of the input section is the most significant and needs some explanation. The op-amp there, ICl-c, is configured as a Schmitt trigger with offset. The hysteresis components are R22 and R21. Normally a Schmitt trigger is bistable, and in the quiescent state the output would be either positive or negative depending on the last signal transition. The difference in this case is that the offset volt-
age at the inverting input is greater than the hysteresis voltage at the noninverting input; resistors R20 and R39 establish that offset. The purpose of the offset is to assure that the output at pin 8 of ICl is always low (negative) when no signal is present. The output will only switch when the input exceeds the sum of the hysteresis and offset voltages. The circuit acts as a gating system to the following phaselocked loop circuits.

The Schmitt trigger is the second stage of the processing that rejects the harmonics and noise in the input signal from the microphone. The input level, set by R41, is adjusted by turning that control until reliable response is achieved from a nor-mal-level input (such as a singing voice). It is not adjusted farther than that minimum amount. In that way, the Schmitt trigger will tend to switch only on the peaks of the fundamental. The levels of the harmonics and noise content of the signal are below the threshold switching points and will be rejected. The purer the voice or note, the more reliable the circuit action. A "gravelly" or "rough" voice thus will tend to prove unreliable as a signal source. The best results will be from a whistle, because that produces a note that is relatively free from harmonics of any amplitude.

From the output of IC1-c, the signal is split and fed to both the right and left channels. Since the circuitry in the two channels is identical, we will look at only one of those: channel 1 .


FIG. 1-SCHEMATIC DIAGRAM of the mini synthesizer. The potentiometers can be either panelmounted or PC-mounted.


\section*{* SEE TEXT}

FIG. 2-THE LOCATIONS OF ALL PC-board mounted components are shown here. To disconnect the microphone input stage, cut the trace between the pads marked with an asterisk.

From the Schmitt trigger, the signal is passed to IC2, a 4046 CMOS \(P\) hase Locked Loop (PLL). That IC consists of two separate circuits. One is a VCO that runs from subaudio to over 1 MHz . The other is a dual-output phase comparator. By adding just a few external components, a complete PLL can be formed. (For more information on the 4046, see the manufacturer's data sheet.)

Without an input signal, the output of the Schmitt trigger will be low, and therefore pin 13 of IC4-a, a 4016 analog switch, will also be low. That means that the analog switch will be off, and pin 13 (PLL COMPARATOR-OUTPUT 2 ) of the 4046 will be disconnected from pin 9, the input to the VCO. The voltage present on the lowpass filter capacitor (C5), together with the RC timing components ( C 3 and R5), will determine the frequency of the


FIG. 3-THE OFF-BOARD COMPONENTS should be connected to the PC board via wires.


FIG. 4-THE FINISHED PC-BOARD is shown here. Note the use of PC-mounted trimmers in this version.

VCO. The voltage across C5 will not dissipate for a long period of time, because of the very high input impedance at pin 9 of the 4046, and the low-leakage characteristic of that \(0.1-\mu \mathrm{F}\) polyester capacitor. That, in turn, means that the output frequency of the VCO at pin 4 will remain stable for a long period of time.

Now let us look at what happens when an input signal is present. Pins 13 of the analog switch and 12 of the PLL will go high. The analog switch is then on, and the phase-comparator output of the PLL (IC2, pin 13) will be connected to the VCO (IC2, pin 9) via the lowpass filter (R9, R10, and C5). Provided that the input signal continues at a fixed frequency for a
short period of time, the PLL will lock in and follow any frequency variations. For the most part, in that locked state, the pHASE PULSE output (pin 1) will be high (there will, however, be some narrow negative-going pulses).
With pin 1 of the 4046 high, D3 will be forward-biased via R8, so C6 will charge. The voltage across the capacitor is applied to the base of Q1, which acts as a voltage follower/buffer. That transistor is half of the VCA output stage, the other section being another 4016 analog switch (IC4b). The control gate (pin 5) of IC4-b is connected directly to the output of the VCO (pin 4 of IC2), or indirectly via the 4013 (a dual D flip-flop), depending on the

\section*{PARTS LIST}

Resistors
All resistors are \(1 / 4 /\)-watt, \(5 \%\), unless otherwise noted.
R1, R19, R21, R36- 1000 ohms
R2, R3, R8, R18, R26, R37-4700 ohms
R4, R6, R9-R11, R15, R17, R22, R23, R27, R28, R33, R34, R39-100,000 ohms
R5, R13, R24-47,000 ohms
R7, R16, R29, R30-1 megohm
R12, R14, R25, R31, R32- 10,000 ohms
R35, R36- 4.7 megohms
R40, R44-1 megohm, potentiometer, linear taper
R41, R42, R45- 10,000 ohms, potentiometer, audio taper
R43- 100,000 ohms, potentiometer, linear taper

\section*{Capacitors}

C1, C7, C11-0.47 \(\mu \mathrm{F}, 10\) volts tantalum C2- \(100 \mu \mathrm{~F}, 16\) volts, electrolytic
\(\mathrm{C} 3, \mathrm{C} 10-0.01 \mu \mathrm{~F}\), ceramic disc
C4, C8-120 pF, ceramic disc
\(\mathrm{C} 5, \mathrm{C} 12-0.1 \mu \mathrm{~F}\), polyester
C6, C9, C13, C14- \(2.2 \mu \mathrm{~F}, 16\) volts, electrolytic
C15-470 \(\mu \mathrm{F}, 16\) volts, electrolytic
Semiconductors
IC1-LM324 quad op-amp
IC2, IC3-4046 CMOS PLL
IC4-4016 quad analog switch
IC5-4013 dual D flip-flop
Q1, Q2-ECG123AP NPN transistor
D1-D4-1N4148 silicon diodes
Other Components
S1, S2-DP3T miniature slide switch
S3-S5-DPDT miniature slide switch
J1-miniature phone jack
J2, J3-phono jack
B1-9-volt battery
Miscellaneous: PC board, case, knobs, battery snap, wire, etc.
A kit of parts (Catalog Number K-2669) is available from Dick Smith Electronics, PO Box 8021, Redwood City, CA 94063. The kit includes the PC board, but not the case, the battery or the jacks. The price is \(\$ 19.95\).
setting of S1. The squarewave output from the VCO opens and closes the analog switch. It can be seen that that action directly gates the voltage available to the output terminal via the two current limiting resistors and the potentiometer.
When the input signal disappears, pin 1 of the 4046 returns low. As described previously, the VCO remains running at a frequency determined by the voltage on capacitor C5. The voltage across C6 then begins to discharge via the transistor follower and the two resistors, R12 and R40. Those two resistors control the "decay" rate. If R40 is set to its maximum value ( 1 megohm) the discharge time will be long (decay will occur slowly). As the discharge is taking place, the continuous output from the VCO switches the analog gate ICl-b on and off to "sink" the decaying voltage on the emitter of Q1 via the load resistor (R18) at the VCO rate.

The frequency of the note reaching the output stage can be changed from that of the original. By using a flip-flop as a di-vide-by-two element, the output can be halved or doubled depending on where it is coupled into the circuit. With the oc-tave-select switch (S1) in the center position (F), the output frequency will be identical to to input. When the \(\mathrm{F} / 2\) position is selected, the 4013 (configured as a clocked flip-flop) divides the output from the VCO (IC2, pin 4) by two (lower octave). In the 2 F position, the flip-flop is connected between the output of the VCO and the comparator of the PLL. That results in a frequency that is twice that of the input (upper octave) at pin 4 of the VCO.

So far we've only used three sections of the LM324 quad op-amp. The fourth section (IC1-d) is used as a tremolo generator. The op-amp is configured as an astable multivibrator to give a squarewave output. Potentiometer R43 is included to adjust the frequency (tremolo rate). The squarewave is then smoothed somewhat to give a more natural tremolo effect to the output note. That waveform is then applied to the VCO (at pin 12, IC2) via a selector switch and a 4.7 megohm resistor.


ONCE COMPLETED, the circuit should be installed in a case.

\section*{Optional inputs}

The VCA of either channel can be triggered from an external source. A positive pulse input via D2 to the VCA will charge C6. If that input is held high, the output from the VCA will also remain high. If the input is a pulse from a sequencer or even a simple switch, the output from the VCA can be triggered without the need for an audio signal at the microphone input. That could be used with rhythm generators, etc., to create different effects. The frequency of the VCO can still be changed by using the microphone input. If you require that the input signal from the microphone-amplifier stage not trigger the VCA, create an open circuit by removing D3 from the board. (The foil pattern for the project is provided in our "PC Service" department; the partsplacement diagrams are shown in Figs. 2 and 3.)

By disconnecting the input stage from the VCO, the PLL can be used separately by providing an external input signal. Isolation is performed by cutting the PC
board trace between the pads marked with an asterisk in Fig. 2. The input is applied via D1. That input should be a squarewave. The peak-to-peak voltage of the input should not greater than the circuit's supply voltage, and should not be less than \(75 \%\) of the supply voltage. If the input stage is disconnected from the PLL in the manner described, the circuit could be used to modify the output of an electronically amplified instrument or a sound generator (provided that the input signal meets the requirements of the 4046). For example, the output from a simple monophonic organ can be used to create more interesting tones and sound effects. The decay control could be used to vary the note shape, and the octave switch can change the note frequency.

To reconnect the PLL to the microphone input stage, the link destroyed when the PC trace was cut must be restored. That is done by installing a jumper between the pads marked with an asterisk in Fig. 2; for convenience, that jumper could be replaced by a switch.

\section*{Assembly}

Assembly is very straightforward if you follow Figs. 2 and 3. Start by mounting all of the low-profile components on the board. Those include the resistors, diodes, and the jumper. Next, install the capacitors. Be sure to observe the polarity of the electrolytics. After that, install the IC's and transistors, taking care to observe the proper orientation. Finally hook up the switches, potentiometers, and other off-board components. Note that the board has been designed to accept PCmount potentiometers, but you can use panel-mount potentiometers and connect them to the board with wire.

To test the system you will need to use an audio amplifier of some type. Feed the output of the synthesizer to the amp, plug in the microphone and battery, and turn the unit on. Whistle a few times close to, but not directly into the microphone. Turn up the microphone input level set control until some response is heard from the outputs. Set the volume controls of both channels to an appropriate level. Vary the pitch (frequency) of your whistle; the output should vary in kind.

Next, try varying the DECAY controls. With the controls set to maximum, you will hear the outputs change in frequency as the note of your whistle changes. Notes that are wide apart in frequency will take a little longer to lock. Now try changing the setting of the octave select switches. Also try out the TREMOLO RATE control; be sure that the tremolo section is switched into the circuit when you do that.

If all is working, the completed board (see Fig. 4) can be mounted in a case to complete assembly. If you detect any problems, go over your work carefully to find the cause of the problem.


THE UPPER TRACE is a \(3-\mathrm{kHz}\) input signal from a signal generator. The bottom one is the output that results.
(Photo courtesy of Tektronix)

\section*{Operation}

Connect the microphone and switch the unit on. Now slowly whistle a tune a short distance from the mouthpiece. Do not blow directly into the microphone. Best results are obtained with the microphone at the side of your mouth so that any air currents do not hit it directly.

Turn the input-level control up until you get a reliable response from the unit every time a note is whistled at the same volume. Turn the output-volume controls to a suitable level to avoid feedback. The whistled note should be a short, clean burst. Try changing the decay controls so that the generated sound varies from a short staccato to a long, slowly-decreasing tone.

Note that the synthesizer is sensitive to all sound. Thus, when the microphone picks up the output sound of the amplifier, feedback occurs and the system locks up in an uncontrolled state. To avoid that, you must keep the microphone away from the speaker system.

When you find a suitable input level, try the other functions. By changing the octave select switches, the output frequency can be set to be the same as the input, or one half of the input, or twice the input. By adding tremolo to one or both channels, interesting tonal effects can be achieved.

Try singing or humming into the microphone as the sound source. The system will respond best to pure, clean notes. Rough voices will not be reliable.

By striking different shaped objects next to the microphone, the system will tend to pick up the fundamental resonance of the object and produce an equivalent note.

Musical instruments can also be used as the sound source. A simple recorder, for instance, can produce many and varied sounds. Other instruments, such as a guitar, can produce different notes and sounds. The combinations are endless.

To give greater versatility to the unit, channel one can be controlled from external sources as described above. With a little practice, this mini synthesizer can make you a "one-man band."


Compact disc players are the most exciting development in audio in years.
In this article we'll show you how those devices work, and how you can repair them when something goes wrong.

Part 4Last time, we began to look at how to adjust CD players. The first adjustment we talked about was the laser output adjustment. Unfortunately, we ran out of room before we could finish that topic, so let's do that now and then move on to the rest of the CD player adjustments.

The simplest way to check laser-diode drive current is to measure the voltage across a resistor in series with the diode, such as R623 in Fig. 20- \(a\), and then calculate the drive current. For example, if the recommended laser diode current is 40 to 70 mA , and the series resistance is 22 ohms, the voltage should be between 0.88 and 1.54 . That check can be done before
you adjust the laser diode, and must be done after adjustment.

Some literature recommends monitoring the laser with a light meter. However, it is more practical (and much easier) to adjust the laser diode output until you get an EFM (Eight-to-Fourteen Modulation) signal of correct amplitude, as we discuss next. Before you make the adjustment, set R629 to minimum, and then increase the setting as required. Also, note that the chuck or chu switch (S3) must be in the closed (tray in) position before power is applied to Q601 and the laser. (That eliminates the need for separate interlocks.) Of course, you will probably find it necessary to override that interlock (close S3
manually) during adjustment and troubleshooting.

First, connect the oscilloscope as shown in Fig. 20-a. That allows you to monitor the EFM signal (after the photodetector output is preamplified by IC601). As discussed, the EFM signal (at that test point) is applied to the tracking, focus, and pickup motor servos, as well as to the signal processing circuits.

Next, load a disc in the player and select the PLAY mode. The EFM signal should appear on the scope, and produce a waveform similar to that shown in Fig 20-b. Finally, adjust R629 until the EFM signal level is 700 mV (or as specified in the manual, typically 550 to 950 mV ).


FIG. 20-THE SIMPLEST WAY to check laser diode drive is to measure the voltage across a resistor in series with the diode, such as R623.


FIG. 21-WHEN THE slide-motor offset adjustment has been completed, be sure to remove the temporary ground.

\section*{Slide-motor offset}

The slide-motor offset adjustment sets the point where the pickup accesses the beginning of the disc (the dise directory). If the adjustment is not correct, the program information may not be read properly. Note that that adjustment controls the pickup motor servo, and is not to be confused with the inner-limit microswitch, although the two adjustments are interrelated. However, if the microswitch is properly set, you should have no trouble in setting the pickup servo offset, as follows.

First connect a DC voltmeter as shown in Fig. 21. That allows you to monitor the
motor gain output from IC101.
Next, load a disc in the player and select the play mode. While the disc is playing, ground IC301, pin 11 as shown in Fig. 21 to simulate a low TSw signal. (If the TSW line is high, IC301 shuts the system down.) Finally, set the player to stop. After about 10 seconds, measure the DC level at TP15 and adjust R107 so that the reading is \(0 \mathrm{~V}( \pm 50 \mathrm{mV})\). Adjust R107 in small increments and wait for the voltage level to stabilize before proceeding.

\section*{Tracking-servo offset}

Figure 22 is the tracking-servo offset adjustment diagram. Here, we are adjust-
ing the optical pickup (through the servo and tracking actuator coil) so that the laser beam is properly centered on the tracks, producing maximum EFM. The efm signal is monitored via an oscilloscope connected to TP13. Note that R603 sets the offset of the two tracking diodes, but not the four remaining focus/signal diodes.

As before, begin by loading a disc in the player and selecting the PLAY mode. Then adjust R603 until the efm is of maximum amplitude. That indicates that the laser is properly centered on the tracks. On some players, the display may become erratic and the audio will mute after that adjustment is made. If so, set the player to stop and then go back to play. That should eliminate the erratic display.

\section*{Focus servo offset adjustment}

Here, you are again monitoring the EFM signal, but you are now adjusting the optical pickup (through the servo and focus actuator coil) so that the laser beam is properly focused on the tracks. Connect an oscilloscope to TP13 (see Fig. 23) and adjust for the maximum EFM signal. Note that R116 sets the offset of the four focus/ signal diodes, but not the two remaining tracking diodes.

The first step is to load a disc in the player and select the PLAY mode. The EFM waveform should appear on a scope connected to TP13.

Next, adjust R116 for the maximum EFM signal (that indicates that the optical pickup is focused on the tracks). Again, if the display becomes erratic after this adjustment, stop and restart the player.

\section*{Disc motor Hall gain balance}

The adjustment diagram is shown in Fig 24. In that set up, one channel of a dual-channel scope is connected to TP18, while the other is connected to TP17. That allows you to simultaneously monitor the drive signals to both coils ( A and B ) of the disc motor (from the motor drive, IC201).

Start by loading a disc in the player and selecting the play mode. Adjust R201 so that the output levels at TP18 (DMCA) and TP17 (DMCB) are equal. Usually, DMCA and DMCB are about 2 volts ( \(\mathrm{p}-\mathrm{p}\) ).

\section*{Sample-and-hold offset adjustment}

The adjustment diagram is shown in Fig. 25. Note that this adjustment is not available on all players. Also the circuits shown in Fig. 25 are not to be confused with the sample-and-hold audio circuits. The S/H circuits we are dealing with here are part of the pickup servo, IC101, and control the tracking error or TER signals.

To perform this test, we must create a simulated defect on a disc. The disc is then played and R103 adjusted for minimum audio dropout. The simulated defect is created by placing a strip of black (nonreflective) tape, about \(0.5-\mathrm{mm}\) wide, on the mirror side of the disc.


FIG．22－WHEN THE tracking－servo offset adjustment has been performed correctly，the laser beam is properly centered on the disc tracks and the efm signal is at a maximum．The efm signal can be monitored on an oscilloscope connected to TP13．


FIG．23－CONNECT AN OSCILLOSCOPE to TP13 and adjust R116 for maximum EFM signal．


FIG．24－ADJUST R201 so that the levels of the drive signals to the two coils are equal（about 2 － volts \(p-p\) ）．

You can make this adjustment by ear． The simulated defect produces a chatter－ ing or ticking in the audio．You simply adjust R103 for minimum noise．Of course，monitoring the EFM signal with a
scope（via TP13），looking at the amount of audio dropout，is generally more accu－ rate．Incidently，do not turn the volume up with a simulated defect．The noise is un－ bearable！

If you monitor the EFM on a scope，note that with such a defect，a portion of the EFM display will be cut out（typically a notch or wedge，starting from the top），no


FIG．25－HERE，you are adjusting the sample－ and－hold circuits contained within IC101，not the audio sample－and－hold circuits．
matter how you set R103．However，you should be able to eliminate all（or most）of the audio dropout，as indicated by a cutout at the bottom of the efm display．If you get considerable dropout at all R103 settings， IC101 may be defective．

\section*{Troubleshooting CD players}

As with adjustments，troubleshooting procedures found in CD player manuals are limited at best．They give you the usual ＂troubleshooting tree＂with some test points to check，but no hint as to what other components are involved．With the following material，we will attempt to provide you with that important informa－ tion．

The following sections are a collection of trouble symptoms that match the trou－
bleshooting－tree found in the service manual for a typical CD player．After se－ lecting the symptom that matches that of the player you are servicing，follow the steps in the corresponding troubleshoot－ ing procedures．

\section*{Preliminary checks}

It is always a good idea to make a few preliminary checks before you tear into the player．Here are some examples．

The transit or shipping screw must be removed or loosened before the player can operate normally．Be sure that that has been done．

Cleaning the objective lens should be a routine part of servicing．A dirty objective lens can cause a variety of symptoms（in－ termittent or poor focus，skipping across the disc，erratic play，and excessive drop－ outs，to name a few）．Also，those same symptoms can be caused by a defective disc．Try a known good disc before start－ ing service．

Do not replace the pickup assembly，or make any adjustments on the pickup，be－ fore checking for mechanical problems that can affect the pickup．For example， look for binding at any point in the pick－ up＇s travel（indicating that the rails or guides are adjusted too tight．）On the other hand，if you hear a mechanical ＂chattering＂when the pickup is moved， the rails may be too loose．

It is assumed that if you have such ob－ vious symptoms as＂none of the front－ panel LED＇s light when the POWER switch is pressed＂you will check the fuses（right after you have checked that the power cord is plugged in）．It is also assumed that you will check for bad solder connections， broken foil traces，loose or unseated boards，and the like（cleaning the module and board contacts with a pencil eraser，as necessary）．Finally，you should check the various power supply voltages if entire sections（or functions）appear to be in－ operative．That means that you must lo－ cate the power－supply circuits on the schematic and／or block diagram，and measure the voltages．（If you can not do that，you should not even attempt to ser－ vice a CD player yourself！）

Now，let＇s go on to the symptoms，and their cures！

1．Tray does not open or close prop－ erly．Figure 26 is the troubleshooting di－ agram．If the tray will not open or close， first check that the system micro－ processor，IC301，is getting signals from the front－panel OPEN／CLOSE Switch，S318． If not，suspect S318 and／or the wiring between S318 and IC301．Next，check that the loading motor receives a signal from pin 12 of IC102 when S318 is pressed．If so，suspect the motor；if not， you have a problem between IC301 and the motor（through IC102）．

Next check for signals at pins 10 and 11 of IC102 each time S318 is pressed，and
that the signals invert (pin 10 high and 11 low, then vice versa). Check for corresponding inverted signals at pins 33 (OPEN) and 34 (Close) of IC301. If absent, or if the signal does not invert when S318 is pressed, suspect IC301.

If the tray opens, but not fully, check the point were S02 (Lido) actuates, as indicated by a low-to-high change at pin 48 of IC301. If necessary, adjust S02. First check for any condition that might prevent the tray from opening fully.

If the tray opens fully, but the loading motor does not stop, the problem is almost always an improperly adjusted S02, although IC301 could be at fault.

If the tray closes, but not fully, and the clamp or chuck does not hold the disc in place on the turntable, check the point were the chu switch, S03, actuates, as indicated by a high-to-low change at pin 47 of IC301. If necessary, adjust S03. First check for mechanical problems.


FIG. 26-IF THE TRAY does not open or close properly, use this diagram to help pinpoint the cause.

If the tray closes, and the clamp or chuck goes fully down, but the loading motor does not stop, the problem is likely an improperly adjusted S03, but the problem could be IC301. Look for a high-tolow change at pin 47 of IC301, which should occur when the tray is fully in, and the clamp is down on the disc.
2. Laser-diode problems. Figure 27 is the troubleshooting diagram. CD player operation depends on the laser producing a beam of the correct level. If the beam is absent, there is no EFM signal. If the beam is weak, the EFM signal is weak. If the monitor diode does not monitor the laser diode properly, the beam can shift to an incorrect level (high or low), without that event being sensed by the laser-drive circuits. Any of those conditions can cause improper tracking which, in turn, can produce an even weaker EFM.

So, if you have symptoms with no apparent cause, such as improper tracking that can not be corrected by adjustment, excessive audio dropout with a known good disc, etc., suspect the laser circuits.

If the laser diode appears to be in-


FIG. 27-LASER CIRCUIT PROBLEMS can cause a variety of malfunctions.


FIG. 28-IF THE PICKUP does not move to the inner limit when power is first applied, this troubleshooting diagram can help pin down the cause.
operative, check if Q601 is getting +5 volts through S 03 (CHU). If not, suspect S03 (or the adjustment of S03). When S03 is in the open position, +5 volts is applied to pin 47 of IC301 to show that the tray is open and/or the clamp is not fully down. That disables a number of IC301 system control functions including LASW. When the clamp is fully down, S 03 moves to the closed position, and the laser diode receives power through Q601.

If power is applied to the laser diode, look for an LASW signal (indicated by a low) at pin 51 of IC301 (TP14). If that signal is absent (pin 51 of IC304 is high), suspect IC301. If present, check for a signal at IC604 from the monitor diode. If abnormal, suspect the monitor diode and/ or R629.

If signals are present at both pins 5 and 6 of IC604, look for drive signal at pin 7 of IC604, and the base of Q601. If absent, suspect IC604. If present, suspect Q601.
3. Pickup does not move to inner limit when power is applied; disc directory is not read properly. Figure 28 is the troubleshooting diagram. When power is first applied, the pickup moves to the inner limit. The system microprocessor applies a temporary SLR (reverse) signal to the pickup servo. In Fig. 28, a reset signal is generated by the circuit made up of Q103, Q104, Q301; it is applied to pin 24 of IC301. That produces a temporary SLR signal at pin 60 of IC301, which is applied to the motor through IC101, IC604, and IC102. That signal causes the pickup to move inward until the inner-limit LMSw switch, S01, is actuated.

If the pickup appears to move to the inner limit when power is applied, but the disc directory is not read properly (for instance, the total playing time, or number of programs on the disc is not given on the front-panel display), try correcting the problem by adjusting the motor offset (as described earlier) before ripping into the motor servo circuits.

If the pickup does not move when power is first applied (you may not be able to see the pickup, but you should hear the motor) check for SLR at pin 60 of IC301. If absent, suspect IC301, or Q301 and the reset circuit. If you get SLR, but the motor does not run, suspect IC101, IC604, IC102, and the motor itself. Also check for motor drive voltage at the output of IC102 and at the motor. If the motor runs, but the pickup does not move, look for mechanical problems.

If the pickup moves, but does not reach the inner limit, check when \(\mathrm{SO1}\) actuates, as indicated by a high-to-low change at pin 46 of IC301. If necessary, adjust S01. Before adjusting S01, check adjustment of the pickup servo offset. If the offset can be adjusted so that the pickup accesses the disc properly, S 01 is probably adjusted correctly. Generally, S01 does not go out of adjustment.

If the pickup reaches the inner limit, but the motor does not stop, the problem is almost always one of an improperly adjusted lmsw switch (unless IC301 is defective).

That's all we have room for now, Next time, we'll finish up the troubleshooting procedures, and this article.

\section*{All About TRANSISTOR SWITCHES \\  \\ L. B. CEBIK \\ It's easier than you think to control motors, relays, and lightbulbs with digital IC's! \\ Learn how here.}

\section*{Transistor basics}

Before we get into the details of switch-ing-circuit design, let's review fundamental transistor operation. The base of a transistor is used to control the current flowing through its emitter and collector. If we block current flow to the transistor's base, collector current will cease. When that happens, we say the device is in cutoff.

On the other hand, as we supply more and more current to the base of a transistor, its collector current increases at a corresponding rate. Eventually we reach a point where additional base current causes no corresponding increase in collector current. At that point we say that the transistor is in saturation. Between cutoff and saturation is the linear operating region.

Circuits using transistors as small-signal amplifiers often try to avoid operating in either cutoff or saturation, although some designs do utilize one or the other extreme (Class-C RF amplifiers, for example). By contrast, transistors used as switches attempt to avoid operating in the linear region. We try to switch a transistor from cutoff to saturation, and from saturation to cutoff, instantaneously. Of course, that is impossible, but if we make the transition period short enough, it will appear to be instantaneous. Overdriving a transistor-that is, forcing it "hard" into
saturation-can adversely affect switching speed by increasing junction capacitance, so we must be careful not to apply too much base current to a switching transistor.
Calculating just the right amount of base current comprises the bulk of the work in switching-transistor circuit design. Let's see how to do that now.

\section*{Single-transistor switch}

We often make the base current a function of an applied voltage. In Fig. 1-a, resistor \(R_{B}\) is wired in series with transistor Q1's base. As shown in the upper trace of Fig. 1-b, when an appropriate voltage \(\left(\mathrm{V}_{\mathrm{IN}}\right)\) is applied to \(\mathrm{R}_{\mathrm{B}}\), Q1 will turn on. At that point, as shown in the middle trace, collector current becomes appreciable, and collector voltage drops to (almost) zero, as shown in the bottom trace.


FIG. 1-THE VALUE OF THE BASE RESISTOR in this switching circuit depends on the value of \(\mathrm{I}_{\mathrm{C}}\) and the gain of the transistor.

We can use Ohm's law to calculate the value of the base resistor: \(R_{B}=V_{B} / I_{B}\). Assuming we're working with a standard logic family, we'll know \(\mathrm{V}_{\mathrm{B}}\) : the output voltage of the IC driving our switch. For example, standard TTL IC's have an output of about 2.4 volts DC. In order to calculate the value of \(\mathrm{I}_{\mathrm{B}}\), we need to know the amount of current ( \(\mathrm{I}_{C}\) ) our load ( \(\mathrm{R}_{\mathrm{L}}\) ) requires. We can determine that current easily using standard data books, or by "guesstimate," if necessary, and refine our calculated value on the breadboard later. Catalogues often give the current ratings of lamps, buzzers, and other devices. Relays (and other components) may be given only a resistance rating, but, by using Ohm's law, we can easily calculate the load current. So, half the battle is won without a struggle.

We also need the current gain, \(h_{\mathrm{FE}}\), of

\section*{TABLE 1-COMMON SWITCHING TRANSISTORS}
\begin{tabular}{|c|c|c|c|c|}
\hline Number & Type & \(\mathrm{V}_{\text {C(MAX) }}\) & \(\mathrm{I}_{\mathrm{C} \text { (MAX) }}\) & \(\mathrm{H}_{\text {FE }} @ \mathrm{I}_{\mathrm{C}}\) \\
\hline 2N2219 & NPN & 40 & 500 & 30 (500) \\
\hline 2N2222 & NPN & 40 & 500 & 30 (500) \\
\hline 2N2907 & PNP & 60 & 500 & 35 (500) \\
\hline 2N3638 & PNP & 25 & 500 & 20 (300) \\
\hline 2N3702 & PNP & 40 & 500 & 60 (300) \\
\hline 2N3704 & NPN & 30 & 100 & 100 (50) \\
\hline 2N3904 & NPN & 40 & 100 & 30 (100) \\
\hline 2N3906 & PNP & 40 & 100 & 30 (100) \\
\hline 2N4400 & NPN & 40 & 100 & 20 (500) \\
\hline 2N4402 & PNP & 40 & 500 & 20 (500) \\
\hline 2N5400 & PNP & 130 & 100 & 40 (50) \\
\hline
\end{tabular}
the transistor we 'll be using; \(h_{\mathrm{FE}}\) may usually be obtained from a data book. The gain of commonly-available transistors may range anywhere from twenty to two hundred. If you don't know the gain of the transistor you'll be using, or if you use surplus or unmarked transistors, assume that \(h_{\mathrm{FE}}\) for high-power devices is twenty, and that, for low-power devices, it is forty. Don't worry about being exact; using those values will allow you to get started, and we can optimize resistor values on the breadboard. And since gain ratings are often listed as "typical" or "minimum," we'll probably have to experiment a little anyway.

As long as we've got the data book open, let's check one other rating to ensure that the transistor we're using will be able to do the job. The maximum collec-tor-to-emitter voltage ( \(\mathrm{V}_{\mathrm{CEO}}\) ) of the transistor is important, because, when it is in cutoff (that is, when no current is flowing through the load), the full supply voltage will appear across the collector and the emitter. A common rule of thumb is that a transistor should be able to withstand at least twice the maximum voltage that will appear across it. In most CMOS and opamp circuits, the maximum supply voltage will be fifteen volts. It's easy to find thirty-volt transistors, and they'll handle five-volt TTL devices with ease.

Assuming the transistor meets our load's voltage and current requirements, we can use \(h_{\mathrm{FE}}\) to calculate the base current that we need to turn the transistor on. As you recall, base and collector currents are related by \(h_{\mathrm{FE}}: \mathrm{I}_{\mathrm{B}}=\mathrm{I}_{\mathrm{C}} / h_{\mathrm{FE}}\). We then plug \(I_{B}\) into our previous equation and calculate \(R_{B}\). To account for variations among transistors, add about twenty percent to the base current before calculating the value of the base resistor. (Or simply increase the calculated value of \(\mathrm{R}_{\mathrm{B}}\) by twenty percent.) Now we're just about ready for the breadboard.

However, there's one other thing to check. We must ensure that the (digital or other) device driving our switching circuit can safely supply the calculated base current. Safety is not really a question of burning out the driving IC; rather, we want to ensure that we don't force that IC
to operate unreliably. TTL IC's (and some special IC's like the ubiquitous 555) can usually supply all the current necessary. Regular TTL IC's can supply sixteen mA ; low-power TTL IC's can supply twenty mA ; and low-power Schottky IC's can supply eight mA . You should treat those values conservatively, but for many applications, there will be more than enough current to drive a simple switch like the one we're discussing.

On the other hand, CMOS IC's and opamps are voltage-operated devices; they are able to supply very little current. The amount of current CMOS devices can deliver varies with supply voltage; it ranges from about one mA at five volts, to four mA at ten volts. Likewise, op-amps can supply only a milliamp or two. If more current is required, a somewhat more complicated circuit must be used. But more on that in a minute. For now, let's take a look at some real-world transistors and their ratings.

\section*{Transistor ratings}

To help you choose an appropriate switching transistor, in Table 1 we list maximum voltage and current ratings for several common devices, along with typical current gains at specific collector currents. Manufacturers' data books will contain more specific information, but the information in the Table should be enough to get you started. In general, almost any transistor that can withstand the required voltage and current can be pressed into switching service, but it is wasteful to use a one- or two-dollar transistor when a tenor twenty-cent device will suffice.

The circuit shown in Fig. 1 is useful when you need to control a device with modest voltage and current requirements, when the driving circuit can supply enough current to reliably turn the switching device on and off, and when you are working with a positive supply voltage. If you were working with a negative supply, all you would need to do in order to make that circuit functional would be to substitute an appropriate PNP transistor for Q1.

In that case, the base-voltage trace in Fig. 1- \(b\) would be inverted. For example,
\(\mathrm{V}_{\mathrm{B}}\) would normally be "high" (zero volts); to turn on the transistor (and thereby the load), a negative voltage would be applied to the base. The collector-voltage trace would normally be low; it would go high when the transistor were turned on. The collector-current diagram would not change, as it indicates the magnitude, not the direction of current flow.

For most of the remainder of this article we will discuss circuits with positive supply voltages, so, if you are designing with a negative supply, just substitute transistors of the opposite type for those shown.


FIG. 2-THIS INVERTED-POLARITY switching circuit applies current to the load when \(V_{\text {IN }}\) is low. But see the text for precautions on use of this circuit.

\section*{Polarity inversion}

Sometimes we want a low from the controlling device to enable current flow, and a high to disable it. The circuit shown in Fig. 2- \(a\) will do just that. As the traces in Fig. 2- \(b\) reveal, when the base voltage goes high, collector current ceases, and collector voltage drops to zero. There is one precaution to observe when using that circuit: Make sure that the signal driving the switch goes high enough to cut it completely off. Op-amps and some logic devices may have outputs as much as twenty percent below the supply voltage. That may allow the switch to remain on constantly, as \(\mathrm{V}_{\mathrm{BE}}\) will not go below the 0.7 volt difference necessary to place the transistor in cut-off. Only a breadboard test will tell for sure.

\section*{Two-transistor switches}

Sometimes a single transistor switch just won't meet our design requirements. For example, suppose that the current required by the load exceeds the current that our single-transistor switch can supply. Or perhaps we need to switch a high volt-age-or even one of the opposite polarity.


FIG. 3-THIS TWO-TRANSISTOR CIRCUIT can switch heavier loads than the previous circuits.

Perhaps we want to invert the driving signal, and the circuit of Fig. 2-a is just not reliable enough. The solution is to add a second transistor as a kind of "buffer" or "pre-amplifier" for the main switch. There are several ways of doing that.

The circuit shown in Fig. 3- \(a\) is an inverting controller. A high at \(\mathrm{V}_{\text {IN }}\) turns Q1 on and Q2 off, and thereby prevents current from reaching the load, \(\mathrm{R}_{\mathrm{L}}\). The voltage and current traces in Fig. 3-b illustrate how that works. When Q1 turns on, its collector goes low, and current flows through \(\mathrm{R}_{\mathrm{C}}\). However, \(\mathrm{R}_{\mathrm{B} 2}\) is also brought low, so Q2 is cut off. Hence its collector goes high, and collector current stops flowing.
To design such a circuit, we work backward from the load. After determining the voltage and current required by the load, we select a transistor for Q2 that can withstand those values. We then determine Q2's required base current using the gain equation listed above. In our one-transistor circuits, that current was supplied by the driving device; now it is supplied by Q1. The base resistor for Q2 is really the series combination of \(\mathrm{R}_{\mathrm{C}}\) and \(\mathrm{R}_{\mathrm{B} 2}\). However, in applications where Q1 only supplies current to \(\mathrm{Q} 2, \mathrm{R}_{\mathrm{B} 2}\) may be omitted. The driving voltage, \(\mathrm{V}_{\mathrm{B}}\), is the supply voltage feeding \(R_{C}\).

Note that \(\mathrm{R}_{\mathrm{C}}\) determines the current that flows through Q1 when it is on, and that is one place where many designers
mistakenly allow too much current to flow. Unless you're dealing with very high-powered circuits, a few milliamps will suffice. And if we're using the circuit only to perform "inversion" (not to supply high current or voltage), even less will suffice. The same formula may be used to calculate Ql's base current; using Ohm's law we can calculate the value of \(\mathrm{R}_{\mathrm{B} 1}\). Even with an additional twenty-percent of base current, our driving IC does not have to supply very much current.


FIG. 4-THIS TWO-TRANSISTOR CIRCUIT can control heavy loads, but, like the circuit shown in Fig. 2, several precautions must be observed.

The circuit shown in Fig. 4- \(a\) is an extension of the one shown in Fig. 2, and it is subject to the same caution. In order for Q1 to be cut off completely, the driving voltage must closely approach the supply voltage. However, the lighter load on the driving device provided by the revised circuit may make that possible. As illustrated in Fig. 4-b, when \(\mathrm{V}_{\text {IN }}\) goes high, Q1 is cut off, so no current flows through its collector. The base of Q2 now goes low, so Q2 turns on. It's collector voltage goes high, and collector current flows.

By combining an NPN with a PNP transistor, as shown in Fig. 5, we gain several advantages, including the sensitivity a two-transistor circuit can provide, and an output that follows the input-no inversion. The traces in Fig. \(5-b\) illustrate circuit operation. When \(\mathrm{V}_{\mathrm{IN}}\) goes high, Q1 turns on. Its collector goes low as current flows through the device. That drops Q2's base to ground, so that


FIG. 5-POSSIBLY THE BEST all-round twotransistor switching circuit, this design does not invert the control signal.


FIG. 6-NEGATIVE VOLTAGES may be switched by this circuit, which is driven from a positive supply.
transistor now turns on. Its collector goes high, and current flows through Q2 and the load.
The two-transistor circuits shown in Fig. 4 and Fig. 5 have an additional advantage. In both cases, the loads are not connected directly to the supply line, but through the collector of the output transistor. That could be useful if you wanted to control a single load with several different switches. Connect the collectors of all the Q2 transistors together, make sure that there can never be more than one switch on simultaneously, and you're ready to go. That configuration is com-
monly referred to as "wired-OR," since a logical or function is achieved simply by wiring outputs together.

\section*{Switching negative voltages}

A circuit that switches negative voltages, but that operates from a positive supply, is shown in Fig. 6. It is useful for tasks ranging from switching keyingtransmitter lines to providing negative retrace voltages for oscilloscopes. The circuit functions as follows.
When Q1 is off, its collector is high, and so, therefore, is Q2's base, so Q2 is cut off. To turn on Q 2 , its base must go 0.7 volts more negative than its emitter. Diodes D1 and D2 clamp the emitter at about +1.4 volts, so the base must fall below at least 0.7 volts in order for Q2 to turn on. That provides some insurance against accidental turn-on due to leakage through Q1. In a real-life version of that circuit, \(\mathrm{R}_{\mathrm{C} 2}\) would actually be part of the transmitter keying line. For heavier loads, an additional transistor could be used.


FIG. 7-A DARLINGTON TRANSISTOR may be used to obtain high gain in a single package.

Another way to switch moderate loads is with a Darlington device, such as that shown in Fig. 7. Connecting two transistors in that fashion provides a simple way of attaining very high gain, so that low-power drivers-like CMOS IC'scan drive relatively heavy loads. The 2N6576 and 2N6388 are typical NPN Darlingtons. To use a Darlington, just make sure that it can supply the required voltage and current, and then calculate the value of \(R_{B}\) just as with the simple onetransistor switch of Fig. 1.
There are many variations on the circuits presented above that are used in special applications. But the heritage of most such circuits can be traced to one (or more) of those presented here. So, having mastered the basic theory, you should be able to understand just about any interface circuit you might encounter. And now that you've got the theory down, let's get out our breadboards and learn how to test and refine discrete transistor switching circuits.

\section*{Practical considerations}

What we'll do now is give step-by-step instructions on how to test and optimize


FIG. 8-SWITCHING CIRCUIT OPTIMIZATION is simple with this set-up. The value of the driving voltage is set with R1, and Q1's optimal base current is found by varying R3.
several of the circuits we've already discussed (those shown in Fig. 1 and Fig. 5). Once you understand the processes we describe, you should have no trouble adapting those circuits to the requirements of the devices you are driving.

A single-transistor switching-circuit test set-up is shown in Fig. 8. Switch S1 may actually be a jumper, and R3 is a potentiometer with a value of 25 K or 50 K ohms; it is used to adjust the current fed to Q1. The voltage divider composed of R1 and R2 gives you a convenient means of simulating the voltage that will actually drive the circuit. The two terminals in Ql's collector circuit (connected by a dashed line in our figure) indicate that you'll need to provide some way of inserting an ammeter. The component designated \(R_{L}\) may be the actual device that will be switched, or a resistor that draws the same amount of current.

Our design goal is to find the highest value for \(R_{B}\) that provides reliable switching. First calculate the base current that is required, according to the load current and the known (or assumed) gain of the transistor. If the driving source will be a lower voltage than the supply line, set the voltage that appears at the lower terminal of S1 with R1. Now follow these steps:
1. With all components hooked up, and with \(+\mathrm{V}_{\mathrm{CC}}\) applied, gradually increase the value of R3 until the collector current \(\left(\mathrm{I}_{\mathrm{C}}\right)\) begins to drop. Some switching transistors have narrow linear ranges and may appear to drop out completely with only a small change in R3.
2. Reduce R3 slightly, to ensure reliable switching, and test through several onoff operations. If the transistor appears to switch reliably, measure and record the value of R3.
3. If the driving voltage may vary, perform steps one and two twice: once with \(V_{B}\) set for the upper, and once for the lower, limit of the anticipated driving voltage. When using the R1-R2 voltage divider, recheck the voltage as you approach the switching point, since it may vary a bit as \(I_{B}\) changes.
4. Repeat steps one through three using two or three other transistors of the same type.
5. Use the lowest value of R3 measured in all of the tests. That will be the highest


FIG. 9-OPTIMIZING A 2-TRANSISTOR switching circuit is simple with this circuit. The base current of Q1 is set by \(\mathrm{R}_{\mathrm{B} 1}\); the dashed jumpers indicate important points for current measurements.
reliable value for the circuit. Note the amount of current drawn from the supply for future reference.
6. Before completing your design, connect the actual load and verify that the circuit operates reliably, and that current drain is within the limits of the transistor you use.

\section*{Two-transistor optimization}

Optimizing the two-transistor switching circuit shown in Fig. 9 is only a little more complicated. Again, the terminals joined by dashed lines will normally be connected (on your breadboard) by jumpers; the terminals are there to facilitate making current measurements. Now let's see how to determine optimum component values.
1. Calculate the base current needed to turn Q2 on, based on the actual (or calculated) value of load current through \(\mathrm{R}_{\mathrm{L}}\).
2. Calculate Q 2 's required base resistor using the formula \(\mathrm{R} 2+\mathrm{R} 3=\mathrm{V}_{\mathrm{CC}} / \mathrm{I}_{\mathrm{B} 2}\). Note that R3 can be zero, but, in any event, make the value of R2 at least three


FIG. 10-SWITCHING-SEQUENCE PROBLEMS occur with this motor direction-reversing circuit. As shown at \(b\), Q1 and Q2 could both be on at the same time, and that could result in destruction of both transistors.


FIG. 11-SWITCHING-SEQUENCE PROBLEMS ARE SOLVED with this motor direction-reversing circuit. Here, Q2 and Q4 could never be on at the same time (barring circuit failure, that is).


FIG. 12-A FEW SAFEGUARDS MUST BE OBSERVED when switching loads that are not purely resistive.
times that of R3 in order to minimize the current flowing through Q1.
3. Now you can calculate \(I_{C 1}\) as \(V_{C C}\) \(\mathrm{R} 2 . \mathrm{I}_{\mathrm{B} 1}\) is \(\mathrm{I}_{\mathrm{C} 1} / h_{\mathrm{FE}}\) (of Q1). Therefore, \(\mathrm{Rl}=\mathrm{V}_{\mathrm{BI}} / \mathrm{I}_{\mathrm{BI}}\). If the value of R 1 is very high (greater than 50 K ), lower the value of R2 to a value that permits a collector current through Q1 of at least one mA , and then recalculate all values.
4. Now wire up the circuit using fixed resistors for R2, R3, and \(\mathrm{R}_{\mathrm{L}}\), but use a potentiometer for R1. Check the circuit for reliable operation, and if it seems to work well, measure currents \(\mathrm{I}_{\mathrm{B} 2}\) (with Q2 on), \(\mathrm{I}_{\mathrm{C} 1}\), and \(\mathrm{I}_{\mathrm{B} 1}\). If any current is excessive, recalculate resistor values, beginning with R3. If either transistor refuses to switch, decrease the value of its base resistor by ten percent and try again. Repeat until switching occurs reliably.
5. Substitute transistors of the same type for Q1 and Q2, and test for reliable operation. Use the lowest value for R1 that permits reliable switching with all transistor combinations. That will be the highest value you can trust. Be sure to record all resistor and current values.

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\section*{TRANSISTOR SWITCHES}
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It takes only about thirty minutes to follow the procedures outlined above, and that's a small price to pay for an optimized circuit. You may be surprised at the values you obtain. For example, I calculated the following values the first time I set up the circuit in Fig. 9: \(\mathrm{R} 1=1 \mathrm{~K}, \mathrm{R} 2=10 \mathrm{~K}\), \(\mathrm{R} 3=1 \mathrm{~K}\). In my final design I used these values: \(\mathrm{R} 1=10 \mathrm{~K}, \mathrm{R} 2=33 \mathrm{~K}, \mathrm{R} 3=10 \mathrm{~K}\). My load must have been much lighter than I originally thought. Only a suspicious nature and a trusty breadboard kept that switch from drawing more current than all the remaining elements of the circuit put together.

\section*{Switching sequence}

Now that we've got all that under our belt, there is one more topic to consider: switching sequence of bipolar-output circuits, such as the motor direction-reversing circuit shown in Fig. 10-a. With that type of circuit we must ensure that both Q1 and Q2 are never turned on simultaneously. If they were, the \(+V_{c c}\) and \(-V_{C C}\) supplies would essentially be shorted together through the small "on" resistance provided by the transistors. The resulting current flow would in all likelihood destroy those transistors.

If the unconnected ends of R1 and R2 in Fig. 10- \(a\) were connected to the output of an op-amp, we might think that a positive output from the op-amp would turn on Q2, and drive the motor in one direction, and that a negative output would turn on Q1, and drive the motor in the opposite direction. In theory that's correct, but as
the sequencing diagram in Fig. 10-b shows, there is an overlapping time when both transistors are on.
To understand how that could happen, assume the voltage from the op-amp starts off at \(-V_{\mathrm{CC}}\). At that point, Q 1 is on, and Q2 is off. But as the input voltage rises, Q2's base will eventually become 0.7 volt more positive than its emitter, so Q 2 will turn on. Both transistors will remain on until the input voltage rises above \(+\mathrm{V}_{\mathrm{CC}}-0.7\) volt. At that point Q 1 will turn off; and it will not turn back on until the input voltage again falls below \(+\mathrm{V}_{\mathrm{CC}}-0.7\) volt. Therefore, both transistors will remain on until the input again falls below the 0.7 volt threshold needed to keep Q2 on.
One way to solve that problem is to add an additional pair of transistors, as shown in Fig. 11- \(a\). In that circuit, Q4 goes off long before Q2 goes on; Fig. 11-b shows the switching sequence. Assume the input voltage starts at ground. As it approaches -0.7 volts, Q3 turns off, then Q4 turns off as its base is pulled to \(-\mathrm{V}_{\mathrm{CC}}\). As the input voltage continues to rise, Q1 turns on, and that drops Q2's base to ground, so Q2 turns on. Following through the sequence, we see that there is no time when both Q4 and Q2 are on, and there is a short period when all four transistors are off.

Sequence-overlap problems can arise in other ways. For example, in counter circuits, the counter IC can contribute to overlapping "on" times even though the individual switching circuits do not. For example, the data sheet for the 4022 di-vide-by-eight counter reveals that the rise and fall slopes of successive outputs overlap, and it is doubtful that the switching transistors' internal delays would sufficiently compensate for that overlap. In
such circuits it may be best to create an "off" period with some other device, like a one-shot.

\section*{Other precautions}

Aside from the problems with bipolar output circuits we've just been discussing, there are several precautions to keep in mind regarding output loading of any transistor switching circuit. Lamps and other (more or less) purely resistive loads require little attention beyond that paid to voltage and current limits. Inductive and capacitive loads, on the other hand, demand special attention. For example, diodes D1 and D2 in Fig. 12-a serve to protect the transistor from inductive spikes generated by L1, which could be an inductor, a relay, a motor, etc.
There are several ways to the limit initial surge current that occurs when switching heavily capacitive loads. For example, we could use a current-limiting resistor, as shown in Fig. 12-b. Finally, for circuits that work in RF or other fields that radiate much potential EMI, filtering and bypassing, as shown in Fig. 12-c, are necessities.

In this article we have discussed several ways to make simple discrete transistor switching circuits operate both more reliably and more efficiently. Given the voltage and current requirements of a load, and given the voltage and current available from a digital IC (or other) driving circuit, we have seen how to calculate, and how to optimize, the values of the gain-determining components of simple transistor switching circuits. The small investment we make in calculating and breadboarding will be well rewarded by projects that work better, that last longer, and that waste less power.


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\title{
Robоtics
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Robot brains
}


MARK J. ROBILLARD
ROBOTICS EDITOR

We've talked about quite a few things the past few months, including putting together a robotics lab, robot motion and navigation, and last month we discussed, albeit briefly, the subject of voice recognition. It's time now to start putting those elements to-gether-we need to give our robot some brains.

Perhaps you're asking, "Do I really need a computer to control my robot?" You could build a circuit out of discrete logic components that would allow your robot to perform the task you had in mind. But, as Hamlet said, that's the rub. To change that task, you would have to re-design and resolder. However, if you implemented the control logic in software, you could change your design simply by keying in new instructions. Given the advantages of a software approach, there are still quite a few hardware questions to be answered. So what would be the composition of a suitable robot-control computer?

\section*{Microprocessors}

The heart of any home computer is its microprocessor, and there are a number of different microprocessors to choose from. We must pick ours carefully, according to several criteria. The first is power. All devices used in robot-
power, assuming that the robot is to carry its power source (batteries) with it.

There are two chief methods of fabricating microprocessors: NMOS and CMOS. NMOS is the elder of the two, and NMOS al-


FIG. 1
most invariably draws more power than the newer CMOS types. For example, an NMOS Z80 can draw as much as 150 mA of current at five volts. Most microprocessors (8080, Z80, 6800, 6502) were originally built using NMOS technology, although CMOS versions for many of them were introduced later.

Bearing in mind the fact that the microprocessor must be supported by a bevy of other powerhungry components, before you know it, your computer's brain could easily require 1.5 amps of power. A small motorcycle battery would be necessary to keep a sys-
tem with that sort of current drain operational for an hour! And that doesn't include power for the stepper motors!

It's easy to see why the industry almost universally uses CMOS microprocessors for portable computers. Not all microprocessors are available in CMOS yet, but the Z80, 6809, 8085A, 6502 and 8086/8 types are, and Motorola's 68000 will be soon.

The microprocessors mentioned above lead us to our next design criteria: eight or sixteen bits? (And 32 bits will soon be added to the "equation.") There is no clear-cut choice here. I have seen several robots designed with 16 -bit microprocessors that should have been designed with 8-bit devices. Some people automatically assume that a robot requires the larger units, but that is not always the case.

Another consideration is whether the application demands a full-blown multi-IC design. In many cases a single-chip microprocessor will do the job. Singlechip micros typically have 128 bytes of temporary memory and 16 input/output lines. I find singlechip micros most useful as dedicated sensor controllers. For example, you could use one to control your robot's motors and sensors, while the main processor carried out the heavy-duty control logic. Now let's look at the memory question in a bit more detail.

\section*{Memory}

Once again your application will determine how much RAM (Random Access Memory), ROM (Read

Only Memory) and EPROM (Erasable Programmable Read Only Memory) you'll need to provide. RAM is used for storage of temporary data, and data picked up by sensors; ROM and EPROM are used to store the control program and tables of data.

If you're new to this field, and if you're planning to build a research robot that will not be obsolete before you get done building it, then I suggest designing a "universal" memory system. That type of system is possible because there are, nowadays, RAM's, ROM's, and EPROM's that have almost identical pin-outs.

The circuit depicted in Fig. 1-a shows how a single 28 -pin socket can be used to house several different types of memory IC's, and several different devices of each type. The table in Fig. 1-b shows how points \(A, B, C\), and \(D\) in the circuit should be jumpered for each type of memory device. If you're interested in learning more about that subject, drop me a line. I'll send you a reprint of an applications note that discusses the subject in depth.

\section*{Ins and outs}

Robot I/O (Input/Output) is, by far, the most involved decision. Depending on the sensors and motors in your system, I/O circuits can become quite complex. It will simplify matters if you use standard I/O schemes (like RS-232C) for communicating with peripherals or dedicated sensor controllers, like those mentioned above. There are many products on the market that communicate via RS-232C lines, and it's easy to interface your own devices via RS-232C.

\section*{Buy or build?}

Whether you should buy a preassembled control computer or build your own also depends on the application. But if you think there's a chance that a pre-assembled unit will do the job, you'll save yourself a lot of hair-pulling by buying, rather than building.

Based on what I've said above, the specifications of an ideal robot-control computer might include a CMOS microprocessor, plenty of menıory, standard I/O
channels, and battery operation. Suppose I told you that you could buy an off-the-shelf device with a CMOS 8085A, 8 K of battery-backed-up RAM (which is expandable to 32 K ), a real-time clock/calendar, an RS-232C port, a Centronics parallel port, a bar-code reader input, a full 60-key alphanumeric keyboard, and a fortycharacter by eight-line LCD display, all packaged in a case that measures about \(8 \times 11\) inches?

That machine also has, built-in, a special version of Microsoft BASIC that supports all the I/O devicesand interrupts! You can burn your BASIC program into an EPROM and plug it into the ROM expansion socket that is provided. If you have an exotic I/O in mind, the entire system bus is also available.

The complete computer costs less than \(\$ 350.00\). If you haven't guessed what it is by now, you'll have to wait till next month. I'll have several interesting surprises then for those who choose to use that "mystery" computer for robot development.

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\title{
Communications Corner \\ HERB FRIEDMAN COMMUNICATIONS EDITOR
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SOMEONE ONCE SAID THAT "HISTORY always repeats itself;" well, the same thing seems to be true of electronics. That is, some idea from a bygone era is brought back and touted as the cutting edge of technology. The latest idea to be reborn is diversity reception.

Diversity reception is a means whereby we're able to compensate for the apparent loss of signal strength caused by changes in the polarization of an electromagnetic (RF or radio) wave, rather than by loss of the signal itself. Often, because of changes in a signal's polarization, we can't receive the signal even though we're using a decent receiver.

Using such techniques, a wireless microphone, for example, can be received equally well whether its antenna is horizontally or vertically positioned. No longer need reception suffer if a rock star were to literally crawl along the floor at the fringe of a receiver's reception range. While to many the concept is new, diversity reception in nonlaboratory equipment goes back a ways.

While diversity reception has been around from at least the late 1920's (AT\&T began using it then for their transoceanic circuits), I first came across it in some WWII surplus Navy receivers. But that was during the vacuum tube era, and the hardware necessary for diversity reception was larger than your average office desk. In fact, that diversity receiver could be used as a boat anchor for a small freighter. Today, the whole circuit isn't much larger than your thumb.


The importance of polarization
The polarization of an electromagnetic wave starts off in the same plane as the antenna, and maximum energy is obtained when the receiving antenna is in the same plane as the received wave. Unfortunately, many things can affect the polarization of a signal after it leaves the transmitting antenna. And the higher the frequency, the greater the effect on polarization and reception.

Most of us are familiar with signal polarization through experience with antennas for the reception of. FM broadcasts. If the FM station's transmitting antenna is one of the older horizontal designs (not circular) and the receiving antenna is rotated from horizontal to vertical, you can actually see the reading on a signalstrength meter decrease as the antenna is adjusted. At 90 -degree rotation, you'd lose the signal, or it would be so weakened as to be
useless. In other words, the signal polarity would be wrong for the receiving antenna.
Normally, the forces that affect the polarization of RF after it leaves the antenna are most severe in the HF range between 6 and 30 MHz . Skips and bending can change the polarization of a signal; thus, what started out as horizontal polarization in Europe can arrive here almost vertical.

Depending on the frequency, the time of day, condition of the electrical bands circling the earth, etc., polarization changes can occur over a period of only a few seconds. The signal that you think is fading in and out may really be rock steady in strength, but rotating somewhere between horizontal and vertical polarization.

\section*{How it works}

A simplified block diagram of how diversity reception works is continued on page 102

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\section*{COMMUNICATIONS CORNER}

\author{
continued from page 96
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shown in Fig. 1. The idea behind the scheme shown there is to track the polarization of the received signal and automatically switch antennas to maintain the maximum possible signal strength. Basically, as is shown, we have two independent tuners, each having two outputs; an RF output that connects to an electronic switch whose output feeds the receiver's IF amplifier, and a control voltage that is proportional to the received signal strength.

One tuner is connected to a horizontally polarized antenna, the other to a vertically polarized antenna. The control voltages are fed to a comparator that controls the electronic switch. When the received signal from the horizontal tuner is stronger than that of the vertical tuner, the horizontal control voltage is proportionately larger. That forces the electronic
switch to connect the horizontal tuner to the IF amplifier. When the signal from the vertical tuner is the stronger one, the reverse is true.

If anything causes the received signal's polarization to rotate, the signal fades out on the horizontal tuner, causing the control voltage to fall. In the meantime, the signal received by the vertical antenna/ tuner increases, thereby, increasing the vertical tuner's control voltage. At some preset level, the control voltage causes the electronic switch to "flip," connecting the vertical tuner to the IF amplifier.

The receiver continuously tracks the control voltages from the tuners, and selects the one that produces the maximum received signal. To prevent flutter caused by rapid and unnecessarily frequent switching between tuners, there's a hysteresis range of control-volt-age/signal-strength values within which the receiver will not switch. Of course, if signal fading is caused by an actual reduction in received signal strength regardless
of polarization, the receiver stays with the tuner that produces the higher control voltage.

As you might expect, a lot of hardware goes into diversity reception because it requires what is essentially two receivers, an electronic switch, comparison amplifiers, and lots of "witchcraft" to maintain the adjustments. Vacuum tubes made the system too bulky, expensive, and fussy to be used in consumer and conventional communications equipment.

But solid state is something else. Two limited-range tuners require very little in the way of components, and the whole comparator and switcher can be put on a single integrated circuit. In terms of extra costs, we're talking about pennies instead of hundreds of dollars. And now that a new generation of engineers have re-discovered diversity reception, the question is: Which manufacturer will be the first to produce a mass-marketed, all-band diversity receiver priced at under \(\$ 100\) ?



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\section*{C-band DBS?}

LAST MONTH WE LOOKED AT THE REAsoning behind the FCC's 1979 decision to create a special DBS, or Direct Broadcast Satellite, TV service on the Ku band. We came to the realization that, for DBS to work with one- to three-foot dishes, high-powered transponders are required.

However, there are difficult technical problems with increasing transmitter power on the Ku band at this time, so those interested in DBS have begun looking elsewhere: the C band. The problem with the \(C\) band is that maximum power is limited to about 10 watts. And that maximum is limited by regulation, not by technology. So we are no closer to realizing a worldwide DBS service than we were in 1979, when that service was envisioned.

At present there are about 1.3 million US and Canadian homes equipped with C-band TVRO's. And that number is growing by about \(1 / 2\)-million per year. So, by 1990, there will be C-band TVRO's serving no less than \(5 \%\) of all U.S. homes.

However, there are limits to TVRO growth, given current-and foreseeable-technology. And the main thing limiting that growth is dish size. Current 6-10 foot dish antennas are unsightly and impractical. If antennas were small-er-say 2 or 3 feet-a far greater number of homes could receive \(C\) band signals.

You might think that additional performance could be squeezed from another part of the system. But antenna gain and feed efficiency have been maximized for now. Clever receiver circuits that


FIG. 1
trade sensitivity for picture quality (like the so-called "threshold extension systems") have shown that you can improve small-dish systems, but that those improvements are marginal.

In short, for all practical purposes, we're at the state of the

\section*{TVRO dealer "Starter Kit" available}

Bob Cooper's CSD Magazine has arranged with a number of TVRO equipment suppliers to provide a singlepackage of material that will help introduce you to the world of TVRO dealership. A short booklet written by Bob Cooper describes the start-up pitfalls to be avoided by any would-be TVRO dealer, in addition, product data and pricing sheets from prominent suppliers in the field are included. That package of material is free of charge and is supplied to firms or individuals in the electronics service business as an introduction to the 1984/85 world of selling TVRO systems retail.
You may obtain your TVRO Dealer Starter Kit free of charge by writing on company letterhead, or by enclosing a business card with your request. Address your inquiries to: TVRO STARTER KIT, P.O. Box 100858, Fort Lauderdale, FL 33310. That kit not available to individuals not involved in some form of electronics sales and service.

\author{
BOB COOPER, JR. SATELLITE TV EDITOR
}

art-on the receiving end. So if antenna size is going to decrease, and if picture quality is going to increase, the transmitting end of the system will have to change. In short, we've got to give our Cband satellites greater power.

C-band power was originally limited in order to prevent interference to terrestrial microwave circuits. The fear, or threat, of such interference may have been justified. But as we saw last month, the possibility that a satellite some 22,000 miles from the earth could interfere with line-of-sight terrestrial telephone communication is miniscule.

\section*{C-band DBS}

Let's forget regulations for a minute, and ask a technical question: could a 50-watt transmitter do the job on the \(C\) band? The Russian Gorizont satellite has had 50-watt transmitters for nearly five years. The three-foot dish shown in Fig. 1 operates in Motala, Sweden, and it receives signals from the Russian satellite perfectly.

We need create no new technology to increase our present Cband birds to the 50 -watt power level-the technology is here, and it has been proven to work. For example, RCA's Ku1 and Ku2 satellites both have sixteen 45 -watt transponders on board. And Sweden has shown us that small dishes function very well with a 50-watt Cband transmitter.

I suggest, and I am hardly alone with this suggestion, that the time is at hand to abolish the 20-yearold engineering constraints that deny us C-band DBS service. That might happen in several ways:
- After initial opposition, the terrestrial telephone companies might simply accept the fact that power levels greater than 10 watts won't cause interference to their services.
- Alternatively, present frequency assignments (which, in any case, offset satellite channels from terrestrial channels by 10 MHz ) could be modified to eliminate the al-ready-remote possibility of interference.
- Further, the dithering technique that all satellite broadcasters currently use could be further modified to reduce the possibility of interference.
- Last, perhaps only a segment of the sky, say 70 west to 110 west, might be used by high-power Cband birds.

By implementing one or more of the above suggestions, C -band DBS would explode into a service offering dozens, perhaps hundreds, of channels nationwide. If that were to happen, the potential market could increase tenfold, or more!

R-E
\begin{tabular}{|c|}
\hline LETTERS \\
\hline continued from page 22 \\
\hline
\end{tabular}

Also, I'm not an experienced designer, but it appears to me that transistors Q2, Q4, Q6, and Q8 are intended to protect the output transistors. With R9, R18, R27, and R37 at 4.7 ohms, as stated in the article, the protection devices will turn on at about \(125-150 \mathrm{~mA}\) of output current.
As long as the output devices could handle at least that much current, the current-handling capability of the output devices wouldn't have any effect on the turn-on point of the protection devices. According to my calculations, for an output-current of 2 amps, resistors R9, R18, R27, R37 would have to be changed to about 0.3-0.4 ohms at 2 watts.
KARL REEBENACKER
Franklin, MA
Mr. Reebenacker is essentially correct. To increase output current to 2 amps, the transformer's rating
will have to be increased accordingly. The four resistors ( \(R 9, R 18\), R27, R37) should be changed to \(0.27 \Omega\) at 3 watts. In addition to that, the values of the four output capacitors should be increased to \(1000 \mu\) F each. Last, you should also replace the 2 N 3766 with a 2 N6057, and the 2N3740 with a 2N6050.Vaughn D. Martin

R-E

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A NEW KIND OF MAGAZINE FOR ELECTRONICS PROFESSIONALS

\section*{SO YOU'RE GOING TO BUY A MONITOR... A State-Of-The-Art Report}


\section*{GRAFEX-32}

Build This Hi-Res Graphics Adapter For Your Apple II

\section*{SPEAKER ENCLOSURE DESIGN} Let Your Computer Do The Hard Work Part II. The Conclusion

\title{
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}

\section*{Computer \\ DIGEST}

Hugo Gernsback (1884-1967) founder

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Robert A. W. Lowndes, production associate

\section*{9 Build The Grafex-32}

This hi-res adapter board for your Apple II will make a bis difference. Here's the first of a three-part article. Ray Dahlby

\section*{3 Letters}

\section*{3 Computer Products}

\section*{ON THE COVER}

Heath's new Model HVM-122A is a 12-inch amber monochrome monitor with excellent definition for 40 or 80 characters. What's more, you can drop the cost of this unit to well-under \(\$ 100\) by assembling it yourself from the kit. You'll find that for most noncolor applications, such as word processing, you get better definition with a monochrome monitor. So if you're thinking of going monochrome, or chansing to amber, take a lons, hard look at this unit, and see pase 4.

\section*{COMING NEXT MONTH}

Look for an intriguing application for the Commodore light pen as a frequency counter. If you've got any questions about printers, you'll surely find an answer in the printer tutorial we'll be running next month. We'll also be offering part two of the three-part article on the Grafex-32, begun in this issue.

\footnotetext{
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\section*{Karen Tucker,} production assistant
Geoffrey S. Weil, production assistant
Andre Duzant, Technical Illustrator Jacqueline P. Cheeseboro circulation director
Arline R. Fishman, advertising coordinator

Gernsback Publications, Inc.
200 Park Ave. South
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Chairman of the Board:
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ADVERTISING SALES 212-777-6400
Larry Steckler
Publisher

\section*{EAST/SOUTHEAST}

Stanley Levitan
Radio-Electronics
200 Park Ave. South
New York, NY 10003
212-777-6400

\section*{MIDWEST/Texas/Arkansas/Okla.}

Ralph Bergen
Radio-Electronics
540 Frontage Road-Suite 325
Northfield, Illinois 60093
312-446-1444

\section*{PACIFIC COAST} Mountain States
Marvin Green Radio-Electronics 15335 Morrison St., Suite 227, Sherman Oaks, CA 91403 818-986-2001

\section*{LETTERS}

\section*{Glitch!}

I built the circuit for the WriteProtect Notch By-Pass article in your July, ' 85 Issue. I found two small errors in Figure 2, the schematic diasram. Capacitor C4's polarity should be reversed so the positive lead connects to pin 2 of IC1. Also, the lead from the junction of IC1 pin 2 and C1, to one side of S 2 should be connected to the junction of IC1 pins 1, 8 and C1 instead. With the corrected circuit, I was able to load the program and saved it to a write-protected diskette.
A. F ,

Somerset, Bermuda.
Thank you. We appreciate your help and hope we didn't cause

\section*{you too much trouble.}

\section*{CP/M vs MS/DOS}

When VCR's became popular, I held off until I could see whether Beta or VHS was going to dominate. I've been holding off on buying a computer for the same reasons, waiting to see if I should go with CP/M or MS/DOS, but there doesn't seem to be any resolution. What should I do? C. D., Reno, NV.

Buy. We now have interface devices that can make any system compatible with any other. So stop denying yourself. Remember when Ralph Kramden refused to
buy a TV set because he was waiting for 3-D TV?

\section*{Dealer's Choice}

I know you select the articles for each issue from those that various authors submit. But if you could have a choice, what would you look for?
S. K., Taos, N.M.

Thanks for asking, S.K. Right now we're scrambling for-believe it or not-SHORT articles to fill one or two pages in the magazine! Everything we seem to have now has to be serialized over two or three issues. The subject matter is wide open, but we prefer wellwritten construction projects. \(\langle\boldsymbol{\omega}\)

\title{
COMPUTER PRODUCTS
}

\section*{For more details use the free information card inside the back cover}

\section*{INVOICING SYSTEM, the}

Supershipper, integrates all the elements involved: it is capable of handling customer-account lists, and product and price lists, as well as printing invoices, labels, and C.O.D. tags.

The Supershipper is available for the Commodore 64. The program retails for \(\$ 99.95\). - Progressive Peripherals \&


CIRCLE 11 ON FREE INFORMATION CARD

Software, Inc., 2186 South Holly, Denver, CO 80222.

BAR CODE READER SYSTEM, the model PSR150, can be used in either of two basic modes. The on line mode is as a fixed bar code decoder, which will decode bar code information and send it directly to your system. In the storage mode, the unit is a hand-held portable storage device that goes anywhere for collecting data and information at the source, thus eliminating pad and pencil. The stored data can then be transmitted directly to your system, also eliminating the need to key information into the system. Another feature is the scan-pad manual mode. In that mode, individual characters may be scanned from the barcoded scan pad to form a record. That record can then be sent in the on-line mode or stored in the store mode by scanning the "Enter" bar code on the scan-pad.

The standard unit of the model PSR150 comes with features such as stainless steel pen, 32-character LCD display, Nicad or alkaline batteries, bar-

code keyboard, and serial interface, and is priced at under \(\$ 500.00\). The model PSR150 is also available with 64 KB RAM, at a price of under \(\$ 1000.00\).-Digitronics Division, Comtec Information Systems, Inc., 53 John Street, Cumberland, RI \(02864 .\langle\boldsymbol{| D}\)

\title{
SO YOU'RE GOING TO BUY A MONITOR...
}

\section*{What you need to know before you shop.}

\section*{Herb Friedman}
- There are probably as many reasons for buying a monitor as there are for buying a computer. Maybe you just haven't got one, and have been using a television receiver. The kids want the TV back, and it's either buy them a new TV set or go out and do what you should have done in the first place-Buy a monitor!

Or maybe you've got an old green screen jobbie, and want to try one of the newer easy-on-the-eyes amber screen units. Or maybe when you first shucked out the bucks for your computer, you couldn't see the extra cost for a color monitor and settled for a monochrome-for now-with all that Christmas loot burning a hole in your pocket, maybe "now" is "now."

Whatever you've got in mind for that new monitor, you'd better be armed with some facts when you go shopping. You'd better be prepared for the salesman's jargon, you'd better know what he's talking about when he starts throwing technicalities at you, and you'd also better be prepared for some of the "on-the-spot"


FIG. 1a-THE COMPOSITE VIDEO output from monochrome personal computers uses the same NTSC format used for broadcast TV.
decisions you're probably going to have to make.
The very-worst thing that can happen if you aren't properly pre-educated, is that you're going to make a wrons purchase. We don't want that to happen, and neither do you. We couldn't possibly send one of our experts along with you on your shopping trip, so we did the next best thing. Here it is:
Selecting a computer monitor used to be easy. Most computer stores stocked only monochrome or color. Today, the terms "monochrome" and "color" are meaningless because there are different models within each category, many are mutually exclusive in that they will not function with signals intended for another kind of monitor.
As a general rule, monochrome monitors present few problems other than the signal connection; except for the IBM-type monochrome monitor all employ what is called a composite video input. "Composite" means the video signal contains both the video and sync signals, such as the NTSC waveform shown in Figure 1a; the same kind of signal used for conventional broadcast Black \& White TV. Any TV set or composite TV monitor can be used for a computer as lons as it can resolve the required detail. Since a composite


FIG. 1b-COLOR COMPUTERS simply superimpose a color burst on the back porch of the vertical sync. The monitor integrates the color burst with the video signal to create a color display. Without the color burst a color monitor would create a monochrome display.
video signal can be carried on a single shielded wire, the video input connector for those composite monitors specifically intended for personal computers is usually a "phono" jack, though some manufacturers have used unusual connectors which require an expensive "adapter cable." (The more rugged UHF and BNC connectors are generally used on monitors primarily intended for closed circuit TV.)
The resolution of a monochrome monitor is usually not a problem because the frequency response of the monitor serves as an indicator of what to expect. The conventional bandwidth of approximately \(6-8 \mathrm{MHZ}\) used for closed circuit TV monitors can resolve up to 40 characters per line, possibly 50 characters. A 60 character line requires about \(8-12 \mathrm{MHz}\), a sharp 80 character line takes from 14 to 22 MHz or greater.
Most important, a composite monochrome monitor can be used for both monochrome and composite color signals. It simply ignores the color burst, which, as shown in Figure 1 b is the same as the composite monochrome signal shown in Figure 1a.
Until the introduction of the IBM-PC, composite video was the standard signal for personal computers. One of the few attempts to avoid using conventional composite video (especially for monochrome display) was the Osborne computer. Aftermarket vendors immediately introduced composite adapters because non-composite monitors was an idea whose time had not yet come. At its best it was a hinderence to convenient use of the computer.
It was IBM that really created the use of noncomposite monitors for personal computers. When the IBM-PC was first introduced considerable sales effort went into convincing the buyer that the composite color output could not be used for monochrome displays. This, of course, was sheer nonsense, meant to sell an expensive monochrome adapter and a special, expensive monitor. Since no one else was selling "IBMtype" monochrome monitors IBM had the market to themselves because no other monochrome monitor would work with IBM's monochrome adapter signals.


FIG. 2-A COMPUTER WITH an IBM-type monochrome output provides individual TTL signals (non-NTSC) for the video component, intensity, and the vertical and horizontal drive. The monitor integrates the four signals into a screen display.

Figure 2 shows how the IBM-type monochrome computer output differs from conventional singleended composite video. The multi-terminal IBM-type output connector provides separate TTL signals for video, intensity, and horizontal and vertical drive: The signals are converted into a video display within the monitor. Obviously, the IBM-type monochrome signals cannot be used to drive a conventional composite monitor. But because conventional composite video isn't used it's possible to employ non-NTSC vertical


FIG. 3-AN RGB COLOR SIGNAL is also non-NTSC and TTL. Each of the three primary colors is provided as a separate signal along with the intensity and vertical and horizontal drive signals. Like the TTL monochrome monitor, the RGB monitor integrates the separate signals into a complete color display.
and horizontal sweep rates. IBM-type monochrome monitors employ a horizontal sweep of 18 KHz rather than the NTSC rate of 15 KHz , which in combination with a bandwidth of 16 MHz results in a display having greater resolution than a conventional monitor.

\section*{Color monitors}

Color monitors are available in two configurations, known as composite and RGB. In basic terms, the composite produces color displays by processing the composite video and the color burst. (If the color burst is eliminated from the computer's signal the video will be displayed in monochrome on both monochrome and color monitors, for without the color burst from the computer a color monitor cannot create color). As a general rule, almost any hue of any intensity can be displayed by a composite monitor; any limitation on the display of a particular hue is more a function of the computer than the monitor.
Unlike composite video which is an analog signal even if the information it carries is derived digitally, RGB signals are digital representations of the instantaneous color and intensity values of a single picture element. Like the IBM-type monochrome displays, RGB color monitors work with TTL signal levels, only now there are individual digital signals for RED, GREEN, BLUE, INTENSITY, and the vertical and horizontal drive. The signals from the computer, shown in Figure 3, are processed within the RGB monitor to produce a color display capable of the 16 colors shown in Figure 4. Normally, just the three RGB signals can produce the 8 colors indicated with an asterisk; the additional 8 are made possible by integrating the intensity signal with the RGB signals.

\section*{RGB COLOR DISPLAY}
\begin{tabular}{ll} 
Basic color & Intensified color \\
: Black & Dark grey \\
: Blue & Light blue \\
- Green & Light green \\
* Cyan & Light cyan \\
: Red & Light red \\
- Magenta & Light magenta \\
Brown & Yellow \\
* White & Intensified (super) white
\end{tabular}
* Indicates conventional pre IBM-PC basic color rainbow.

FIG. 4-A MODERN RGB MONITOR that is IBM-compatible will produce 16 colors including brown. Before the introduction of the IBM-PC, RGB monitors produced yellow instead of brown, and some RGB monitors could only produce the eight colors indicated with an asterisk ("*").


FIG. 5a-USING A MONOCHROME MONITOR with a color signal intended for an RGB monitor usually results in excess "gain" and considerable loss of detail in both the graphics and text characters.

Presently, all conventional RGB monitors create 16 colors from conventional personal computer signals. Unfortunately, some early monitors, a few of which occasionally surface in the surplus market, do not process the intensity signal and provide only 8 colors. It's not that the color table shown in Figure 4 is different from the earlier and conventional RGB colors in that the yellow is not part of the basic 8 colors derived form RED, GREEN and BLUE. In a conventional 8-color rainbow there is no brown: The position occupied by brown in Figure 4 is normally yellow. Unfortunately, although the intensity signal produces lighter versions of seven of the original 8 colors-thereby also producing a legitimate grey and an intense or "super white" (which is actually close to, or equal to the maximum screen brightness)-lighter yellow is still yellow. Also, the lack of brown sharply limits graphic artwork. To make the 16 color rainbow more applicable to "natural"sraphics, IBM substituted brown for the basic yellow, and provided yellow in the intensified rainbow. Although brown came into common use with the IBM personal computers, it is now considered "standard" because most personal computer equipment attempts to be IBM-compatible.

\section*{Monochrome from RGB}

While it is usually possible to use a mononchrome


FIG. 5b-BUT WHEN PROCESSED by a Video Enhancer an RGB color signal can look quite good on a monochrome monitor because the device converts each color to an individual shade of grey.
monitor with a composite color output, programs specifically written for an RGB color output often produce little more than "garbage" on a monochrome display even if the monitor's signal source is a composite color video output. For example, in addition to the RGB color output the IBM Color/ Graphics Monitor Adapter and its aftermarket clones have a composite video output which can be used to drive both composite color and composite monochrome monitors. If the computer is told to operate in the monochrome mode it will convert a color display to composite video that can be viewed on a monochrome monitor in two shades of monochrome: normal and highlight. If the program is self-booting so that the computer outputs only for RGB the composite video will contain "grainy"characters and missing graphic elements.

By using a device known as a Video Enhancer (Power-R, Inc., 4016 Interlake N., Seattle, WA 981031) the RGB, intensity and \(H\) \& \(V\) drive signals are converted into an "enhanced" composite monochrome output having each of the 16 colors represented by a discernable shade of grey. The Video Enhancer is actually built in a conventional 9-terminal D-connector, the same kind normally used for the RGB connector. It derives it's power from the power terminal normally provided on a Color/Monitor Adapter's light pen connector. The output cable of the device is terminated in a conventional phono plus, which can be connected to any conventional composite video monitor.

Figure 5 shows how the Video Enhancer recombines the individual RGB picture elements into a monochromatic grey scale display. Figure 5a, which has not been retouched, shows how the "rings of Saturn" from a Color/Graphics Monitor Adapter's color composite output would appear on a monochrome monitor: There is obviously much detail missing from the graphic art, as well as from the text word "NORMAL." Figure 5b shows how the very same display appears on the same monochrome monitor if taken from the RGB output and processed by the Video Enhancer. Notice that the rings of Saturn are now clearly visible in monochrome. Also, the text word "ENHANCED" is now legible, the way it should really appear on both monochrome and color monitors.

\section*{Match the monitor}

As you can see, not only are the different types of monitors and their signals mutually exclusive, but the software can also determine the kind of monitor needed. For example, while many users prefer a monochrome monitor for word processing (because the image is sharper), much graphics software and software employing graphics intended for color monitors simply doesn't provide a usable display on monochrome monitors, althoush monochrome software is almost never a problem when used with a color monitor. The best of both worlds when you cannot afford an RGB color monitor is the Video Enhancer because it provides usable displays from both monochrome and color software. \(\langle\boldsymbol{\omega}\rangle\)

\section*{COMPUTER-AIDED DESIGN OF LOUDSPEAKER ENCLOSURES \\ PART II}

\author{
Michael Raleigh and Robert Raleigh
}

This article, begun last month, is concluded here.
Figure 8 shows the results of a computer experiment which simulates putting this woofer in a ducted port. The dimensions are provided in the figure. The curve now shows a bump which extends the low-frequency response. The experiment is carried further in Figure 9 which shows the effect of changing the tuning condition of the enclosure by varying the duct length.

On the basis of the results shown in Figure 9 an enclosure was constructed with a. 124 cubic meter volume, a 0889 meter port radius, and a. 229 meter port length ( 3.5 inch radius, 9 -inch length). The agreement between the computer experiments and the actual experiments was verified by comparing the computed and measured admittances (Figure 10). The measurements were made using the same apparatus, as

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.15635892


FIG. 7-ACOUSTIC INTENSITY in watts/meter squared versus frequency for a . 2 cubic meter infinite baffle enclosure. A 3db difference is indicated for this graph and applies to all subsequent response curves.


FIG. 8-COMPUTED RESPONSE of ducted port enclosure constructed in design example. Parameters are listed above.


FIG. 9-COMPUTED RESPONSE for design example with various duct lengths. Other parameters same as Figure 8.


FIG. 10 -MEASURED AND COMPUTER SIMULATED admittance of enclosure built as design example, described in Figure 8. Magnitude of the maximum in the admittance which occurs at approximately 40 Hz is sensitive to the amount of damping in the port. Matching measured and simulated maxima provides a means of determining the damping in the port.


FIG. 11-COMPUTED RESPONSE for the design example with various amounts of damping in the port. Other parameters are as given in Figure 8.
shown in Figure 5, but with the woofer now in the enclosure. To achieve best agreement, a small amount of damping must be assumed in the port.
As a final computer experiment, we investigated the effect of adding additional damping to the port (Figure 11). On the basis of these results, it was decided not to increase the port damping. It is our design philosophy that frequency response takes precedence over transient response. We have therefore made our choice of damping based on this steady-state program result.

Infinite baffle enclosures are simulated by assuming a very long, very narrow duct. (. 001 meter radius, 1000 meter length). A ported enclosure is simply a ducted port enclosure with a very short duct. In this case, a greater proportion of the effective mass of the duct is due to the motion of air external to the duct. This effect is included in the program however, so that a proper simulation of a ported enclosure results from entering the length of the duct as the thickness of the speaker faceplate. For example, a 1-inch thick faceplate implies a duct length of .00254 meters. If a port or duct is not round in cross section, the radius should be that which gives the same area as the real port or duct.

\section*{Conclusion}

We have shown but a few examples. The reader is invited to systematically investigate the effects of varying each woofer and enclosure parameter. This program also provides an output-the admittancewhereby the match between the program and the actual loudspeaker may be verified. \(\langle\omega\rangle\)

\title{
BUILD THE GRAFEX-32 PART 1
}

\section*{A 640 by 400 graphics adapter for the Apple II.}

\author{
Ray Dahlby
}

Dep't. 255
Box C-34069
Seattle, WA 98124-1069

-The Apple Il's graphics resolution of 280 by 192 pixels can no longer be considered the "state of the art." The newer Apple Macintosh and Lisa computers display their bit-mapped images from up to 32 K bytes of memory, over four times that used for display on the Apple II.

Recently, the availability of 64K dynamic RAM chips and VLSI graphics controllers at reasonable prices has made it feasible to expand the Apple ll's graphics resolution using a simple plus-in circuit board. The Grafex- 32 circuit board uses four 16 K by 4 bit RAM chips along with a 7220 graphics controller to display 32 K bytes of memory as a bit-map of 640 by 400 pixels. The board can be expanded to 128 K bytes by plugging in 64 K by 4 bit RAM chips and three such boards can be installed in a system to provide the red, green, and blue signals for color graphics displays.

Our design tradeoffs favor circuit simplicity with the option of expansion later, over a complex design incorporating features which might not have been used by everyone. Although the basic circuit consists of just 20 chips, it displays a monochrome resolution of 256,000 pixels on a standard Apple Monitor. Since this baseline performance exceeds the graphics resolution of the Macintosh computer, I decided not to increase the complexity, and thus the cost, of the basic circuit board and thereby provide hardware "hooks" by which the design can be upgraded by those users with higher resolution or color requirements. This design philosophy seems consistent with that of the Apple II itself.

\section*{The Apple II}

The Apple II/Ile line owes much of its success to the expandability afforded by its expansion slots. In the six or so years that this computer has been on the market, hardware manufacturers have responded to the challenge of filling those slots by developing products to expand the machine and now, with a multitude of
add-on cards available, it is surprising to find an area which has not been adequately served.

One such area seems to be the expansion of the Apple Il's graphics resolution. Although a few cards were produced to add sprite-oriented graphics and luminance attributes to the Apple II, these did not significantly increase the number of pixels on the screen. Part of this lapse can probably be attributed to the introduction of the I.B.M PC which has distracted the focus of third-party hardware manufacturers at a time when VLSI graphics chips and cheap memory make Apple II graphics expansion an easy matter.

The Apple II line can benefit from new graphics technology in other ways than increased screen resolution. The video design used in the Apple II maps the display memory into the 6502 micro-processor's address space in a technique called "memory-mapped video." This was a good choice at the time because it allowed the 6502 full access to the screen memory using all memory reference instructions and addressing modes just as if it were addressing normal system RAM. In fact, if the high resolution screens are not used, the memory space allocated to them can be used for program and data storage. The design of the Apple II limits the graphics resolution for a least two reasons, one being the restricted bandwidth of the home color televisions it was assumed that Apple owners would be using as their display devices. A second reason is limited amount of memory space which can be addressed by the Apple's 6502 microprocessor. This chip has 16 address lines which allows it to address only 65,536 bytes so a memory-mapped video design using half of this precious space just would not have been practical.

Memory-mapped video design is not limited to low resolution graphics as evidenced by the newer Apple Macintosh and Lisa machines. The 68000 microprocessor used in these computers has a 24 -bit address bus and can directly address more than 16M
bytes of system memory. A large graphics RAM mapped into this address space does not represent a significant fraction of the total available for program and data storage.

The answer to improving the Apple Il's graphics resolution without using up all of its memory is to keep the graphics RAM separate from the system RAM. The 6502 won't be able to directly access to display RAM but there are new chips optimized for managing large bit-mapped memories. The 6502 actually benefits from the increased program available by not having part of its system RAM allocated to graphics.

\section*{The 7220 GDC}

The 7220 GDC (Graphics Display Controller) from NEC is designed to handle the repetitive tasks required in figure, line, and character drawing on a raster scan CRT. Unlike previous CRT controller chips such as the Motorola 6845 whose tasks were limited to display refresh and video synchronization. The 7220 has an instruction set which enables it to read, modify, and write data in the display memory. Positioned between the system microprocessor bus and the display memory, it responds to instructions passed to it and draws figures without processor intervention. Since the GDC can handle much of the repetitive pixel drawing and modification tasks, the bandwidth requirement of the microprocessor/display memory path is greatly reduced. Most of the data sent from the
microprocessor during vector and geometric shape drawing will be in the form of commands and parameters sent directly to the GDC which then interprets them into pixel-level operations to be carried out over the high-bandwidth GDC/display memory path. Its pipelined architecture is optimized for such graphics manipulation and it handles these tasks with great speed. For example, a 7220 running at a clock frequency of 5 Mhz can draw a figure at the rate of 800 ns per pixel. This speed is independent of the type of figure being drawn and is much faster than a general purpose microprocessor, such as the 6502, handling the same task.

The 7220 was chosen for this design because its 8 bit up data bus interfaces nicely with the Apple's 8 bit 6502 and its 16 bit video data bus and 18 bit video address bus allows it to accomodate large bit-mapped display memories without impinging on the limited 16 bit addressing of the 6502. It also handles dynamic RAM refresh and video sync generation. This part is housed in a 40 -pin ceramic package and is fabricated in 3um NMOS. It encompasses the equivalent of over 13,000 transistors.

\section*{The display memory}

The 7220 requires its display memory to be organized in 16-bit words. The most common 64 K dynamic RAM chips, such as the 4164, are organized as 65,536 locations of 1 bit each so 16 of these parts are


FIG. 1-BLOCK DIAGRAM OF THE GRAFEX-32 should be used along with the schematic diagram to help understand the theory of operation.

required to assemble a 16-bit memory system. There is another 64 K RAM chip which is organized as 16,384 locations of 4 bits each. This part, designated the 4416, is available from Texas Instruments, Inmos, and Fujitsu, among others. The 16 bit data bus requirement of a 7220 can be met using just four of these chips, thus reducing power dissipation and printed circuit board complexity. The use of the 4416 actually helps to reduce chip count further as it has a separate output enable pin which can be used to tri-state its data lines. This feature allows very tight coupling between the display memory and the 7220 which helps to simplify P.C. layout further.

Texas Instruments has recently announced the 4464 and NEC, the 41254, dynamic RAM chips which are organized as 65,536 locations of four bits each. These parts are housed in the same 18-pin package as the 4416 and allow easy system upgrades. The Grafex circuit, for example, has been designed to address the additional memory provided by these parts and expansion of the Grafex circuit from 32 Kbytes to 128 Kbytes is accomplished simply by replacing the 4 display RAM chips.

\section*{Circuit description}

Referring to the block diagram of Figure 1 and the schematic of Figure 2, it can be seen that the Grafex circuit can be broken down into five sections. These sections are a) the uP bus interface, b) the 7220, c) the display memory, d) the video output circuitry and e) the timing generator.
The 7220, IC1, communicates with the host microprocessor over its eight bidirectional data lines. IC11, A 74LS245 is used to provide buffering and IC2, a 74LS00 is used to convert the Apple's RNW and DS signals into the 8080-type RD and WR signals used by the 7220. The Grafex circuit uses four of the 16 addresses assigned to the peripheral slot in which it is installed in the Apple II. These addresses are used to select the command and parameter registers of the 7220 and to control the video changeover relay latch, IC7. The addresses and their functions are listed in Table 1 which will appear in a subsequent issue. The reset line on the Apple bus is connected to this video relay latch to force a default on power up to the Apple video signal.

The 7220 communicates with the display memory over its 16 multiplexed address/data lines, labelled ADO-AD15 on the block diagram. The CAS latch, IC12, a 74LS374, is used to latch the high order 8 bits of addresses and then to sequence them onto the 8 RAS/ CAS address lines of the display memory. This is a tight-coupled design from the standpoint of data and address multiplexing. The low address/data path from the 7220 carries, in sequence, the row addresses, column addresses, and finally, the low byte of display memory data. The high byte of display memory data is carried over a separate path directly to the 7220 AD8AD15 pins.

The display memory, IC13-IC16, has a multiplexed 8bit address bus and a 16 -bit data I/O with separate output enable. Like other dynamic RAM arrays, the
eight row address bits are first strobed into the on-chip latches, then the eight column address bits are presented on the address lines and finally, these are strobed into the memory. After access time specifications have been met, the data is read during a read cycle or written during a write cycle. The 4416 and 4464 parts have a separate output enable pin which allows data to be read from the selected address location but not presented on the output pins until needed. This " \(G\) " pin allows a fast ( 40 ns ) turn-on of the output buffers to supply data to the external circuit when needed later in the read cycle. In this manner, the eight row/column address lines can serve also as the low 8 -bit data corridor to and from the 7220 and video shift register without the need for an external tri-state buffer. This kind of coupling facilitates printed circuit board layout and improves reliability by reducing the circuit inter-connections.

The 16 bits of data read from the display memory are presented to two 8-bit parallel in/serial out, shift registers, IC17 and IC18. These 74LS166 shifters serialize the 16-bit data into a video bit stream which is clocked at the 16 MHz dot-clock rate. The blanking, Hsync, and Vsync signals from the 7220 are brought into line with data by means of the 74LS174, IC19. It is loaded by the same load/shift signal as are the shift registers.

The video-bit stream is gated with the 7220 blanking signal by IC4 and then mixed with the composite sync provided by exclusive-or gate IC5. The video amplifier, consisting of Q1 and Q2 provides a standard 1 volt P-P composite video signal into 75 ohms. This composite video signal is routed to the video changeover relay, K1 and K2, which selects either the Grafex video or an external input as the source for the video monitor. When the Grafex board is installed in an Apple II, the external input is normally connected to the Apple's video output connector and the chanseover relays output connected to the system video monitor. Alternatively, two monitors could be used to simultaneously display Grafex and Apple video. The video changeover relay is software actuated and defaults on reset to the external input, allowing the Apple system to be operated normally after power up. In this way, unless the Grafex board is specifically addressed, a user need never be aware of its existence.

The timing generator, comprised of the 16 MHz crystal and IC3, IC4, IC6, IC8, IC9, IC10, and IC20 provides the various clock and control signals used in the system. All timing is derived synchronously from the 16 MHz clock. The timing generator has two modes of operation depending on whether the 7220 is executing a display cycle or a RMW, (read, modify, write), cycle. These two types of cycles are differentiated by the DBIN pin of the 7220. The 7220, in turn, uses as its master clock, a 2 MHz signal labelled 2Xwc1k. This clock, as its name implies, runs at twice the display word rate and all internal timing of the 7220 is derived from this signal since 16 pixels comprise one 16 bit word of display memory, the \(2 \times w c 1 \mathrm{k}\) used in this design is \(2 \times 16 \times 65 \mathrm{~ns}=500 \mathrm{~ns}\) or 2 MHz .

That's all the space for now. We'll continue this article next month.


Typical of RCA CTC 85 thru 108 LV Regulator Circuits

\section*{How many of these questions can you answer?}
) Every circuit has a beginning and an ending. Where does this circuit begin?
?) Specifically, what is the purpose of this circuit?
3) What turns it on? What turns it off, or does it ever really turn off?
4) Does this circuit have a shut down feature? If so, which components are involved?
j) What would happen if Q103 were to become shorted E to C?
3) What purpose does Z115 serve ?
7) What would happen if D114 became shorted?
3) What purpose does C126 serve? What will happen if C126 becomes open?
3) Is the winding between terminals 3 and 4 of the flyback a primary or a secondary winding?
j) What purpose does C117 serve? Exactly what does it do, and exactly how does it do it?
1) Exactly what do resistors R113, 114, 115, 116, and 117 do? What happens if they change value?
2) What occurs that causes this circuit to produce an initial start up pulse?
3) Why does this entire circuit become shorted and begin to destroy horiz output transistors if the regulator SCR becomes shorted?
4) There is exactly one safe and practical method of circumventing this LV regulator circuit for test purposes. This technique does not involve a variac. Instead, you must disconnect one wire then connect a jumper wire from terminal \#4 directly to Which wire do you disconnect and where do you connect the other end of your jumper wire?
5) If SCR100 is shorted, this circuit will still "eat" horiz output transistors even if you are using a variac. Why?
6) Why does this circuit use a floating ground?

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\title{
Antioue Radios
}

\author{
Early radio history
}


\author{
RICHARD D. FITCH CONTRIBUTING EDITOR
}

AFTER DISCUSSING OUR ANTIQUE RAdio of the month, we'll explore some of the history of electricity, magnetism, radio, and related disciplines. It's a long history lesson, so you'll have to wait until next month for its conclusion.

\section*{Antique of the month}

You'll never guess the make of the antique radio shown in Fig. 1. Actually, it's a composite-it's made up of components that weren't assembled together originally. Composite antiques like that are not unusual. During the lean 1930's, the radio enthusiast often assembled his set from other people's cast-offs. It's possible that your antique is one of those com-posites-which doesn't make it any less valuable as an antique.

In the 1920's and earlier, all radios were, in a sense, composites. Early radio enthusiasts had complete knowledge of both chassis and cabinet construction. If you've got one of those homemade composites, you've got a real "one-of-a-kind" that may be a valuable link in the evolution of radio.

If you have a chassis and a cabinet left over, feel free to make your own composite, as I did. Take the usual precautions pertaining to exposed high voltages, and provide some means of heat ventilation. Do a good job, and you'll have a fine conversation piece.

My composite has a cabinet made by E. T. Earl. It pre-dates the 1930's, and I got it without the original chassis; so I had to alter the cabinet to accommodate a Zenith chassis I happened to have on hand. The beautifully-worked es-


FIG. 1
cutcheon from that cabinet will go in my collection. The chassis that I used to fill the cabinet is about seven years newer than the cabinet itself. I wanted to mount a Wave-magnet antenna (from another Zenith) inside the cabinet as well. Unfortunately, it doesn't fit, so I may have to mount it on the back (or on the top) of the cabinet. In general, you can be a little more creative than usual when "restoring" a composite.

\section*{Early radio}

In the past, we limited our discussions mostly to radios originally manufactured in the era from the early 1930's to the mid 1940's. But radios from that era were considered modern by some readers who have been involved with radio since the 1920's, or even earlier. They built their chassis from mail-
order parts and instructions in radio magazines like Modern Electrics (an early Gernsback publication). Then they built or purchased a cabinet to house that chassis. We haven't talked much about radio before the 1920's, but that's when it really took off in a commercial sense. However, before we talk about the ' 20 's, we'll go back even farther to gain some historical perspective on the development of the radio arts through the ages.

\section*{Ancient history}

In ancient Greece, around 600 BC, the philosopher Thales noticed that rubbing a piece of amber caused it to attract small bits of material. That's the earliest record of static electricity. In Greek, the word for amber is elektron, and that's the root of many words we use every day.

Magnetism, too, was known in ancient times; for example, the Greek playwright Euripides mentioned, in 425 BC, that lodestone attracts iron. The word magnetism comes from the town in Asia Minor, Magnesia, where lodestones are found.

Saint Augustine discussed magnetism and electricity in the late fourth and early fifth centuries, AD, but little else happened throughout the dark and middle ages. In fact, it wasn't until the Renaissance when electrical and magnetic phenomena were again investigated in a scientific manner. William Gilbert, physician to Queen Elizabeth, built a device for detecting electricity that he called an electroscope. Gilbert was the first person to use the word electric in English. Later in the 17th
century, Sir Isaac Newton did some work with electricity, but things really took off in the 18th century.

Many phenomena were discovered then, including the difference between insulators and conductors, and the difference between "positive" and "negative" electricity. Ben Franklin performed his famous kite experiment, which proved that electricity and lightning are different manifestations of the same phenomenon; the leyden jar was discovered in 1745/6.

\section*{The leyden jar}

Several investigators simultaneously discovered a device that we now consider to be the first capacitor (or as readers of this column used to say, condenser). The leyden jar is named after the Dutch town of Leyden (also spelled Leiden); leyden jars were used by all early investigators to experiment with static electricity.
The leyden jar is lined, inside and out, with thin, conductive foil; those linings are the "plates" of the capacitor, and they condense, or store, a large quantity of electricity. The leyden jar must be used on a non-conductive table top, and the glass jar must be kept dry. The jar is charged by touching a charged plate to the brass ball on top. The jar is discharged by using a wand to short the ball and the foil-lined outer surface of the jar. The wand has two brass balls connected by stiff wires; the wires extend from an insulated handle held by the experimenter.

Adams, in the late 1700 's, and Matteucci, in the 1820's, worked with leyden jars. Their work centered around inducing reactions at a distance of several feet. Riess and Henry also experimented with the Leyden jar, as did Paazlow and Lodge, who worked with huge, oversized Leyden jars. Sir Oliver Lodge, with his syntonic (literally, in harmony with, or "tuned with") leyden jars, probably made the most important contributions to the field of radio. Variations on the leyden jar were used in many early electrical appliances.

Other discoveries of the late 1700's included Coulomb's formalas that quantify the attractive
forces of electricity, and magnetism. Galvani discovered a biological phenomenon that was the forerunner of the electrical storage cell, or battery; later that device was refined by Volta.

\section*{The 1800's}

The nineteenth century was the real Golden Age of electrical discovery and invention. Among the more important discoveries were those of Oersted, who showed that an electrical current could deflect a magnet, and Faraday, who did experiments that eventually led to the electric motor.

The German Georg Simon Ohm discovered the mathematical relationship we now call Ohm's law in 1825. Later, Henry and Faraday independently discovered mutual induction, and shortly thereafter Henry discovered self-induction. Gauss built a rudimentary telegraph, and, in the U. S., Morse made a practical instrument of it. Possibly the most important theoretical breakthrough was that of James Clerk Maxwell, who found a way of mathematically relating light, electricity and magnetism.

Many useful devices were invented in the 19th century, including Alexander Graham Bell's telephone (1876), and Edison's incandescent bulb (1879). Edison also came up with the idea of placing a second element in the light bulb, and that, without a doubt, opened the door for many experimenters. However, the Edison Effect, as it was called, lay dormant for over ten years.

In 1888 Rudolf Hertz provided an experimental verification of Maxwell's theories, and that is what led to practical radio transmission as we know it. A year earlier, Hertz had demonstrated that both sender and receiver had to be tuned to the same frequency for long-distance communication to occur. And Marconi made one of the first long-distance (nine miles!) radio transmissions in 1897.
In the late 1800's the need for standardizing electrical units was recognized. The International Electrical Congress, meeting in Paris, convened a commission to study the matter in 1881.

We'll continue our look back at radio in our next column. R-E

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JACK DARR SERVICE EDITOR

THE FRENCH USED TO CALL A TRANsistor "La bête noire avec trois pattes," or, the black beast with three legs. I guess they would call an IC "La bête noire avec quatorze pattes," or, the black beast with fourteen legs. IC's are still somewhat mysterious to many of us, but they needn't be. If you think of them in terms of functional blocks, much of that mystery evaporates with the morning dew.

For example, the ECG742 shown in Fig. 1 is a complete TV sound system with a detector and a twowatt audio output. Every TV set must have some sort of detector, as well as an audio power stage. The IC just combines them in one package, and you should troubleshoot the IC just as you would troubleshoot a circuit made up of discrete components. Using a scope, check the inputs and then the outputs. The signal at the sound detector's input should appear as on any TV set, and so should the output. And the audio stage is even easier to troubleshoot. If you find a good-sized gain, the chances are that that stage of the IC is still in good working order.

But if you're not convinced, check the voltage at each pin; a schematic of the TV you're working on is essential for doing that. If one or more of those voltages are off, chances are that the IC is bad. Otherwise you'll have to check elsewhere. A discrete component connected to the "trouble" pin may have gone bad, for example.

We find very few "weak" IC's (unlike tubes). They're almost always either good or bad, not somewhere in between. The sup-

ply voltage is fed to pin 11 of many-but by no means all-nondigital IC's. Always check the schematic to be sure.

Sometimes you can let your fingers do the checking. Apply a fingertip to the case of an IC you suspect is bad. Most IC's run as cool as a clam. I don't have exact figures on clams' case temperatures, but experience has shown them to be pretty cool. So, if you find a hot one (an IC, not a clam), the chances are that it has an
internal short. Of course, power IC's (as in our example in Fig. 1) may dissipate enough power to run fairly warm, or even hot.

So, the best way to check a suspect IC is with an oscilloscope. By comparing the input and output signals you can tell instantly whether the IC has any gain or not. And most analog IC's have at least some gain. Even sync-separator IC's have some perceptible difference between the input and output signals. It often helps to
use a known input signal (from a sweep generator, for example) for signal tracing. That makes it easier to tell whether an IC is doing the right sort of processing.

\section*{IC removal}

Finding a bad IC is often the easiest part of a repair job! Removing an IC can be a real pain unless you use the right tools. Probably the best all-round tool for IC removal is the "solder-sucker" type of soldering iron. That tool has a hollow tip and a teflon-lined squeeze-bulb. You must fit the hollow tip around the leg of the IC (or any other component, for that matter) and sqeeze the bulb. When the solder melts, simply release the bulb. If everything goes right, all solder from the joint will be sucked up inside the bulb. If you follow that procedure for each IC pin and do a careful job, the IC should just drop out.

But don't get frustrated if it doesn't! If you yank the IC out, you're liable to destroy the little hollow cylinder of plating that connects the top and bottom sides of a double-sided PC board. Or you might rip up some of the copper traces. So be very gentle, and assume that at least one pin, and probably a lot more, will require extra attention before the device can be safely removed.

Another IC remover that comes in handy once in a while works like this: A spring-loaded clamp is attached to the IC from the top side of the board. A soldering iron with a special tip having dual parallel bars is then pressed to the bottom of the board. The bars melt all the joints of all the pins at once, and then the spring pulls the IC out.

In the absence of fancy tools like those, clip all the legs of the IC as near the plastic package as possible. Remove the "body" of the IC, and then use needle-nose pliers to pull the pins one by one from the top of the board, while heating the pad from below with a soldering iron. It's tedious removing an IC that way, but it works. Clamping the PC board in a vertical position will allow easier access to both sides of the board.

So, don't be confused by IC's. Just think of them in terms of the functions they perform, and go on
about your troubleshooting in the usual way. Check the input and output of each stage; and if either is off, check signals at the other pins. If anything differs drastically from what is shown on the schematic, see if any external components (RC networks, for example) could be causing that difference. If not, the IC is probably bad, so replace it carefully.

\section*{SERVICE OUESTIONS}

\author{
B + TOO LOW
}

While working on a Teknika, model 3249, I found the B+ adjust not working and the \(B+\) voltage too high. I later found that the regulator transistor was shorted, so I replaced it. With a new one in, \(B+\) is now too low, and I still get no reaction from the \(B+\) adjustment. Please help!L.W. Baltimore, MD

You have a two-stage regulator in that set, so it couldn't be simpler. The B+ adjust sets up the operating voltages of the error amplifier by altering its base voltage. The emitter, being directly coupled to the base of the regulator, causes shifts in the output voltage. Under the circumstances, I would certainly want to look at the error amp. Needless to say, there are a number of other components that must come under close scrutiny as well: both Zener and standard diodes, and so on.

\section*{WHAT KIND OF SCOPE FOR TV?}

I want to buy an oscilloscope for TV servicing. What's the best kind?-M. R., Lutherville, MD.

I've always said that it's easy to pick out a wife, a suit of clothes, etc., but you've got to be careful in choosing test equipment. Seriously, there are a lot of good scopes available. Heathkit, B \& K, Hickok, Tektronix, and many others all make scopes, and you can't go too far wrong with any of them. I've got six of various makes, and they're all good. My suggestion would be to try the Old Professor's Famous Test for Whisky: Pour some in a glass and drink it! In other words, arrange for a demonstration in your shop and try different several models.

R-E
\begin{tabular}{|c|}
\hline NEW IDEAS \\
\hline continued from page 46 \\
\hline
\end{tabular}
fed to output-buffer gates IC4-b and IC4-c, which are wired in parallel to provide additional drive capability. The width of the output pulse is about 150 microseconds, which should be sufficient for most TTL and CMOS circuits. If you need a longer (or shorter) output pulse, connect a precision monostable between the IC4-b-IC4-c output pins and your external circuit.

There are two other means by which IC2 may be reset. When power is initially applied to the circuit, pin 13 of IC4-a is held low for about 20 milliseconds, as determined by the R2-C3 time constant. During that time pin 11 of IC4-a is high, and that resets IC2. A reset pulse is also generated each time momentary switch S1 is pressed. Whether it comes from S1's being depressed or from a power-up sequence, the RESET pulse is also buffered by gate IC4-d; that signal may be used to synchronize external circuitry.

The circuit may be built in any convenient manner; just be sure to keep lead lengths short. Mount the crystal near IC1, and use sockets for all IC's. If you need a highlyaccurate pulse-source, substitute a \(50-\mathrm{pF}\) trimmer capacitor for C 2 and adjust it while observing a frequency counter connected to pin 7 of IC1. You should be able to drive several TTL loads with the circuit as shown, but if more drive capability is necessary, connect a 4049 inverter in series with the output.

The MM5369 is available with several different division ratios that provide \(50-\mathrm{Hz}\) (MM5369EYR) \(60-\mathrm{Hz}\) (MM5369AA) and \(100-\mathrm{Hz}\) (MM5369EST) outputs. Make sure you get the correct part!

If you need an oddball reference frequency ( \(2 \mathrm{~Hz}, 3 \mathrm{~Hz}, 5 \mathrm{~Hz}\), etc.) you can alter the RESET/OUTPUT pulse rate by NANDing various combinations of IC2's outputs. For example, if you needed a \(2-\mathrm{Hz}(1 / 2-\) second) output, you'd need to count 30 pulses before resetting IC2. To do that you'd need to NAND IC2's Q2, Q3, Q4 and Q5 outputs.
-Chester C. Roher

\title{
Drawing BOARD
}

Z80 demo program
now that we've built our z80 system, the time has come, as they say in Transylvania, to bring it to life. When the circuit is powered up, the un-enlightened among your friends might get the impression that it's not doing anything. However, we in the know are perfectly well aware that it is doing something-it's waiting for us to tell it what to do. When it comes to microprocessors, there's one thing you should always remember: obedience and stupidity are twin virtues.

If you've done your homework and read up on the Z80's instruction set, and programming techniques in general, you may have come to the realization that our circuit is pretty limited. We've got a fair amount of program storage space in the EPROM, but RAM storage is limited to the Z80's internal registers, and our one-way I/O leaves a lot to be desired. So can we do anything with our circuit that is at all useful?

Let's take things one step at a
time. First we'll write a short demo program to make sure the circuit is working. Then we'll talk about how we can expand the circuit to make it easier to accomplish something really useful. But before we start, I should mention that we're not going to go into the software in any great depth; we simply don't have the space. If you're familiar with any kind of programming at all-even in the BASIC language-you shouldn't have any trouble following our discussion. Otherwise you will have trouble; so get out those data books and start reading!

Now for the demo program. Here, and throughout the rest of this column, all numbers will be in hex, unless otherwise specified. Now, since we've got a four-bit port, let's write a program that causes the Z80 to output values from \(\emptyset\) to \(F\) to that port.

\section*{Software design}

Writing software is similar to designing hardware. The first thing

TABLE 1-EXAMPLE PROGRAM
\begin{tabular}{|c|c|c|c|c|}
\hline Line & Address & Op Code & Source Code & Comments \\
\hline 1 & 0000 & AF & XOR A & Zero the Accumulator \\
\hline 2 & 0001 & 26 0F & LD H, ©F & Set the display number \\
\hline 3 & 0003 & 2EAO & LD L,A \({ }^{\text {¢ }}\) & Set the loop counter \\
\hline 4 & \(\emptyset 005\) & 7 C & LD A, H & Load the Accumulator \\
\hline 5 & 0006 & D3 FF & OUT (FF),A & Send it to the latch \\
\hline 6 & 0008 & C3 1100 & JP 0011 & Go to delay subroutine \\
\hline 7 & \(\square 00 B\) & 25 & DECH & Decrement port count \\
\hline 8 & ø0. & 2D & DEC L & Decrement loop counter \\
\hline 9 & 000 D & C20500 & JP NZ 0005 & Do again if not zero \\
\hline 10 & 0010 & 76 & HALT & End of the program \\
\hline 11 & 0011 & 11838 B & LD DE,8B83 & Preset the delay loop \\
\hline 12 & 0014 & 1B & DEC DE & Decrement the counter \\
\hline 13 & 0015 & C2 1400 & JP NZ 0014 & Jump back if not zero \\
\hline 14 & 0018 & C3 øВ øø & JP ØøøB & Return if finished \\
\hline
\end{tabular}


FIG. 1
to do is to get a clear idea of what you want to accomplish. With hardware you draw a block diagram; with software you draw a flowchart, like the one shown in Fig. 1. The flowchart lets you see the way the program is going to operate without getting lost in a maze of low-level details. The flow of a small program like ours is more or less intuitive, but drawing flowcharts is a good habit to get into. Remember Grossblatt's Fourth Law: You have to know the
rules to break the rules. In other words, don't look for shortcuts until you know where you're going.

Our flowchart is easy to understand. First we initialize things by loading appropriate values in the registers (which we'll think of as RAM). Then we send the number to be displayed to our latch. We wait for half a second, decrease the display number by one, and then do it all over again. That's repeated over and over until the displayed number is zero.
The actual program listing is shown in Table 1; the Z80 instructions should be self-explanatory now that we understand the flowchart. The final version of the program added one thing not shown in the flowchart: a loop to repeat that whole process ten times and then quit. We use the H register to store the number we want to display, the L register to keep track of the number of times we've gone through the loop, and the DE register pair to keep track of the elapsed delay time.

The first instruction, XOR A, is a neat way to clear the accumulator using only one byte of program memory. What happens is that every bit in the register is xOR-ed with itself. We could get the same result by directly loading the accumulator with zero, but doing it that way takes two bytes.

The more bytes used, the longer the program gets, the more time it take to run, and the more memory it uses. That's not important in our demo program, but another good habit to develop is that of saving memory, increasing speed, or both, as in the present case, by using "tricks" like that. Also, ANDing, OR-ing, and XOR-ing a register with itself is useful for manipulating the Z80's flag bits with a single instruction. Any good book on programming the Z80 should be loaded with tricks like those. If they're not there, it's not a good book.

So, after initializing the registers, line 5 of the program sends the number to be displayed to the latch. If you're wondering why I'm using an out (fF), A instruction, you should re-read last month's column. The fF is the address of the port I want the number sent to.

We could actually use any number because our circuit doesn't decode I/O ports, so any out instruction will wind up sending data to the latch. In a more complex system we'd have address-decoding circuitry that would select the proper port. In our circuit, the address lines are used only to load program instructions and data from the EPROM.

Our program now jumps to the delay loop that begins at line 11. We use the delay to slow down the program so you can see the countdown on LED's. Just connect them to the outputs of the latch with 330 -ohm resistors. If you're really ambitious you could build a display circuit to have the output appear on a seven-segment readout.

Without the delay loop, the program would cycle so quickly that you wouldn't be able to see any of the individual numbers. I used a full register pair to set up the delay time because the D and E registers together will allow any value up to FFFF.
Calculating the length of the delay isn't difficult. Each Z80 instruction takes one or more clock cycles, called \(T\) cycles, to execute. The number of cycles depends partly on the length of the instruction. Each trip through our delay loop takes 14 T cycles. Since we have a \(1-\mathrm{Mhz}\) clock, or close to it, each cycle will take 1 microsecond for a total of 14 microseconds. If we want a delay of half a second we have to generate a loop that lasts half a million microseconds. Dividing 500,000 by 14 we get 35,714 , or 8B83 hex.

Normally such a delay would be set up as a subroutine, callable by other routines in the program. But since we have no RAM, we can't do a subroutine call, because the Z 80 automatically stores-in RAMthe address it is to return to after executing the subroutine. So we'll have to write our program without subroutines. We simply jump to and from the delay "subroutine" using JP, rather than using CALL and ret (gosub and return, for you BASIC programmers).

We have a few more things that we need to talk about, but not this month; unfortunately we're out of room. We'll get to those topics the next time.

R-E

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+5.5 volts at the emitter of Q7, and check for +4.5 V at the collectors of Q4 and Q5. The emitter of Q7 may show 4-7 voltsthat's OK. Also, Q7 will normally run somewhat warm to the touch.

If all DC voltages check out, you can begin alignment. First, check the voltage at the collector of Q5. As you ground the base of Q6, that voltage should increase to over 10 if the modulator is working.

Next, connect the RF probe to the junction of C3 and C8. Tune L4 until Q1 starts to oscillate, as indicated by a reading of 0.5 to 1.0 volts on the VOM. Now connect the RF probe to the base of Q3 and spread or squeeze the turns of both L2 and L3 to obtain maximum voltage. Now connect the RF probe to the base of Q4 and adjust the turn spacing of L5 and L6 for a maximum voltage. Now, re-adjust L2, L3, and L4 to maximize the voltage reading. Connect the voltmeter to the base of Q5 and adjust trimmers C22 and C25 for maximum reading. Re-adjust L5 and L6.

Now connect the dummy load to J2, and connect the RF probe across that load. Adjust C22, C25, C28 and the tap positions on both L8 and L9 until no further increase in output can be observed.

If one stage has no output, the cause may be misalignment of (or circuit trouble in) the preceding stage. It may be necessary in some instances to add or subtract a turn from L2, L3, L5, or L6 if proper adjustment cannot be obtained by adjusting turn spacing. If a coil's turns are spread far apart, and the alignment calls for even more spacing, a turn should be removed. On the other hand, if a coil has to be "crushed" together, add a turn. (But make a new coil rather than splicing on more wire.)

Now tune the TV receiver to the transmitter's output frequency. You should see a blank raster when the transmitter is turned on. Connect a video signal to the input jack ( J 1 ) and adjust video level so that a stable picture is obtained. Increase the video level until tearing or rolling develops; that indicates sync-tip compression. Retune the Q4 and Q5 stages for best picture quality without tearing.

Adjust C36 for clearest audio while applying an audio signal to C38. Be careful not to overdeviate or misadjust the 4.5MHz oscillator frequency. A frequency counter can be helpful for that. Connect it to the emitter of Q7 and adjust C36 for a frequency of 4.500 MHz with no video input (short R21 to ground).

That completes alignment and testing of the wireless video link. A very short antenna (about one inch) connected to a 51 ohm resistor makes a good limitedrange antenna. For greater range, a sixinch whip antenna could be used. R-E


That isolating impedance is either an inductor (with a value between 50 - and 500 \(\mu \mathrm{H}\) ) in power-line hybrids, or, as shown in Fig. 9, a resistor (whose value is typically a few tens of ohms) in signal-line applications.

\section*{Designing transient suppressors}

The most severe source of power-line transients is a lightning stroke. Therefore, most suppressors are primarily designed to protect against lightning. Most of the energy associated with a lightning stroke is in the form of common-mode current and voltage. However, due to unequal wiring parameters and the fact that commonmode suppressors are not perfectly matched (do not suppress equally), some differential-mode energy will also be present. Thus, a lightning-protection network should incorporate both commonand differential-mode protection.

Varistors, which are commonly used as suppressors on household AC power lines (117 volts), are available from a number of electronics suppliers. When selecting a varistor for a particular application, you should consider the nominal operating voltages and currents that the circuit will see, and the maximum surge voltage and current that are likely to be encountered. In addition, the suppressor must be rated to withstand the energy that it will need to dissipate. In the case of 117 -volts AC, as stated previously, the most powerful transient that is likely to occur will have a peak voltage of 6000 and a peak current of up to 200 amperes; that's a fairly sizeable wallop. If the suppressor clamped that surge voltage at 500 , the amount of energy that the suppressor would need to absorb would be approximately 0.8 joule. Lower clamping voltages will mean that the suppressor will have to absorb more energy.

In addition to power lines, varistors designed to protect low-voltage circuits (as low as 5 volts) are available. The selection of such a varistor follows the same general procedure as above.

Silicon avalanche suppressors also are commonly used to protect low-voltage systems. For instance, to protect sensitive CMOS devices on a PC board, a 5 -volt SAS could be connected between the power and ground connections on the board. Data lines similarly could be protected by installing SAS's between the lines and ground.

The suitability of an SAS for a particular application is determined by it power rating. Those power ratings range from 500 watts to several kilowatts. An SAS with a power rating of 1000 watts, that is designed to clamp the voltage at 5 , can handle surge currents of up to 200 amperes.

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\section*{SATELLITE-TV RECEIVER}
continued from page 60
At this point you are ready to attach a TV (or a video monitor) and an audio amplifier to the receiver. CAUTION: Disconnect the receiver from the AC voltage source while hooking up everything. After everything is hooked up, reconnect the receiver to the AC voltage and follow these steps:

1-Turn on the receiver and adjust the TRANSPONDER TUNING control to receive a picture. Adjust the SKEw control and the pOLARITY switch for the best picture.
2-Adjust C71 for maximum contrast in the picture. Gently compress and expand L2's coils slightly while observing the picture on several different channels. Adjust L2 for the best picture.

3-Adjust R109 for the best picture. Video level is controlled by that potentiometer, as is contrast. If it is adjusted to too high a value, a buzzing sound may be heard in the audio when lettering appears on the screen.

4-Set the subcarrier tuning control to the center position, and set the bandwidth switch to wide. If no sound is heard, adjust R112 slightly. If nothing but noise is heard, adjust C72 until the audio comes through. That can be a "touchy" adjustment. Get it close, and then try fine-tuning the front-panel control. Once the sound is heard, readjust R112 for best audio.

5-Aim your dish at Satcom F-3, and tune in the appropriate transponder for either WTBS or WGN. Set the bandwIDTH switch to NARROW, and then slowly turn the subcarrier control counterclockwise from center. Several FM-radio programs should be heard. Adjust R111 for the best sound.

6-Adjust R107 for full-scale meter deflection when receiving the strongest station in your area.

7-Trimmers R102 and R104 may need to be adjusted slightly in order to make R103 correspond with the markings on the front panel. Set the transponder tuning control to the number of the lowest transponder channel received in your area and adjust R104 for best reception. Then set the panel control to the number of the highest channel in your area and adjust R102. Those adjustments will interact slightly, so go back and forth until both channels come in correctly.

At this point you can sit down and relax: you've got a fully-functional satellite-TV receiver! We have stuck pretty much to the basics in this article, but if you need more information, be sure to consult the manuals that accompany your LNA, downconverter, feedhorn and dish. Also, see "Installing Your TVRO," which appeared in the June and July 1985 issues of Radio-Electronics.

R-E

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Output \\
Channel
\end{tabular} & \begin{tabular}{c} 
Price \\
Each
\end{tabular} & \begin{tabular}{c} 
TOTAL \\
PRICE
\end{tabular} \\
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\end{tabular} \begin{tabular}{l} 
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CT-90 9 DIGIT 600 MHz COUNTER
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CT-125 9 DIGIT 1.2 GHz COUNTER
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PR-2 COUNTER PREAMP
The PR- 2 is ideal for measuring weak signals
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\hline \begin{tabular}{c} 
SYSTEM A \\
(PIN BDACP100)
\end{tabular} & Base System (See Left) & \(\mathbf{\$ 9 9 5 . 0 0}\) \\
\hline \begin{tabular}{c} 
SYSTEM B \\
(P/N BDACP101)
\end{tabular} & \begin{tabular}{l} 
Base System with additional \\
Floppy Disk Drive
\end{tabular} & \(\mathbf{\$ 1 0 9 9 . 0 0}\) \\
\hline \begin{tabular}{c} 
SYSTEM C \\
(P/N BDACP102)
\end{tabular} & \begin{tabular}{l} 
Base System w/12" Grn Monitor w/Titit \& Swivel \\
base and additional Floppy Drive
\end{tabular} & \(\mathbf{\$ 1 2 9 5 . 0 0}\) \\
\hline \begin{tabular}{c} 
SYSTEM D \\
(PNN BDACP103)
\end{tabular} & \begin{tabular}{l} 
Base System w/RGB Color Monitor w/Tilt \& \\
Swivel base and additional Floppy Drive
\end{tabular} & \(\mathbf{\$ 1 4 9 5 . 0 0}\) \\
\hline \begin{tabular}{l} 
SYSTEM E \\
(FIN BDACP104)
\end{tabular} & \begin{tabular}{l} 
Base System w/10Mb Hard Disk and Green \\
Monitor with Tilt \& Swivel base
\end{tabular} & \(\mathbf{\$ 1 6 9 5 . 0 0}\) \\
\hline \begin{tabular}{l} 
SYSTEM F F \\
(PNN BDACP105)
\end{tabular} & \begin{tabular}{l} 
Base System w/10Mb Hard Disk and RGB Color \\
Monitor with Tilt \& Swivel base
\end{tabular} & \(\mathbf{\$ 1 9 8 5 . 0 0}\) \\
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PC DOS 2.1................. \(\$ 65.00\) GW Basic. .................... 75.00
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Output voltage is adjustable from 0-15V DC, two curren limit range are available for selection: 200 mA or 2 A . An elaborated protection system is specially designed, 'BB' sound and a sparkle light will appear when the output is verloaded.
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as that in valley and musichection and reverberation effect such as that in valley and music hall. It has a 3 section effect control control. Special effect can be made in your record tapes by using this model. All kinds of infield sound effect can be obtained by skilful use of this control. It has LED display to show reflection and reverberation
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Hold: Hold the last input signal
Power supply: OC6V Battery or DC9V 250MA Adaptor Assembled with Tested

TA-477 120W MOSFET POWER AMPLIFIER


This amplifier consists of three super low TIM differential stages, and Hitachi 2SJ49/2SK 134 match pair "MoSFET" as output component whose frequency response and transient esponse is superior to the other power transistor. Therefore this amplifier sounds clear and high fidelity and has superior nalysis over whole Audio spectrum, so it is suitable for repro ducting classic and modern music Kit.
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The heart of this Pre-amplifier is TL-984 IC which contains 4 BI-FIT operational amplifier and has very good transient esponse and low distortion.
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This unit uses the newly developed display driving IC to drive 20 LEDs/channel, it has two display modes: BAR or DOT. Dis so the display is not only beautiful but also red LED, eadability, it can be used in various power amplifier to display peak power level.
This unit already has a rectifier so only needs a transformer (AC \(12 \mathrm{~V} \times 21 \mathrm{~A}\) ) it can also use 12 V CAR battery as power supply. It is simple in construction and easy to build.
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- Uses N.S. INS 8250 BAUD Rat
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A new family of high speed CMOS logic featuring the speed of low power Schottky (8ns typical gate CMOS: very low power consumption, superior noise immunity, and improved output drive. 74HCOO
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\hline \({ }_{74 \text { H/C }}\) & & \({ }_{74 \mathrm{HH}}\) & 89 \\
\hline 7 7 HCO 8 & 59 & \(74 \mathrm{HC157}\) & 39 \\
\hline & 59 & 74 HC & 5 \\
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\hline \(74 \mathrm{HC139}\) & & \(74 \mathrm{HC405}\) & \\
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\hline \(74 \mathrm{HCT02}\) & . 69 & \(74 \mathrm{HCT174}\) & 1.09 \\
\hline \(74 \mathrm{HCT04}\) & . 69 & 74 HCT 193 & 1.39 \\
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\hline \(74 \mathrm{HCT10}\) & . 69 & 74 HCT 240 & 2.19 \\
\hline \(74 \mathrm{HCT11}\) & . 69 & 74 HCT 241 & 2.19 \\
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\hline 74F00 . 69 & \(74 F 74\) & .79 74F2 & \\
\hline \(74 \mathrm{FO2} \quad .69\) & 74586 & \(99 \quad 74525\) & \\
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\hline 74F08 . 69 & 74F139 & 1.69 74F28 & \\
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\hline 4F64 - 89 & 74F244 & \(3.29 \quad 74 F 37\) & \\
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\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{11}{|c|}{DIP CONNECTORS} \\
\hline \multirow[t]{2}{*}{description} & \multirow[t]{2}{*}{order by} & \multicolumn{9}{|c|}{CONTACTS} \\
\hline & & 8 & 14 & 16 & 18 & 20 & 22 & 24 & 28 & 40 \\
\hline HIGH RELAABILITY TOOLED
STIC SOCKETS & AUGATxxST & . 62 & . 79 & . 89 & 1.09 & 1.29 & 1.39 & 1.49 & 1.69 & 2.49 \\
\hline high rellability tooled WW IC SOCKETS & AUGATxxWW & 1.30 & 1.80 & 2.10 & 2.40 & 2.50 & 2.90 & 3.15 & 3.70 & 5.40 \\
\hline COMPONENT CARRIES (DIP HEADERS) & ICCxx & 49 & . 59 & . 69 & . 99 & . 99 & . 99 & . 99 & 1.09 & 1.49 \\
\hline RIBBON CABLE DIP PLUGS (IDC) & IDPxx & --- & . 95 & . 95 & -- & -- & -- & 1.75 & -- & 2.95 \\
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FOR ORDERING INSTRUCTIONS SEE D-SUBMINIATURE BELOW

\(\square\)


AUGAT 24ST
\begin{tabular}{|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{2}{|l|}{\multirow{3}{*}{DESCRIPTION}} & \multirow{3}{*}{ORDER BY} & \multicolumn{6}{|c|}{\multirow[t]{2}{*}{CONTACTS}} \\
\hline & & & & & & & & \\
\hline & & & 9 & 15 & 19 & 25 & 37 & 50 \\
\hline \multirow[t]{2}{*}{SOLDER CUP} & MALE & DBxxP & . 82 & . 90 & 1.25 & 1.25 & 1.80 & 3.48 \\
\hline & FEMALE & DBxxS & . 95 & 1.15 & 1.50 & 1.50 & 2.35 & 4.32 \\
\hline \multirow[t]{2}{*}{RIGHT ANGLE PC SOLDER} & MALE & DBxxPR & 1.20 & 1.49 & --. & 1.95 & 2.65 & -- \\
\hline & FEMALE & DBxxSR & 1.25 & 1.55 & --. & 2.00 & 2.79 & -- \\
\hline \multirow[t]{2}{*}{WIRE WRAP} & MALE & DBxxPWW & 1.69 & 2.56 & \(\cdots\) & 3.89 & 5.60 & \(\cdots\) \\
\hline & FEMALE & DBxxSWW & 2.76 & 4.27 & -- & 6.84 & 9.95 & \(\cdots\) \\
\hline \multirow[t]{2}{*}{\[
\begin{aligned}
& \text { IDC } \\
& \text { RIBBON CABLE }
\end{aligned}
\]} & MALE & IDBxxP & 2.70 & 2.95 & - & 3.98 & 5.70 & --- \\
\hline & FEMALE & IDBxxS & 2.92 & 3.20 & ... & 4.33 & 6.76 & \(\ldots\) \\
\hline \multirow[t]{2}{*}{HOODS} & METAL & MHOODxx & 1.25 & 1.25 & 1.30 & 1.30 & --. & \(\cdots\) \\
\hline & GREY & HOODxx & . 65 & . 65 & -.. & . 65 & . 75 & . 95 \\
\hline \multicolumn{9}{|l|}{\begin{tabular}{l}
ORDERING INSTRUCTIONS: INSERT THE NUMBER OF CONTACTS IN THE POSITION MARKED ' XX " OF THE 'ORDER BY' PART NUMBER LISTED. \\
EXAMPLE: A 15 PIN RIGHT ANGLE MALE PC SOLDER WOULD BE DB15PR.
\end{tabular}} \\
\hline
\end{tabular}


IDC CONMECTORS
\begin{tabular}{|c|c|c|c|c|c|c|c|c|}
\hline \multirow[t]{2}{*}{description} & \multirow[t]{2}{*}{ORDER BY} & \multicolumn{6}{|c|}{CONTACTS} & \multirow[t]{4}{*}{} \\
\hline & & 10 & 20 & 26 & 34 & 40 & 50 & \\
\hline SOLDER HEADER & IDHxxS & . 82 & 1.29 & 1.68 & 2.20 & 2.58 & 3.24 & \\
\hline RIGHT ANGLE SOLDER HEADER & IDHxxSR & . 85 & 1.35 & 1.76 & 2.31 & 2.72 & 3.39 & \\
\hline WW HEADER & IDHxxW & 1.86 & 2.98 & 3.84 & 4.50 & 5.28 & 6.63 & IDS34 \\
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\hline RIBBON HEADER SOCKET & IDSxx & . 79 & . 99 & 1.39 & 1.59 & 1.99 & 2.25 & \\
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\hline \multicolumn{9}{|l|}{FOR ORDEAING INSTRUCTIONS SEE D-SUBMINIATURE ABOVE} \\
\hline
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\begin{tabular}{|c|c|c|c|}
\hline \multicolumn{4}{|l|}{DIODES/OPTO/TRANSISTORS} \\
\hline 1N751 & \({ }_{25}^{25}\) & & \\
\hline \({ }^{1} \mathbf{N 1 4 9 8}\) & 5/1.00 & \({ }^{4 N 28}\) & 9 \\
\hline 00 & & \({ }_{4}^{4 N 33}\) & 9 \\
\hline KBPo4 & 55 & McT-2 & \\
\hline & 95 & McT-6 & 1.29 \\
\hline MDA990-2 & 35 & TLL-111 & \\
\hline \({ }_{\text {¢ }}{ }_{\text {N } 2222222}\) & 25
10 & 2N3906 & 10 \\
\hline 2905 & 50 & 2 N 4402 & 25
25 \\
\hline 2N2907

2N305 & \({ }_{79} 25\) & 2n4403
2N6045 & - 2.25 \\
\hline \({ }^{2 N 3904}\) & 10 & \({ }_{\text {Til }}\) & 1.49 \\
\hline
\end{tabular}


\section*{SWITCHES}
\begin{tabular}{|c|c|c|c|}
\hline \multicolumn{4}{|c|}{SWITCHES} \\
\hline \multicolumn{4}{|l|}{SPST
PINT-TOGGLE
MIN-ON-ON} \\
\hline \multicolumn{4}{|l|}{\multirow[b]{2}{*}{DPDT MINI-TOGGLE ON-OFF-ON 1.75}} \\
\hline & & & \\
\hline \multicolumn{4}{|l|}{SPST MIN1-PUSHBUTTON N.O. . 39} \\
\hline \multicolumn{4}{|l|}{\multirow[t]{2}{*}{\(\begin{array}{lll}\text { SPST } & \text { MINI-PUSHBUTTON N.C. } & .39 \\ \text { SPST }\end{array}\)}} \\
\hline & & & \\
\hline \multicolumn{4}{|l|}{BCD OUTPUT 10 POSITION 6 PIN DIP 1.95} \\
\hline \multicolumn{4}{|c|}{DIP SWITCHES} \\
\hline 4 POSITION 5 POSITION & .85
.90 & 7 Position
8 Position & . 95 \\
\hline \({ }_{6} 5\) Position & . 90 & 10 Position & 1.29 \\
\hline
\end{tabular}

RIBBON CABLE
\begin{tabular}{|c|c|c|c|c|}
\hline \multirow{2}{*}{ CONTACTS } & \multicolumn{2}{|c|}{ SINGLE COLOR } & \multicolumn{2}{|c|}{ COLOR CODED } \\
\cline { 2 - 5 } & 1 & \(10^{\circ}\) & 1 & \(10^{\circ}\) \\
\hline
\end{tabular}
\begin{tabular}{|c|c|c|c|c|}
\hline CONTACTS & \(1^{\prime}\) & \(10^{\prime}\) & \(1^{\prime}\) & \(10^{\prime}\) \\
\hline 10 & .18 & 1.60 & .30 & 2.75 \\
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\end{tabular}

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50 & .05 \\
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\hline
\end{tabular} 100 PCS same value .02
\begin{tabular}{|llrr}
\hline \multicolumn{4}{c|}{ RESISTOR NETWORKS } \\
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\hline \begin{tabular}{c} 
PART \\
NUMBER
\end{tabular} & DIMENSIONS & \begin{tabular}{c} 
DISTRIBUTION \\
STRIP(S)
\end{tabular} & \begin{tabular}{c} 
TIE \\
POINTS
\end{tabular} & \begin{tabular}{c} 
TERMINAL \\
STRIP(S)
\end{tabular} & \begin{tabular}{c} 
TIE \\
POINTS
\end{tabular} & \begin{tabular}{c} 
BINDING \\
POSTS
\end{tabular} & PRICE \\
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